



#### 8-CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

### **Product Summary**

V <sub>BR (Min)</sub>	I <sub>PP (Max)</sub>	C <sub>I/O (Typ)</sub>
5.5V	5	0.55pF

### **Description**

The D3V3F8U9LP3810 is a high-performance device suitable for protecting eight high speed I/Os. This device is assembled in U-DFN3810-9 (Type B) package and has high ESD surge capability, low ESD clamping voltage and ultra-low capacitance.

### **Applications**

Typically used at high-speed ports such as USB 3.0, USB 3.1, Serial ATA, display ports.

### **Features**

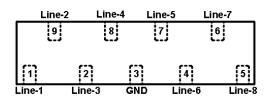
- Clamping Voltage: 5V at 16A TLP
- IEC 61000-4-2 (ESD): Air ±12kV, Contact ±12kV
- IEC 61000-4-5 (Lightning): 5A (8/20µs)
- 8 Channels of ESD Protection
- Ultra-Low Channel Input Capacitance of 0.55pF Typical
- TLP Dynamic Resistance: 0.25Ω
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

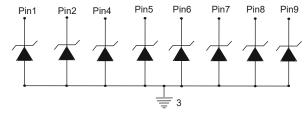
### **Mechanical Data**

- Package: U-DFN3810-9
- Package Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Schematic
- Terminals: Finish NiPdAu, Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.005 grams (Approximate)

#### U-DFN3810-9 (Type B)



Pin Description (Top View)



Device Schematic

### **Ordering Information** (Note 4)

Orderable Part Number	Dookogo	Dookses Marking Book Size (i	Reel Size (inches)	Tape Width (mm)	Packing		
Orderable Part Number	Package	Marking	Reel Size (Iliches)	rape widin (mim)	Qty.	Carrier	
D3V3F8U9LP3810-7	U-DFN3810-9 (Type B)	MW5	7	8	3,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**

U-DFN3810-9 (Type B)

MW5 YM

MW5 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: M = 2025)

M = Month (ex: 9 = September)

Date Code Key

Downloaded from **Arrow.com**.

Year	2017	-	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	Е	-	М	Ν	Р	R	S	Т	U	V	W	Χ
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	IPP	5	Α	I/O to Vss, 8/20µs
Peak Pulse Power, per IEC 61000-4-5	P <sub>PP</sub>	32	W	I/O to Vss, 8/20µs
ESD Protection – Contact Discharge, per IEC 61000-4-2	Vesd_contact	±12	kV	I/O to Vss
ESD Protection – Air Discharge, per IEC 61000-4-2	Vesd_air	±12	kV	I/O to Vss
Operating Temperature	TOP	-55 to +150	°C	_
Storage Temperature	Tstg	-55 to +150	°C	_

# **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	P <sub>D</sub>	350	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	R <sub>θ</sub> JA	360	°C/W

# **Electrical Characteristics** (@TA = +25°C, unless otherwise specified.)

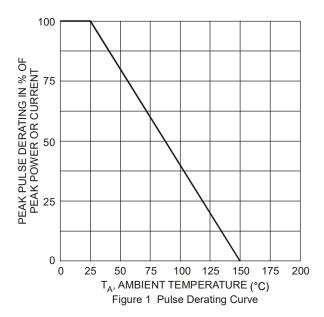
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	VRWM		_	3.3	V	I <sub>R</sub> =1mA, I/O to V <sub>SS</sub>
Reverse Current	I <sub>R</sub>	_	_	1.0	μΑ	$V_R$ = 3.3V, I/O to $V_{SS}$
Reverse Breakdown Voltage	V <sub>BR</sub>	5.5	7.0	_	V	I <sub>R</sub> = 1mA, I/O to V <sub>SS</sub>
Forward Clamping Voltage	VF	-1.0	-0.85	_	V	I <sub>F</sub> = -15mA, I/O to Vss
Holding Reverse Voltage	V <sub>HOLD</sub>	_	1.19	_	V	I/O to V <sub>SS</sub>
Holding Reverse Current	IHOLD	_	90	_	mA	I/O to Vss
Clamping Voltage (Note 6)	Vc	_	5	_	V	TLP, 16A, tp = 100ns, I/O to Vss
Clamping Voltage (Note 6)	Vc	_	5	_	V	TLP, -16A, tp = 100ns, I/O to V <sub>SS</sub>
Dynamic Reverse Resistance	R <sub>DIF-R</sub>	_	0.25	_	Ω	TLP, 10A, tp = 100ns, I/O to Vss
Dynamic Forward Resistance	R <sub>DIF-F</sub>	_	0.2	_	Ω	TLP, 10A, tp = 100ns, V <sub>SS</sub> to I/O
Channel Input Capacitance	C <sub>I/O</sub>	_	0.55	_	pF	V <sub>I/O</sub> = 0, V <sub>SS</sub> = 0, f = 1MHz

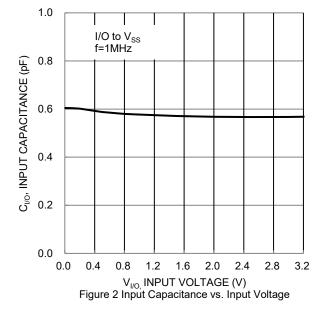
Notes:

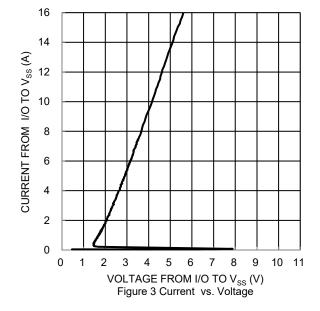
<sup>5.</sup> Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

<sup>6.</sup> Clamping voltage value is based on a TLP model. TLP conditions:  $Z_0=50\Omega$ , tp = 100ns, averaging window; t1=70ns to t2=90ns.







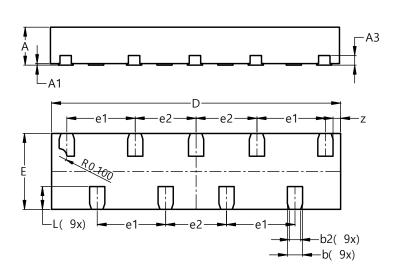




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN3810-9 (Type B)

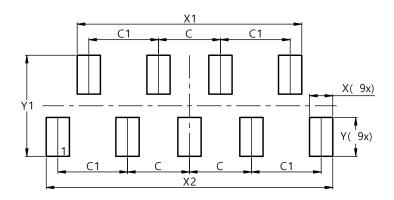


U-DFN3810-9 (Type B)						
Dim	Min	Max	Тур			
Α	0.45	0.55	0.50			
A1	0.00	0.05	0.02			
A3	_	_	0.127			
b	0.15	0.25	0.20			
b2	0.10	0.20	0.15			
D	3.75	3.85	3.80			
Е	0.95	1.05	1.00			
e1			0.90			
e2		_	0.80			
L	0.25	0.35	0.30			
Z		_	0.10			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN3810-9 (Type B)



Dimensions	Value (in mm)		
Dillielisions			
С	0.800		
C1	0.900		
X	0.300		
X1	2.900		
X2	3.700		
Υ	0.500		
Y1	1.300		



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