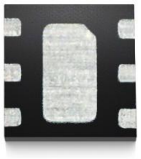
DATA SHEET





GRF6011

SPDT Failsafe Switch 0.01 to 6 GHz

RELEASE 0 DATA SHEET

FEATURES

Path: RFC to RF1: 1.9 GHz, V_{DD} = 5 Volts

- Insertion Loss: 0.64 dB
- IP0.1dB: 31 dBm
- IIP3: 55 dBm at P_{OUT} = 18 dBm
- Failsafe Mode: High Loss

Path: RFC to RF2: 1.9 GHz, V_{DD} = 5 Volts

- Insertion Loss: 0.44 dB
- IP0.1dB: 29 dBm
- IIP3: 54 dBm at P_{OUT} = 18 dBm
- Failsafe Mode: 0.5 dB Loss

APPLICATIONS

- Signal Boosters/Repeaters
- Tower Mounted Amplifiers
- ISM Radios
- Automotive Telematics
- RFID

DESCRIPTION

The GRF6011 is a linear, ultra-low loss SPDT Switch that has been designed with failsafe characteristics when all voltage inputs are removed. In switching mode, the device delivers IP0.1dB levels greater than 1 Watt along with > 55 dBm IIP3 levels for both RF paths.

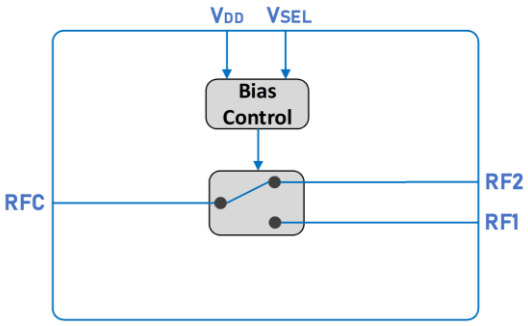
When powered down (failsafe mode) RFC to RF1 defaults to a high insertion loss while RFC to RF2 defaults to a low insertion loss state that retains high linearity.

With optimization of external components, the upper frequency range of the device can be extended to 6 GHz. Data plots using this high frequency tune are also included on the following pages. Optimization for a particular band essentially involves selecting the optimal series capacitor values (M1, M4, M5) for the three RF Ports.

The device is operated from a supply voltage of 3 volts to 5 volts with the single control input (VSELECT) from 3 volts up to V_{DD}.

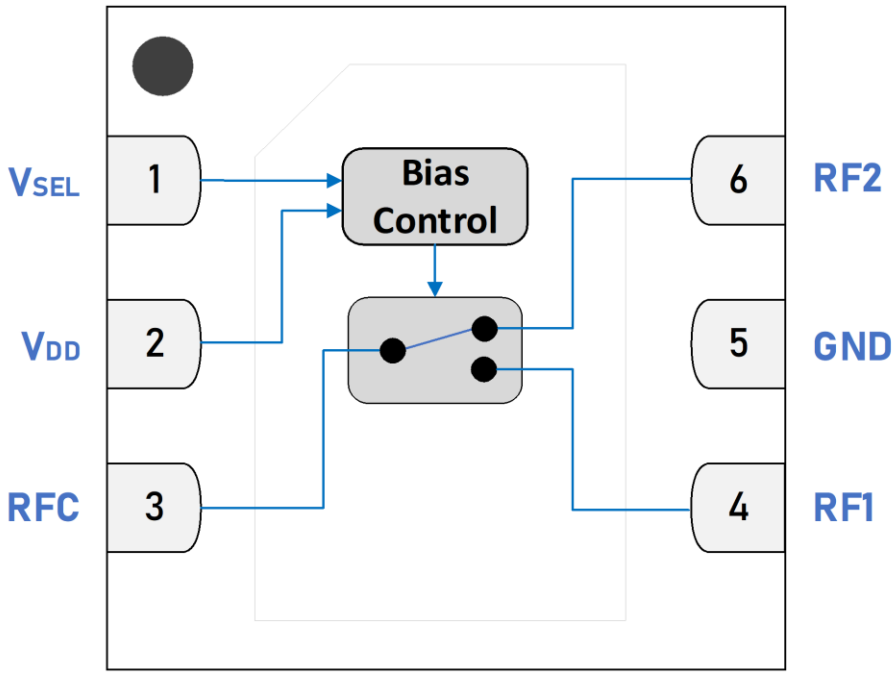
Please consult with the GRF applications engineering team for technical support.

BLOCK DIAGRAM



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DFN-6 1.5 x 1.5 mm Pin Out (Top View)

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Pin Assignments

Pin	Name	Description	Note
1	V_{SELECT}	Switching Control Input	V_{SEL} voltage selects RF path.
2	V_{DD}	Supply Voltage Input	Supply voltage input from 3 to 5 V.
3	RFC	Common RF Path	An external DC blocking capacitor must be used.
4	RF1	RFC to RF1	This path defaults to high insertion loss when all power is removed. An external DC blocking capacitor must be used.
5	GND	Ground	No internal connection to die. We recommend connecting these pins to ground.
6	RF2	RFC to RF2	This path defaults to low insertion loss when all power is removed. An external DC blocking capacitor must be used.
PKG BASE	GND	Ground	Provides DC and RF ground as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.

Power On/Off Sequence (V_{DD}/V_{SEL}/RF)

	V_{DD}	V_{SEL}	RF
POWER ON →	On →	On →	On

	RF	V_{SEL}	V_{DD}
POWER OFF →	Off →	Off →	Off

NOTE: Voltage can be applied to and removed from V_{DD} and V_{SEL} simultaneously.

Control Logic Truth Table

Mode	Description	V_{DD}	V_{SEL}
RFC to RF1	Selects RF1	≥ 3 V	1
RFC to RF2	Selects RF2	≥ 3 V	0
Failsafe	No voltage input (RF2 path selected)	0 V or float	0 V or float
V_{SEL} Logic Level "0"	Logic Low	≥ 3 V	< 0.1 V
V_{SEL} Logic Level "1"	Logic High	≥ 3 V	3 V ≤ V_{SEL} ≤ V_{DD}

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Absolute Ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V_{DD}	0	6	V
RF Input Power (average) VSWR: 1:1	$P_{IN\ MAX}$		39	dBm
Operating Temperature (package base)	$T_{PKG\ BASE}$	-40	115	°C
Maximum Channel Temperature	T_{MAX}		170	°C

Electrostatic Discharge

Human Body Model	HBM	175		V

Storage

Storage Temperature	T_{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	

Caution! ESD Sensitive Device.
Exceeding Absolute Maximum Rating conditions may cause permanent damage.

Note: For additional information, please refer to Manufacturing Note MN-001 — Package and Manufacturing Information.

All Guerrilla RF products are provided in RoHS compliant lead (Pb)-free packaging requiring no exemptions. Additional information for this topic can be found at this link - *Environmental and Restricted Substance Statement Library*

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Recommended Operating Conditions

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Supply Voltage	V_{DD}	3	5	6	V	
RF Frequency Range	F_{TEST}	0.7	1.9	6	GHz	Typical application schematic with external matching components **(notes 1, 2).**
Operating Temperature (package base)	$T_{PKG\ BASE}$	-40		115	°C	
RFIN Port Impedance	Z_{RFIN}		50		Ω	Single-ended.
RFOUT Port Impedance	Z_{RFOUT}		50		Ω	Single-ended.

Note 1: Operation outside this range is possible, but with degraded performance of some parameters.

Note 2: Contact the Guerrilla RF Applications team for guidance on optimizing the tuning of the device for alternative bands.

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Nominal Operating Parameters

The following conditions apply unless noted otherwise: typical application schematic using the 0.7 to 6 GHz tuning set, V_{DD} = 5 V, V_{SEL} = 5 V, I_{DD} = 810 µA, F_{TEST} = 1.9 GHz, 50 Ω system impedance, $T_{PKG\ BASE}$ = 25 °C. Evaluation board losses are included within the specifications.

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Switch Mode: RFC to RF1 Selected						
Loss (Package Device)	Loss_1		0.64		dB	V_{DD} = 5 V, V_{SEL} = 5 V, TA = 25°C.
Isolation: RF1 to RF2	Isol_1_2		24		dB	
Input Power for 0.1 dB Compression	IP0.1dB_1		31		dBm	
Input 3rd Order Intercept Point (P_{OUT} = 18 dBm)	IIP3_1		55		dBm	
Supply Current	I_{DD}		810		µA	
V_{SEL} Current	I_{SELECT}		420		µA	
Switch Mode: RFC to RF2 Selected						
Loss (Package Device)	Loss_2		0.44		dB	V_{DD} = 5 V, V_{SEL} = 0 V, TA = 25°C.
Isolation: RF1 to RF2	Isol_1_2		26		dB	
Input Power for 0.1 dB Compression	IP0.1dB_2		29		dBm	
Input 3rd Order Intercept Point (P_{OUT} = 18 dBm)	IIP3_2		54		dBm	
Supply Current	I_{DD}		140		µA	
V_{SEL} Current	I_{SELECT}		0		µA	
Failsafe Mode: RFC to RF2 (default short)						
Loss	Loss_2		0.5		dB	V_{DD} = 0 V, V_{SEL} = 0 V, TA = 25°C.
Isolation: RF1 to RF2	Isol_1_2		23		dB	
Input Power for 0.1 dB Compression	IP0.1dB_2		19		dBm	
Input 3rd Order Intercept Point (P_{OUT} = 8 dBm)	IIP3_2		48		dBm	
Failsafe Mode: RFC to RF1 (default open)						
Loss	Loss_1		22		dB	V_{DD} = 0 V, V_{SEL} = 0 V, TA = 25°C.
Thermal Data						
Thermal Resistance: (Infrared Scan)	θ_{JC}		TBD		°C/W	On standard evaluation board.

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GRF6011 Switching Times

The following conditions apply unless noted otherwise: typical application schematic using the 0.7 to 6 GHz tuning set, V_{DD} = 5 V, V_{SEL} = 5 V, I_{DD} = 810 µA, F_{TEST} = 1.9 GHz, 50 Ω system impedance, $T_{PKG\ BASE}$ = 25 °C. Evaluation board losses are included within the specifications.

Path	State	Freq (GHz)	V_{DD} (V)	V_{SEL} (V)	PIN (dBm)	Time (ns)
Normal Mode						
RFC to RF1 (50% V_{SEL} to 90% RF1_Out)	On	1.9	5	0 → 5	5	45
RFC to RF1 (50% V_{SEL} to 10% RF1_Out)	Off	1.9	5	5 → 0	5	30
RFC to RF2 (50% V_{SEL} to 90% RF2_Out)	On	1.9	5	5 → 0	5	85
RFC to RF2 (50% V_{SEL} to 10% RF2_Out)	Off	1.9	5	0 → 5	5	70
Failsafe Mode						
Failsafe Off to Failsafe On	On	1.9	5 → 0	5 → 0	5	2500
Failsafe On to Failsafe Off	Off	1.9	0 → 5	0 → 5	5	350

Failsafe Characteristics: V_{DD} = V_{SEL} = 0 V

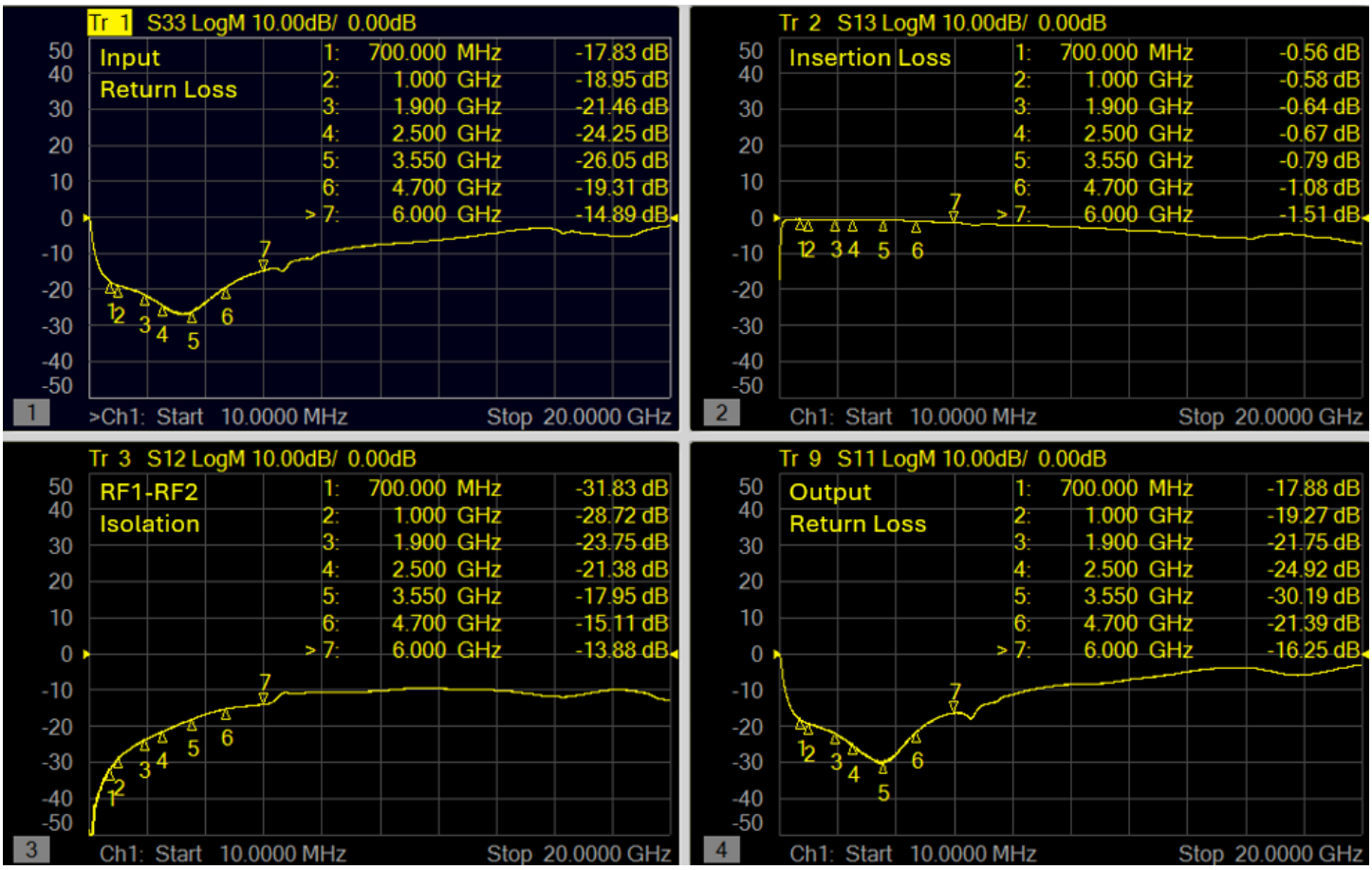
1. RFC to RF1 defaults to high insertion loss state.
2. RFC to RF2 defaults to low insertion loss state.

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GRF6011 Typical Operating Curves: S-Parameters (5 V, 810 µA, 0.7 to 6 GHz)

RFC to RF1 with RF1 Enabled:

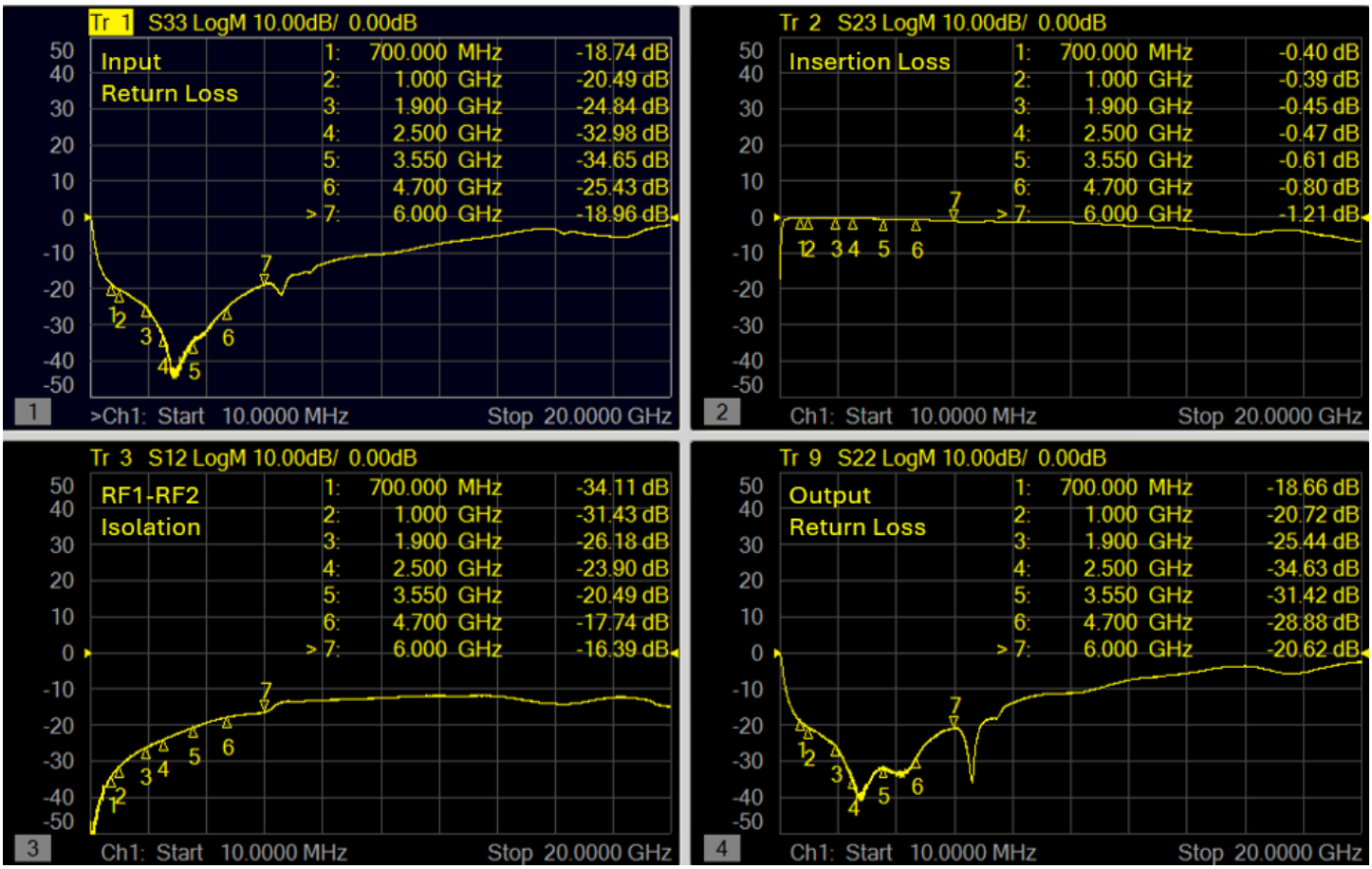


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GRF6011 Typical Operating Curves: S-Parameters (5 V, 140 µA, 0.7 to 6 GHz)

RFC to RF2 with RF2 Enabled:

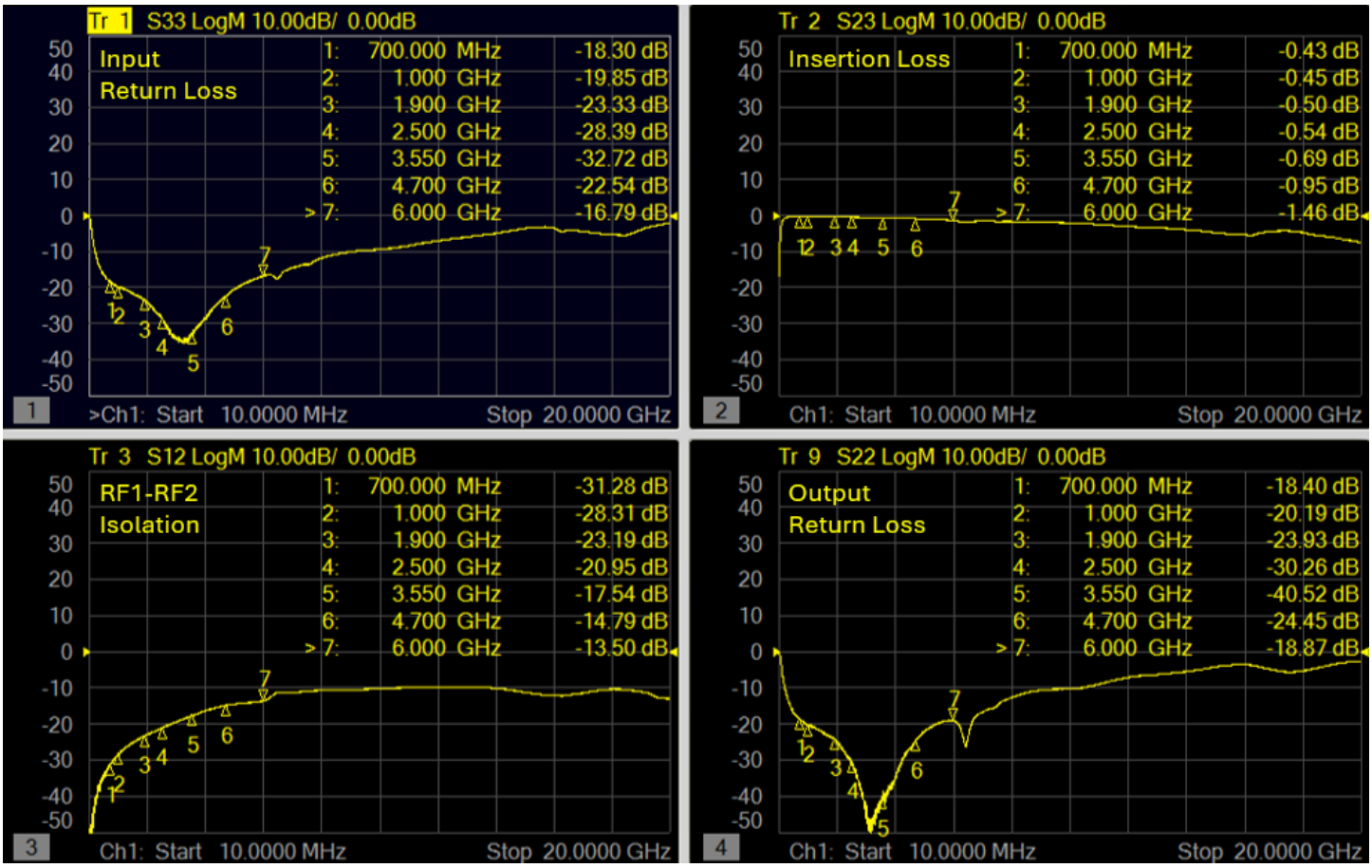


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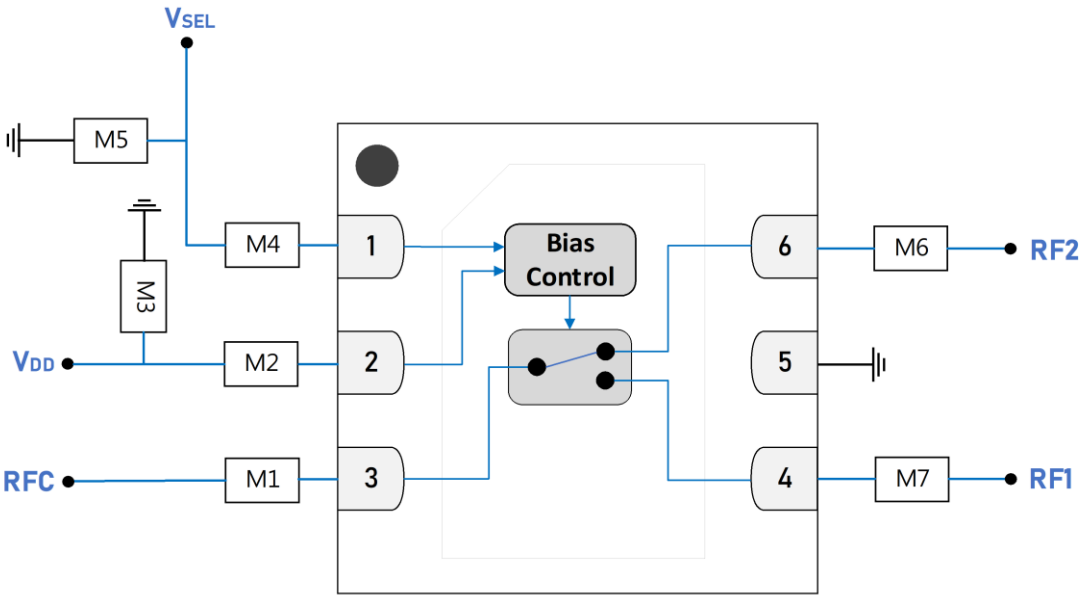
GRF6011 Typical Operating Curves: S-Parameters (0 V, 0 µA, 0.7 to 6 GHz)

RFC to RF2 Failsafe (V_{DD} = V_{SEL} = 0 V):

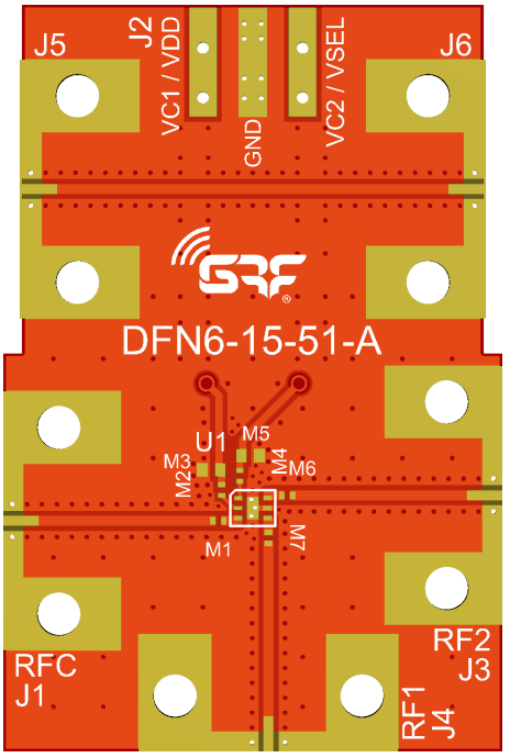


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GRF6011 Standard Evaluation Board Schematic



GRF6011 Evaluation Board Assembly Diagram

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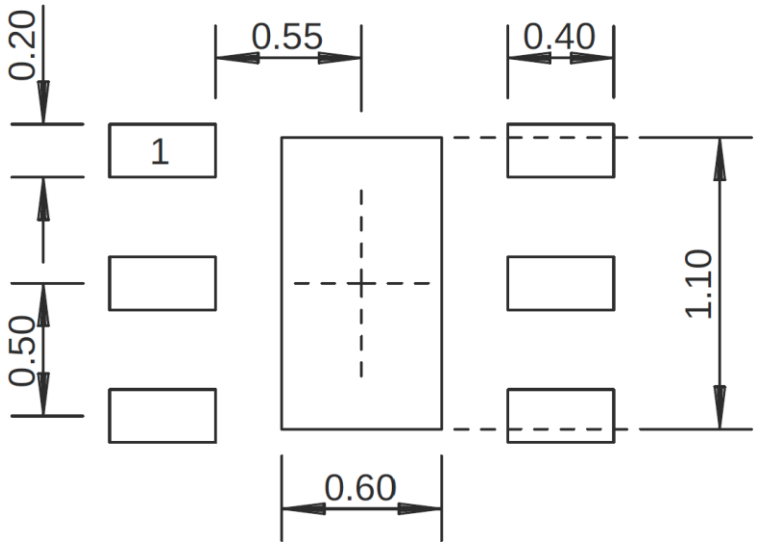
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GRF6011 Standard Evaluation Board Assembly Diagram Reference: 0.7 to 6 GHz Tune

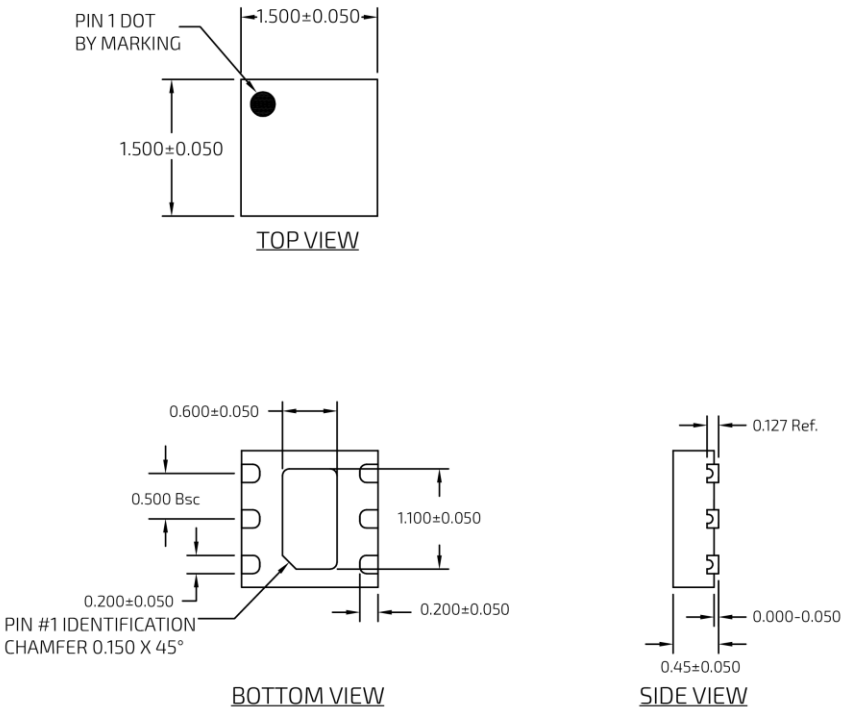
Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Capacitor	Murata	GJM	27 pF	0201	ok
M2	Resistor	Various	5%	0 Ω	0201	ok
M3	Capacitor	Murata	GRM	100 pF	0402	ok
M4	Resistor	Various	5%	0 Ω	0201	ok
M5	Capacitor	Murata	GRM	100 pF	0402	ok
M6	Capacitor	Murata	GJM	27 pF	0201	ok
M7	Capacitor	Murata	GJM	27 pF	0201	ok
Evaluation Board	DFN-15-51-A					

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1.5 x 1.5 mm DFN-6 Suggested PCB Footprint (Top View)

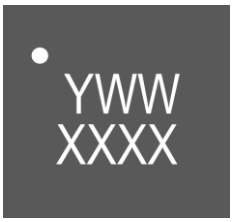


1.5 x 1.5 mm DFN-6 Package Dimensions

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Package Marking Diagram



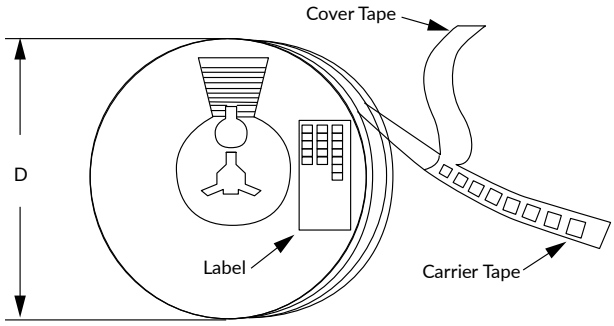
- Line 1: "Y" = YEAR (single digit). "WW" = WEEK the Device was assembled.
- Line 2: "XXXX" = Device Part Number.

Tape and Reel Information

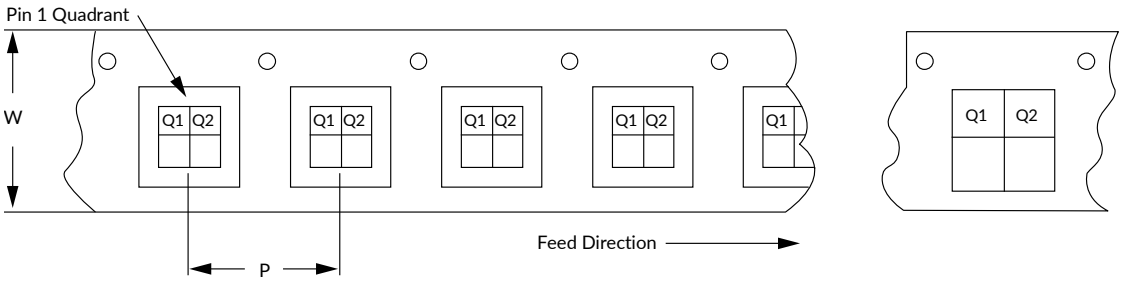
Guerrilla RF's tape and reel specification complies with Electronic Industries Alliance (EIA) standards for "Embossed Carrier Tape of Surface Mount Components for Automatic Handling" (reference EIA-481). See the following page for the Tape and Reel Specification and Device Package Information table, which includes units per reel.

Devices are loaded with pins down into the carrier pocket with protective cover tape and reeled onto a plastic reel. Each reel is packaged in a cardboard box. There are product labels on the reel, the protective ESD bag and the outside surface of the box.

For the Tape and Reel Reference Table, please refer to: *Package Manufacturing Information | Guerrilla RF (guerrilla-rf.com)*



Tape and Reel Packaging with Reel Diameter Noted (D)



Carrier Tape Width (W), Pitch (P), Feed Direction and Pin 1 Quadrant Information

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Revision History

Revision Date	Description of Change
January 13, 2017	Preliminary Data Sheet.
June 23, 2025	Converted Data Sheet to new format only (no change to device or device specifications). Extended lower frequency range from 100 MHz to 10 MHz.
July 23, 2025	Updated IP0.1 dB specifications.
March 4, 2026	Release 0 Data Sheet. Updated 5 volt specifications. Added new evaluation board and plots.
August 17, 2026	Resolved typo in Typ Op Curve caption on p9, now correctly says "RFC to RF2 with RF2 Enabled"

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Datasheet Classifications

Data Sheet Status	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry-supplied transistor S-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on limited evaluation board measurements taken within the Guerrilla RF Applications Lab. All parametric values are subject to change pending the collection of additional data.
Release Ø	All data based on measurements taken with *production-released* material. TYP values are based on a combination of ATE and bench-level measurements, with MIN/MAX limits defined using *modelled estimates* that account for part-to-part variations and expected process spreads. Although unlikely, future refinements to the TYP/MIN/MAX values may be in order as multiple lots are processed through the factory.
Release A-Z	All data based on measurements taken with production-released material *derived from multiple lots which have been fabricated over an extended period of time.* MIN/MAX limits may be refined over previous releases as more statistically significant data is collected to account for process spreads.

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