



Sipeed M1w dock suit (M1w dock + 2.4 inch LCD + OV2640) K210 Dev. Board 1st RV64 AI board for Edge Computing

SKU 110991188



1 Review



Sipeed M1w dock suit (M1w dock + 2.4 inch LCD + OV2640)

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We have released the [Sipeed AI forum area](#), where we will publish relevant resources from time to time. You are welcome to ask questions and communicate here.

Sipeed MAIX: AI at the edge

AI is pervasive today, from consumer to enterprise applications. With the explosive growth of connected devices, combined with a demand for privacy/confidentiality, low latency and bandwidth constraints, AI models trained in the cloud increasingly need to be run at the edge.

MAIX is Sipeed's purpose-built module designed to run AI at the edge, we called it AIoT. It delivers high performance in a small physical and power footprint, enabling the deployment of high-accuracy AI at the edge, and the competitive price make it possible embed to any IoT devices. As you see, Sipeed MAIX is quite like Google edge TPU, but it act as master controller, not an accelerator like edge TPU, so it is more low cost and low power than AP+edge TPU solution.

MAIX's Advantage and Usage Scenarios:

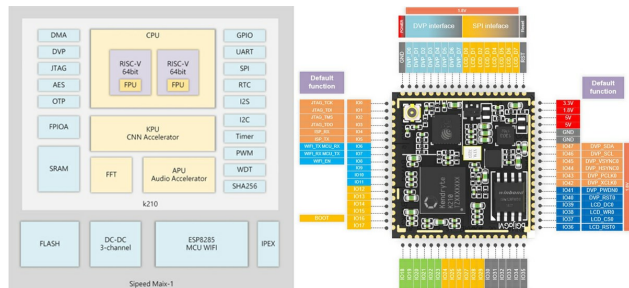
- MAIX is not only hardware, but also provide an end-to-end, hardware + software infrastructure for facilitating the deployment of customers' AI-based solutions.
- Thanks to its performance, small footprint, low power, and low cost, MAIX enables the broad deployment of high-quality AI at the edge.
- MAIX isn't just a hardware solution, it combines custom hardware, open software, and state-of-the-art AI algorithms to provide high-quality, easy to deploy AI solutions for the edge.
- MAIX can be used for a growing number of industrial use-cases such as predictive maintenance, anomaly detection, machine vision, robotics, voice recognition, and many more. It can be used in manufacturing, on-premise, healthcare, retail, smart spaces, transportation, etc.

MAIX's CPU

- In hardware, MAIX have powerful KPU K210 inside, it offers many excited features:
- 1st competitive RISC-V chip, also 1st competitive AI chip, newly release in Sep. 2018
- 28nm process, dual-core RISC-V 64bit IMAFDC, on-chip huge 8MB high-speed SRAM (not for XMR :D), 400MHz frequency (able to 800MHz)
- KPU (Neural Network Processor) inside, 64 KPU which is 576bit width, support convolution kernels, any form of activation function. It offers 0.25TOPS@0.3W,400MHz, when overclock to 800MHz, it offers 0.5TOPS. It means you can do object recognition 60fps@VGA
- APU (Audio Processor) inside, support 8mics, up to 192KHz sample rate, hardware FFT unit inside, easy to make a Mic Array (MAIX offer it too)
- Flexible FPIOA (Field Programmable IO Array), you can map 255 functions to all 48 GPIOs on the chip
- DVP camera and MCU LCD interface, you can connect an DVP camera, run your algorithm, and display on LCD
- Many other accelerators and peripherals: AES Accelerator, SHA256 Accelerator, FFT Accelerator (not APU's one), OTP, UART, WDT, IIC, SPI, I2S, TIMER, RTC, PWM, etc.

MAIX's Module

Inherit the advantage of K210's small footprint, Sipeed MAIX-1 module, or called M1, integrate K210, 3-channel DC-DC power, 8MB/16MB/128MB Flash (M1w module add wifi chip esp8285 on it) into Square Inch Module. All usable IO breaks out as 1.27mm(50mil) pins, and pin's voltage is selectable from 3.3V and 1.8V.



MAIX's development board (M1 dock & M1w dock, w means WiFi version)

Firstly, We make an prototype development board for M1, called M1 dock or Dan Dock, it is simple, small, cheap, but all functions include.

MAix's SoftWare

MAIX support original standalone SDK, FreeRTOS SDK base on C/C++.

And we port micropython on it: <http://cn.maixpy.sipeed.com/>. It support FPIOA, GPIO, TIMER, PWM, Flash, OV2640, LCD, etc. And it have zmodem, vi, SPIFFS on it, you can edit python directly or sz/rz file to board. We are glad to see you contribute for it:

<https://github.com/sipeed/MaixPy> //Maixpy project

https://github.com/sipeed/MaixPy_Doc_Us_En_Backup //Maixpy wiki project

MAix's Deep learning

MAIX support fixed-point model that the mainstream training framework trains, according to specific restriction rules, and have model compiler to compile models to its own model format.

It support tiny-yolo, mobilenet-v1, and, TensorFlow Lite! Many TensorFlow Lite model can be compiled and run on MAIX! And We will soon release model shop, you can trade your model on it.

Technical details

Dimensions	60mm x43mm x5mm
Weight	G.W 30g
Battery	Exclude

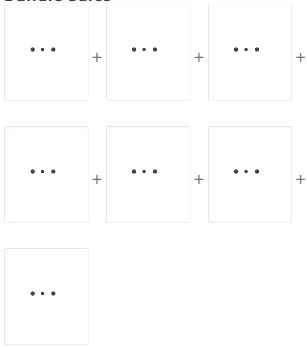
Part List

M1w (w means wifi) dock(mic and pa on board)	1
2.4 Inch LCD	1
OV2640 Camera	1

ECCN/HTS

ECCN	5A002.a.1
HSCODE	8543709990
USHSCODE	85177000
UPC	

Bundle Sales



- ☐ This item: Sipeed M1w dock suit (M1w dock + 2.4 inch LCD + OV2640) K210 Dev. Board 1st RV64 AI board for Edge Computing
- ☐ Sipeed MAix BIT for RISC-V AI+IoT
 - ☐ Sipeed MAix BIT Kit for RISC-V AI+IoT
 - ☐ Sipeed MAix Go Suit for RISC-V AI+IoT
 - ☐ Sipeed 6+1 Microphone Array for Dock/Go/Bit
 - ☐ Sipeed MAIX Binocular Camera for Dock/Go/Bit
 - ☐ Sipeed I2S Microphone for MAIX Dev. Boards

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