

MOSFET – N-Channel, SUPERFET® II

600 V, 7.4 A, 600 mΩ

FCP600N60Z,
FCPF600N60Z

Description

SUPERFET II MOSFET is onsemi's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SUPERFET II MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.

Features

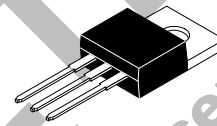
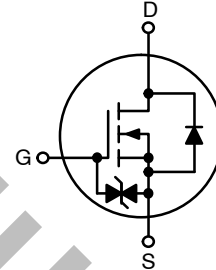
- 650 V @ $T_J = 150^\circ\text{C}$
- Typ. $R_{DS(on)} = 510\text{ m}\Omega$
- Ultra Low Gate Charge (Typ. $Q_g = 20\text{ nC}$)
- Low Effective Output Capacitance (Typ. $C_{oss(eff.)} = 74\text{ pF}$)
- 100% Avalanche Tested
- ESD Improved Capacity
- RoHS Compliant

Applications

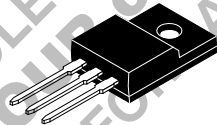
- LCD/LED/PDP TV and Monitor Lighting
- Solar Inverter
- AC-DC Power Supply

V_{DS}	$R_{DS(on)}$ MAX	I_D MAX
600 V	$0.6\ \Omega$ @ 10 V	7.4 A*

*Drain current limited by maximum junction temperature.

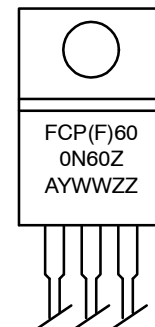


TO-220-3LD
CASE 340AT



TO-220 Fullpack, 3-Lead
/ TO-220F-3SG
CASE 221AT

MARKING DIAGRAM



FCP(F)600N60Z = Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FCP600N60Z	TO-220-3LD	800 Units / Tube
FCPF600N60Z	TO-220 Fullpack	1000 Units / Tube

FCP600N60Z, FCPF600N60Z

MOSFET MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Symbol	Parameter		FCP600N60Z	FCPF600N60Z	Unit
V _{DSS}	Drain to Source Voltage		600		V
V _{GSS}	Gate to Source Voltage	–DC	±20		V
		–AC (f > 1 Hz)	±30		
I _D	Drain Current	– Continuous (T _C = 25°C)	7.4	7.4*	A
		– Continuous (T _C = 100°C)	4.7	4.7*	
I _{DM}	Drain Current	– Pulsed (Note 1)	22.2	22.2*	A
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		135		mJ
I _{AR}	Avalanche Current (Note 1)		1.5		A
E _{AR}	Repetitive Avalanche Energy (Note 1)		0.89		mJ
dv/dt	MOSFET dv/dt		100		V/ns
	Peak Diode Recovery dv/dt (Note 3)		20		
P _D	Power Dissipation	(T _C = 25°C)	89	28	W
		–Derate above 25°C	0.71	0.22	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		–55 to +150		°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300		°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

*Drain current limited by maximum junction temperature.

1. Repetitive rating: pulse–width limited by maximum junction temperature.

2. I_{AS} = 1.5 A, V_{DD} = 50 V, R_G = 25 Ω, starting T_J = 25°C.

3. I_{SD} ≤ 3.7 A, di/dt ≤ 200 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.

THERMAL CHARACTERISTICS

Symbol	Parameter	FCP600N60Z	FCPF600N60Z	Unit
R _{θJC}	Thermal Resistance, Junction to Case, Max.	1.4	4.5	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient, Max.	62.5	62.5	

FCP600N60Z, FCPF600N60Z

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} = 0 V, I _D = 10 mA, T _J = 25°C	600	–	–	V
		V _{GS} = 0 V, I _D = 10 mA, T _J = 150°C	650	–	–	
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25°C	–	0.67	–	V/°C
BV _{DS}	Drain to Source Avalanche Breakdown Voltage	V _{GS} = 0 V, I _D = 7.4 A	–	700	–	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600 V, V _{GS} = 0 V	–	–	1	μA
		V _{DS} = 480 V, T _C = 125°C	–	1.32	–	
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V	–	–	±10	μA

ON CHARACTERISTICS

V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 250 μA	2.5	–	3.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 3.7 A	–	0.51	0.6	Ω
g _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 3.7 A	–	6.7	–	S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	–	840	1120	pF
C _{oss}	Output Capacitance		–	630	840	pF
C _{rss}	Reverse Transfer Capacitance		–	30	45	pF
C _{oss}	Output Capacitance	V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz	–	16.5	–	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 480 V, V _{GS} = 0 V	–	74	–	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 380 V, I _D = 3.7 A, V _{GS} = 10 V (Note 4)	–	20	26	nC
Q _{gs}	Gate to Source Gate Charge		–	3.4	–	nC
Q _{gd}	Gate to Drain “Miller” Charge		–	7.5	–	nC
ESR	Equivalent Series Resistance	f = 1 MHz	–	2.89	–	Ω

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	V _{DD} = 380 V, I _D = 3.7 A, V _{GS} = 10 V, R _G = 4.7 Ω (Note 4)	–	13	36	ns
t _r	Turn-On Rise Time		–	7	24	ns
t _{d(off)}	Turn-Off Delay Time		–	39	88	ns
t _f	Turn-Off Fall Time		–	9	28	ns

DRAIN-SOURCE DIODE CHARACTERISTICS

I _S	Maximum Continuous Drain to Source Diode Forward Current		–	–	7.4	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		–	–	22.2	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 3.7 A	–	–	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 3.7 A, dI _F /dt = 100 A/μs	–	200	–	ns
Q _{rr}	Reverse Recovery Charge		–	2.3	–	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

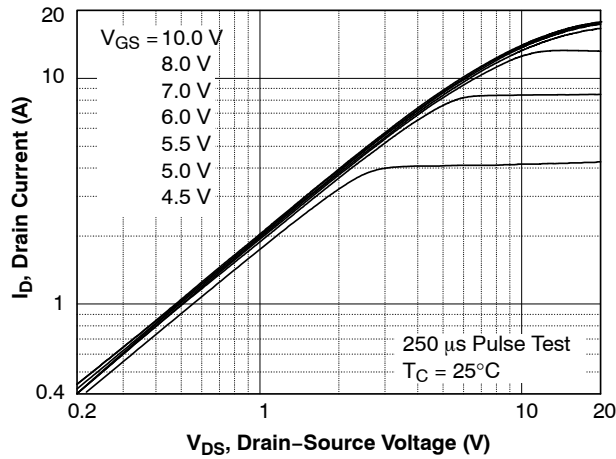


Figure 1. On-Region Characteristics

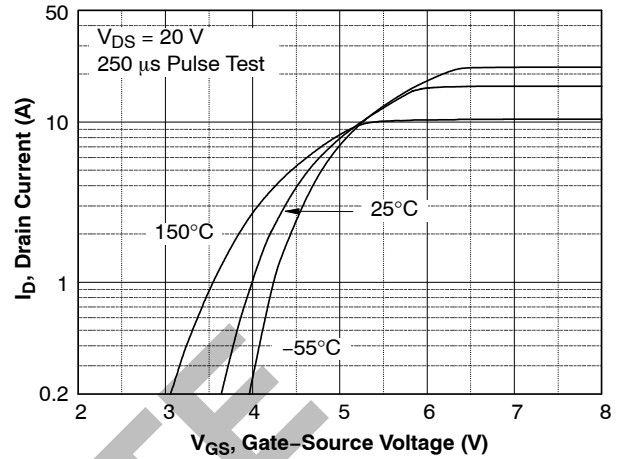


Figure 2. Transfer Characteristics

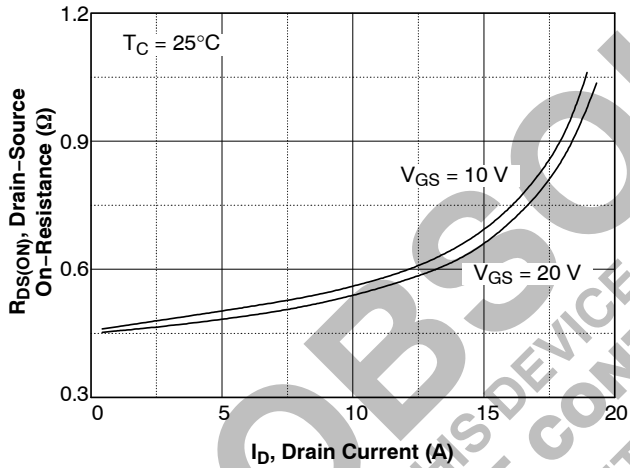


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

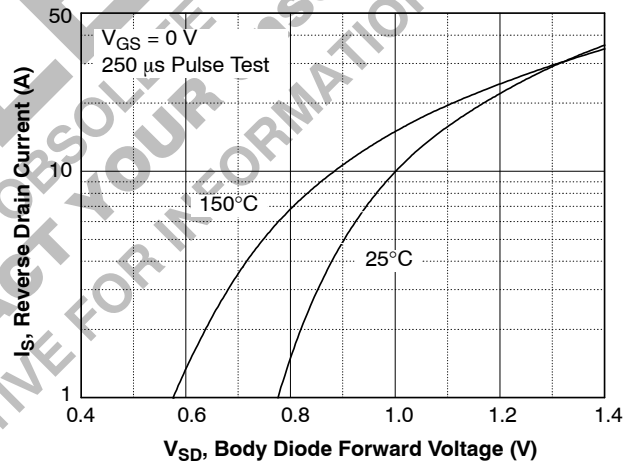


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

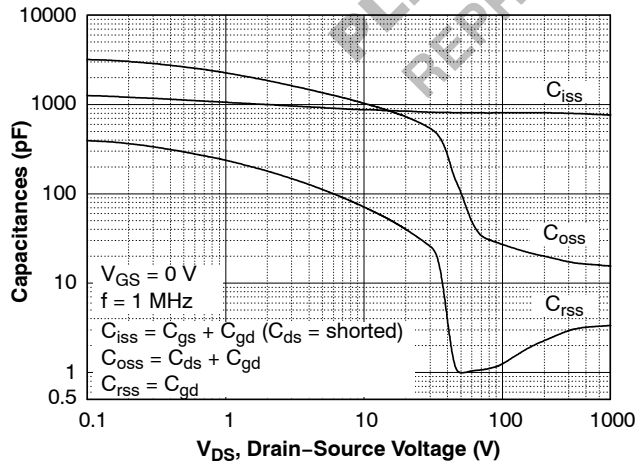


Figure 5. Capacitance Characteristics

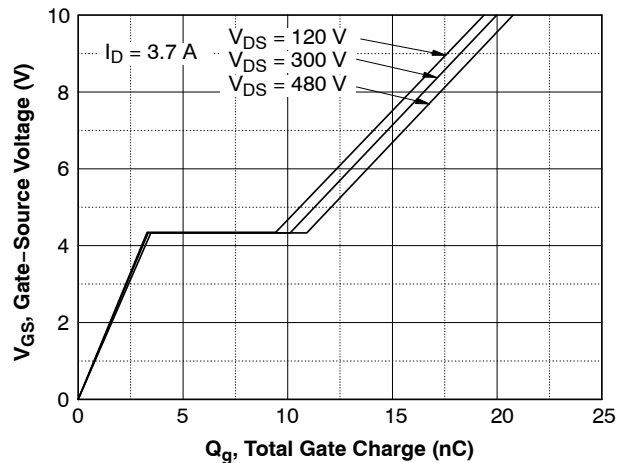


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

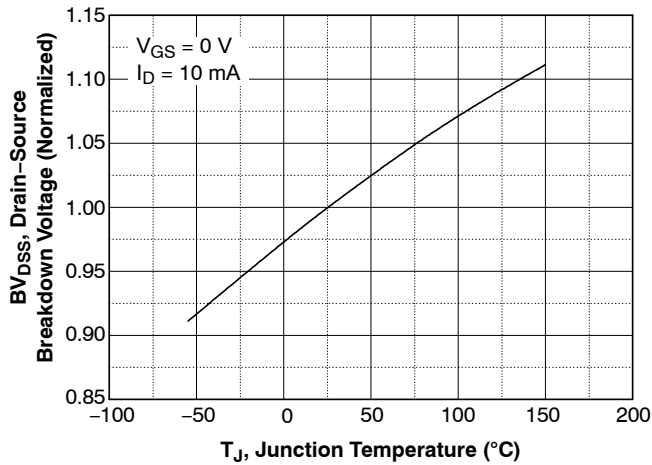


Figure 7. Breakdown Voltage Variation vs. Temperature

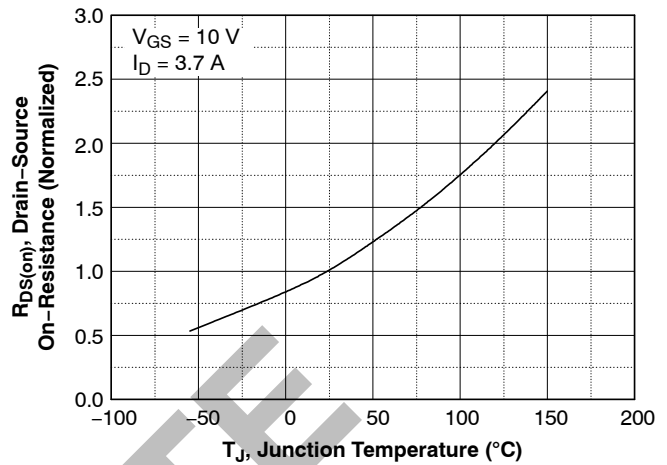


Figure 8. On-Resistance Variation vs. Temperature

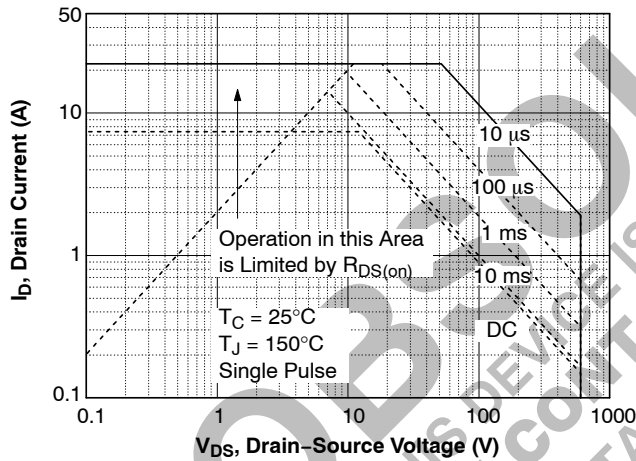


Figure 9. Maximum Safe Operating Area for FCP600N60Z

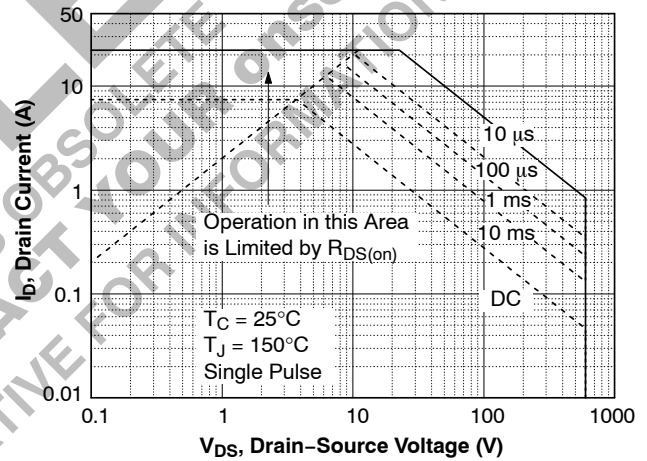


Figure 10. Maximum Safe Operating Area for FCPF600N60Z

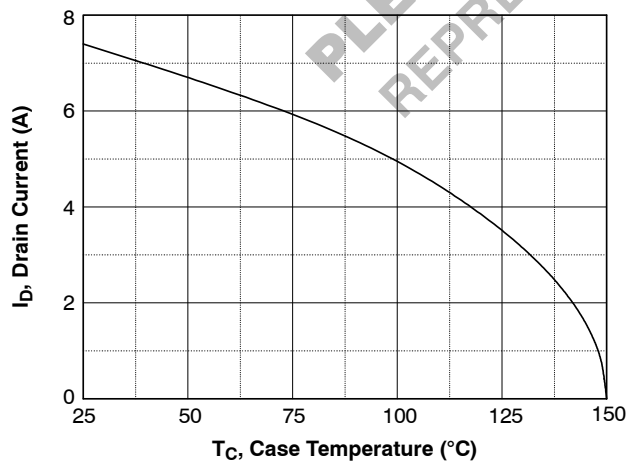


Figure 11. Maximum Drain Current vs. Case Temperature

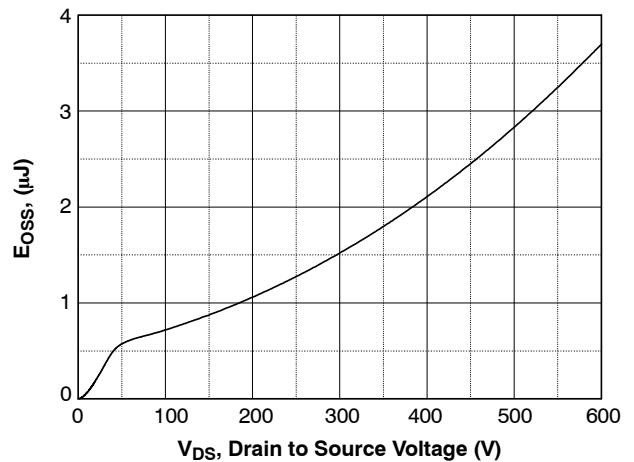


Figure 12. E_{OSS} vs. Drain to Source Voltage

FCP600N60Z, FCPF600N60Z

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

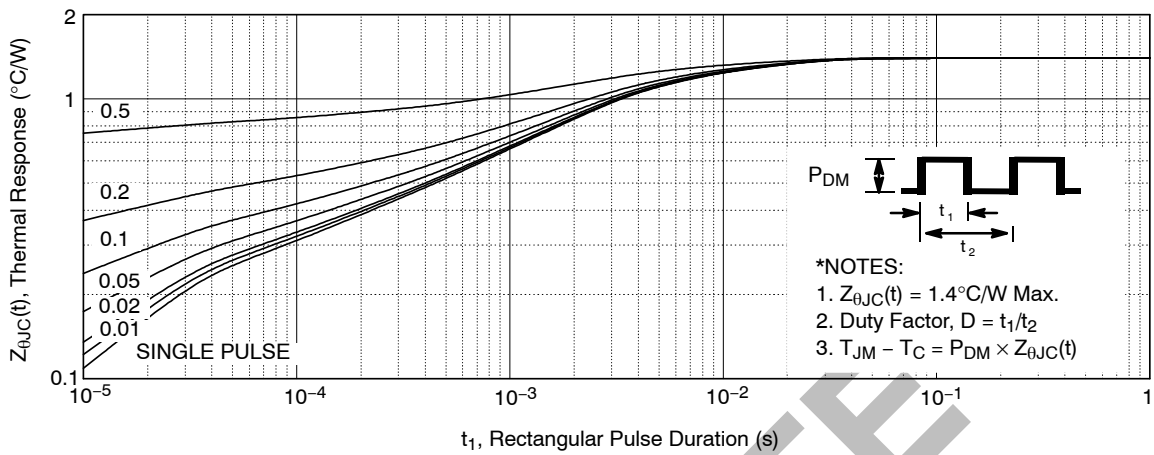


Figure 13. Transient Thermal Response Curve for FCP600N60Z

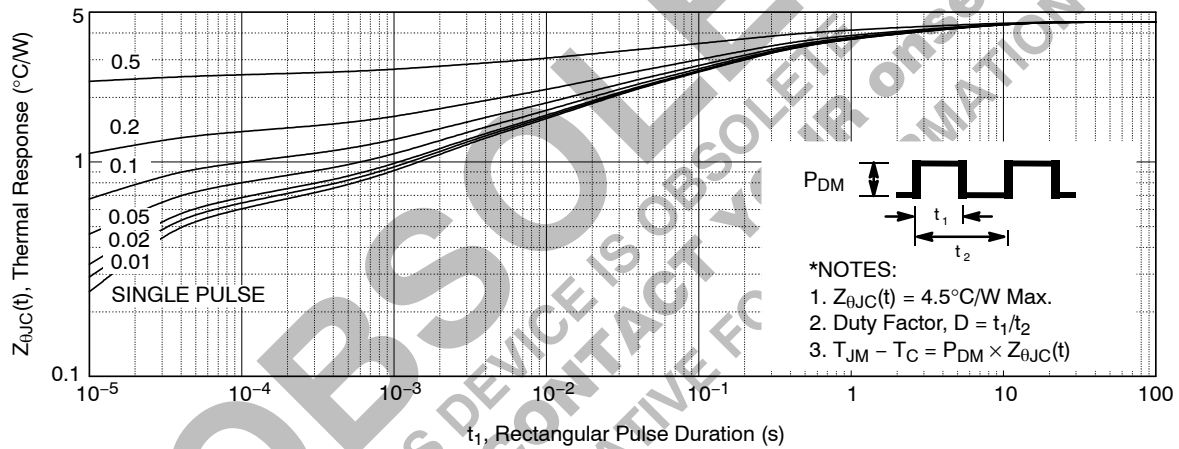


Figure 14. Transient Thermal Response Curve for FCPF600N60Z

FCP600N60Z, FCPF600N60Z

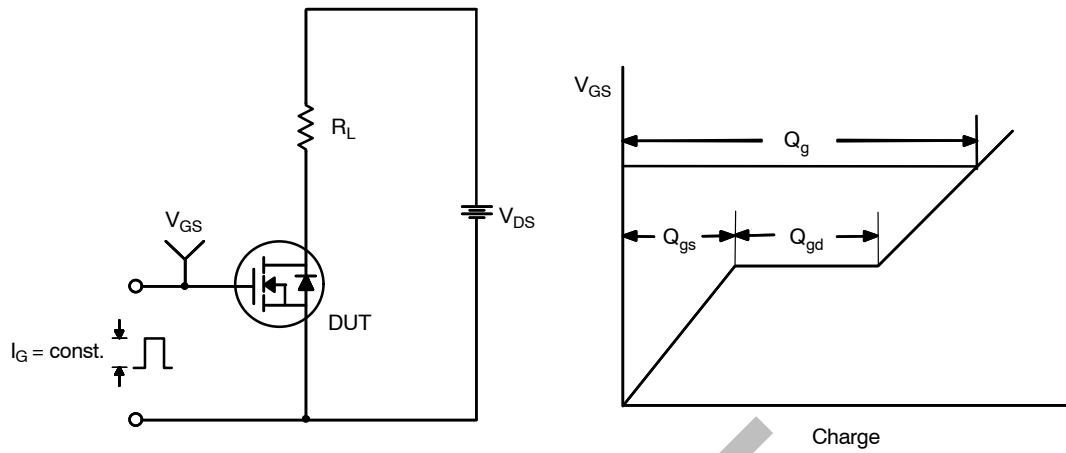


Figure 15. Gate Charge Test Circuit & Waveform

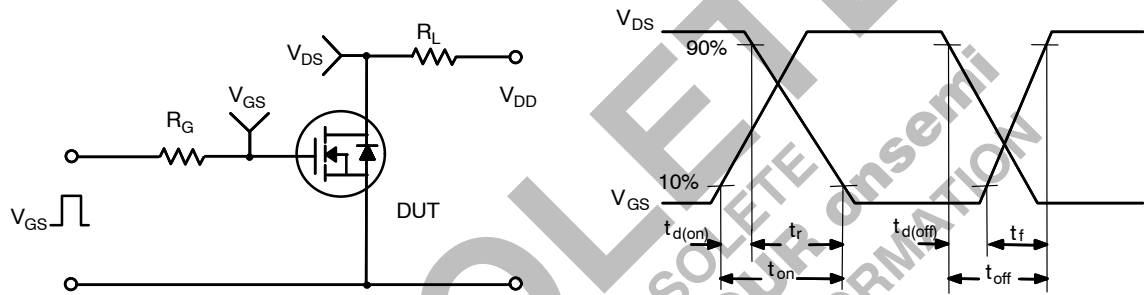


Figure 16. Resistive Switching Test Circuit & Waveforms

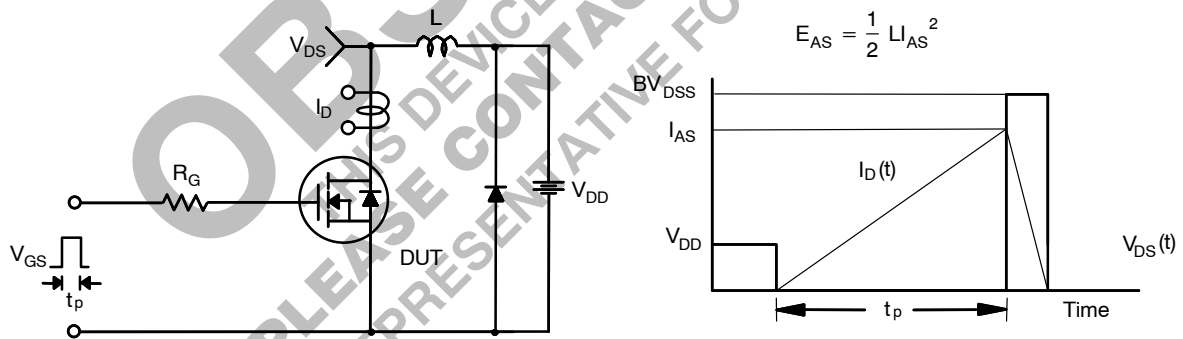


Figure 17. Unclamped Inductive Switching Test Circuit & Waveforms

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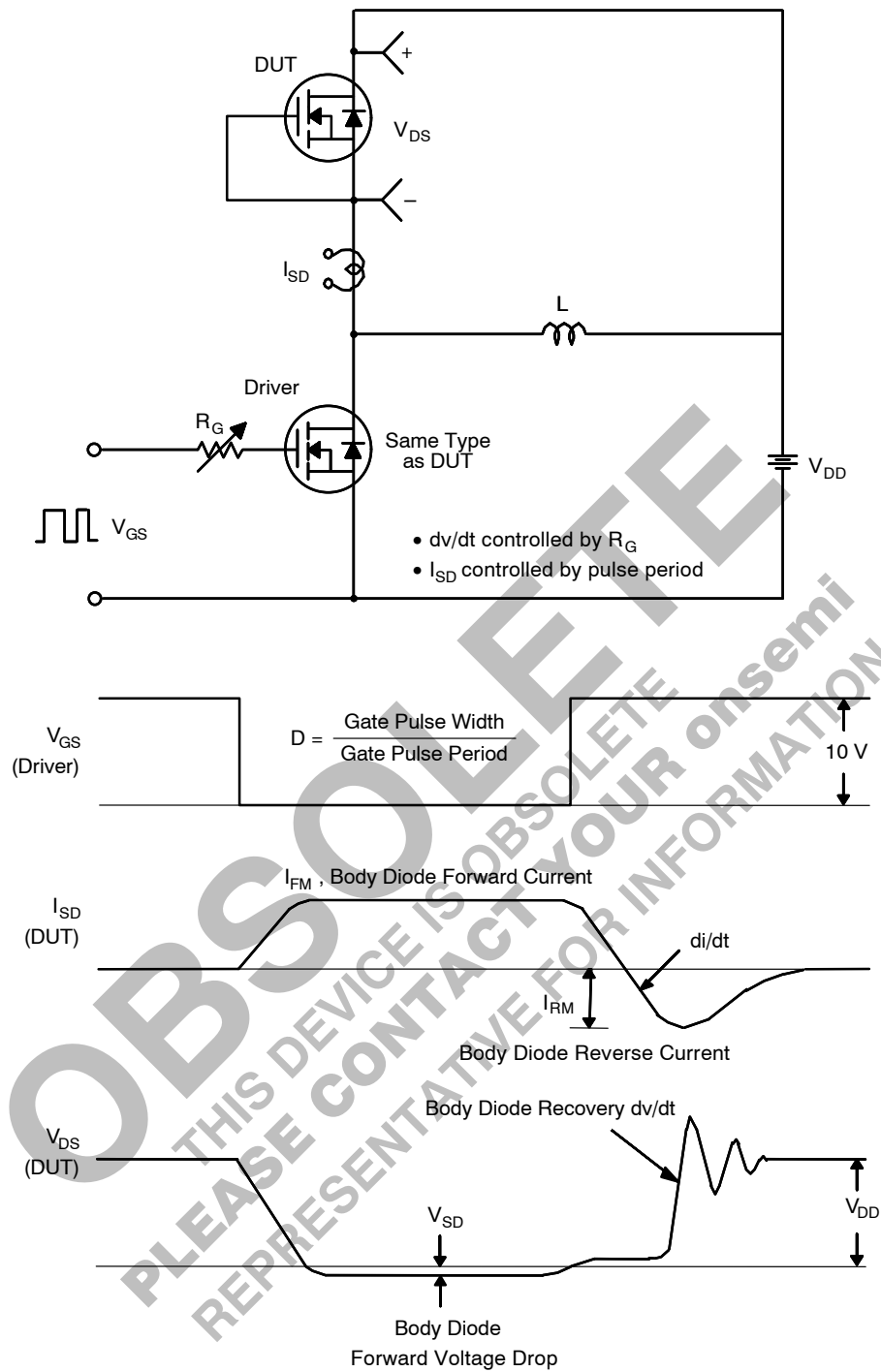


Figure 18. Peak Diode Recovery dv/dt Test Circuit & Waveforms

TO-220 Fullpack, 3-Lead / TO-220F-3SG
CASE 221AT
ISSUE B

DATE 19 JAN 2021



Scale 1:1



OPTION1



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	~	~	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
e	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
L	12.78	12.98	13.18
L1	3.03	3.23	3.43
Ø P	2.98	3.18	3.38
Ø P1	~	1.00	~
Q	3.20	3.30	3.40

NOTES:

A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009

B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCTIONS.

C. OPTION 1 - WITH SUPPORT PIN HOLE

OPTION 2 - NO SUPPORT PIN HOLE

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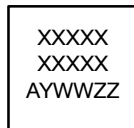
TO-220-3LD CASE 340AT ISSUE B

DATE 08 AUG 2022

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	4.00	--	4.70
A1	SEE NOTE "F"		
A2	2.10	--	2.85
b	0.55	--	1.00
b2	1.10	--	1.62
b4	1.42	--	1.62
c	0.36	--	0.60
D	13.90	--	16.30
D1	8.13	--	9.40
D2	11.50	--	14.30
D3	15.42	--	16.51
E	9.65	--	10.67
E1	7.59	--	8.65
e	2.40	--	2.67
H1	6.06	--	6.69
L	12.70	--	14.04
L1	2.70	--	4.10
P	3.50	--	4.00
Q	2.50	--	3.40
z	2.13 REF		
z1	2.06 REF		
θ	3°	--	5°

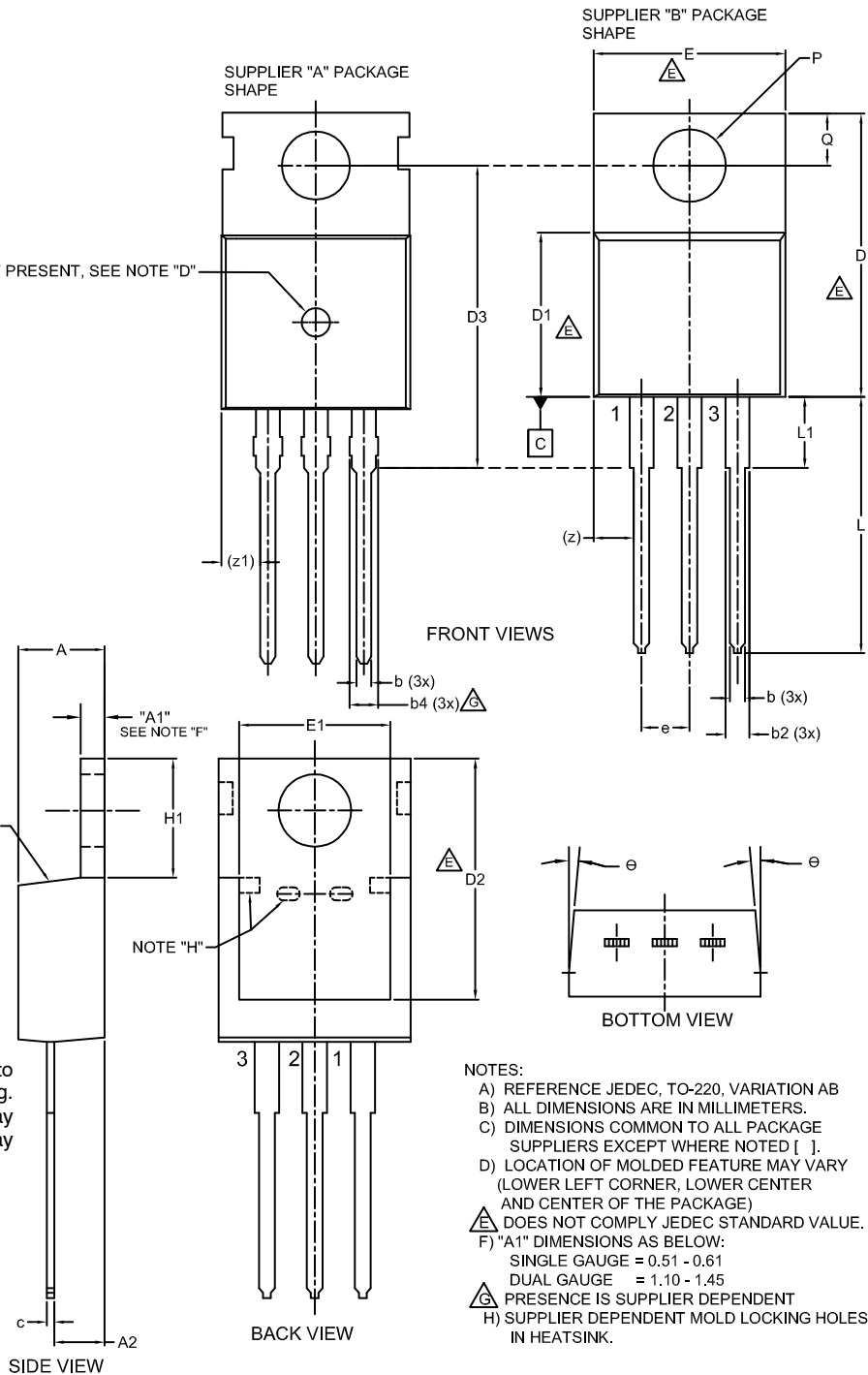
IF PRESENT, SEE NOTE "D"

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



NOTES:

- A) REFERENCE JEDEC, TO-220, VARIATION AB
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS COMMON TO ALL PACKAGE SUPPLIERS EXCEPT WHERE NOTED [].
- D) LOCATION OF MOLDED FEATURE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE)
- △ DOES NOT COMPLY JEDEC STANDARD VALUE.
- F) "A1" DIMENSIONS AS BELOW:
SINGLE GAUGE = 0.51 - 0.61
DUAL GAUGE = 1.10 - 1.45
- △ PRESENCE IS SUPPLIER DEPENDENT
- H) SUPPLIER DEPENDENT MOLD LOCKING HOLES IN HEATSINK.

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