

Data sheet

BAW duplexer Small cell & femtocell LTE band 2

Series/type: B8024

Ordering code: B39192B8024P810

Date: July 19, 2018

Version: 2.4

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RF360 Europe GmbH
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1 Application

- Low-loss BAW RF duplexer for LTE small cell & femtocell systems (Band 2)
- Low insertion attenuation
- High power durability
- Usable pass band: 60 MHz
- Rx = Uplink = 1850 1910 MHz
- Tx = Downlink = 1930 1990 MHz

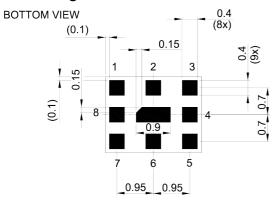
2 Features

- Industrial grade qualified family
- Package size 2.5±0.1 mm × 2.0±0.1 mm
- Package height 0.5 mm (max.)
- Approximate weight 5 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 2a (MSL2a)



Figure 1: Picture of component with example of product marking.

3 Package



4 Pin configuration

1 TX

■ 3 RX

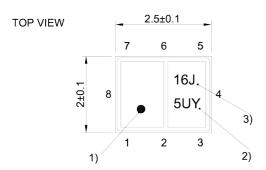
■ 6 ANT

2, 4, 5, 7, Ground 8, 9

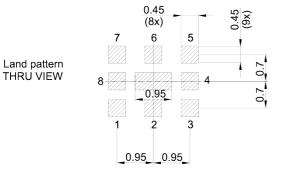
Pad and pitch tolerance ±0.05

SIDE VIEW





- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number



Landing pad tolerance -0.02

Figure 2: Drawing of package with package height A = 0.5 mm (max.). See Sec. Package information (p. 29).

5 Matching circuit

■ L_{p1} = 15 nH

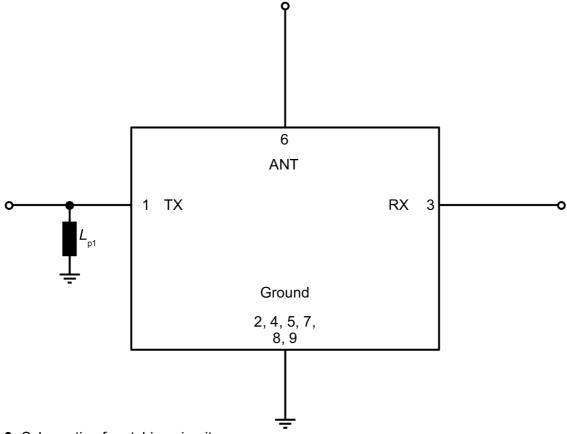


Figure 3: Schematic of matching circuit.

6 Characteristics

6.1 TX - ANT

Temperature range for specification $T_{\text{SPEC}} = -10 \,^{\circ}\text{C} \dots +85 \,^{\circ}\text{C}$ TX terminating impedance $Z_{\text{TX}} = 50 \,\Omega$ with par. 15 nH¹⁾

ANT terminating impedance $Z_{\rm ANT} = 50~\Omega$ RX terminating impedance $Z_{\rm RX} = 50~\Omega$

Characteristics TX – ANT				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{SPEC}} \end{array}$	
Center frequency			f _C				
	1930 1990	MHz		_	1960	_	MHz
Average insertion attenuation			$\alpha_{\text{INT,avg}}^{\qquad 2)}$				
	1930 1935	MHz		_	1.9	2.8	dB
	1935 1990	MHz		_	2.0	2.4	dB
Maximum insertion attenuation			$\boldsymbol{\alpha}_{\text{max}}$				
	1930 1990	MHz		_	2.2	3.3	dB
	1930.24 1989.76	MHz		_	2.2	3.3	dB
	1935 1990	MHz		_	2.2	2.6	dB
Amplitude ripple (p-p)			Δα				
	1930 1990	MHz		_	0.6	2.0	dB
	1935 1990	MHz		_	0.6	1.5	dB
Maximum VSWR			$VSWR_{max}$				
@ TX port	1930 1990	MHz		_	1.6	2.2	
	1935 1950	MHz		_	1.3	2.0	
@ ANT port	1930 1990	MHz		_	1.4	2.3	
	1935 1965	MHz		_	1.3	2.0	
Maximum error vector magnitude			EVM _{max} ³⁾				
	1932.4 1987.6	MHz		_	2.4	3.8	%
Minimum attenuation			α_{min}				
	50 699	MHz		30	32	_	dB
	699 798	MHz		25	31	_	dB
	798 824	MHz		25	31	_	dB
	824 894	MHz		25	30	_	dB
	894 1559	MHz		25	28	_	dB
	1559 1606	MHz		25	28	_	dB
	1606 1710	MHz		25	29	_	dB
	1710 1755	MHz		30	34	_	dB
	1755 1770	MHz		30	37	_	dB
	1770 1850	MHz		35	43	_	dB
	1775 1780	MHz		38	44	_	dB
	1850 1910	MHz		45	50	_	dB
	1850.24 1909.76	MHz		45	50	_	dB
	2010 2110	MHz		3	7	_	dB



Characteristics TX – ANT			$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{SPEC}} \end{array}$	
	2110 2155	MHz	32	36	_	dB
2	2155 2190	MHz	34	37	_	dB
2	2190 2251	MHz	35	38	_	dB
2	2251 2400	MHz	34	40	_	dB
2	2400 2500	MHz	40	46	_	dB
2	2500 2690	MHz	35	50	_	dB
2	2690 3000	MHz	30	54	_	dB
3	3300 3300	MHz	20	39	_	dB
3	3300 3400	MHz	30	52	_	dB
3	3400 3800	MHz	35	52	_	dB
3	8800 5150	MHz	35	42	_	dB
5	5150 5850	MHz	30	39	_	dB

¹⁾

See Sec. Matching circuit (p. 6). Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels. 2)

Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



= -40 °C ... +95 °C Temperature range for specification $T_{ ext{SPEC}}$ TX terminating impedance = 50 Ω with par. 15 nH¹⁾

 Z_{TX} Z_{ANT} Z_{RX} ANT terminating impedance = 50 Ω RX terminating impedance = 50 Ω

Characteristics TX – ANT				$\begin{array}{c} \textbf{min.} \\ \textbf{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	max. for T_{SPEC}	
Center frequency			f _C	SPEC		SPEC	
	1930 1990	MHz	Ü	_	1960	_	MHz
Average insertion attenuation			$\alpha_{\text{INT,avg}}^{\hspace{0.2cm}2)}$				
	1930 1935	MHz	iivi,avg	_	1.9	3.0	dB
	1935 1990	MHz		_	2.0	2.4	dB
Maximum insertion attenuation			$\boldsymbol{\alpha}_{\text{max}}$				
	1930 1990	MHz	IIIdX	_	2.2	3.9	dB
	1930.24 1989.76	MHz		_	2.2	3.9	dB
	1935 1990	MHz		<u> </u>	2.2	2.6	dB
Amplitude ripple (p-p)			Δα				
	1930 1990	MHz		_	0.6	2.6	dB
	1935 1990	MHz		_	0.6	1.5	dB
Maximum VSWR			VSWR _{max}				
@ TX port	1930 1990	MHz	mdx	_	1.6	2.2	
	1935 1950	MHz		_	1.3	2.0	
@ ANT port	1930 1990	MHz		_	1.4	2.3	
© / iii poit	1935 1965	MHz		_	1.3	2.0	
Maximum error vector magnitude			EVM _{max} ³⁾				
•	1932.4 1987.6	MHz	max	_	2.4	3.8	%
Minimum attenuation			$\alpha_{_{min}}$				
	50 699	MHz	min	30	32	<u> </u>	dB
	699 798	MHz		25	31	<u> </u>	dB
	798 824	MHz		25	31	_	dB
	824 894	MHz		25	30	_	dB
	894 1559	MHz		25	28	_	dB
	1559 1606	MHz		25	28	_	dB
	1606 1710	MHz		25	29	l <u> </u>	dB
	1710 1755	MHz		30	34	<u> </u>	dB
	1755 1770	MHz		30	37	l —	dB
	1770 1850	MHz		35	43	<u> </u>	dB
	1775 1780	MHz		38	44	_	dB
	1850 1910	MHz		40	50	_	dB
	1850.24 1909.76	MHz		45	50	_	dB
	2010 2110	MHz		2	7	_	dB
	2110 2155	MHz		32	36	_	dB
	2155 2190	MHz		34	37	_	dB



Characteristics TX – ANT		$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{\tiny SPEC}} \end{array}$	
2190 2251	MHz	35	38	_	dB
2251 2400	MHz	34	40	_	dB
2400 2500	MHz	40	46	_	dB
2500 2690	MHz	35	50	_	dB
2690 3000	MHz	30	54	_	dB
3000 3300	MHz	20	39	_	dB
3300 3400	MHz	30	52	_	dB
3400 3800	MHz	35	52	_	dB
3800 5150	MHz	35	42	_	dB
5150 5850	MHz	30	39	_	dB

See Sec. Matching circuit (p. 6).

Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

³⁾ Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



6.2 ANT - RX

= −10 °C ... +85 °C Temperature range for specification T_{SPEC} TX terminating impedance = 50 Ω with par. 15 nH¹⁾

ANT terminating impedance $= 50 \Omega$ RX terminating impedance = 50Ω

Characteristics ANT – RX				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{\tiny SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{\tiny SPEC}} \end{array}$	
Center frequency			f _C	SPEC	@ * <u>2</u> 0 0	SPEC	
Center requestey	1850 1910	MHz	' c		1880		MHz
Average insertion attenuation	1650 1910	IVII	a 2)	_	1000	_	INIUS
Average insertion attenuation	4050 4040	N 41 1-	$\alpha_{INT,avg}^{\qquad 2)}$		0.0	0.5	
	1850 1910	MHz		_	2.0	2.5	dB
Maximum insertion attenuation			$\boldsymbol{\alpha}_{\text{max}}$				
	1850 1910	MHz		_	2.0	3.0	dB
	1850.24 1909.76	MHz		_	2.0	2.9	dB
Amplitude ripple (p-p)			Δα				
	1850 1910	MHz		_	1.0	2.0	dB
Maximum VSWR			$VSWR_{max}$				
@ ANT port	1850 1910	MHz		_	2.0	2.3	
	1870 1905	MHz		_	1.0	2.0	
@ RX port	1850 1910	MHz		_	1.0	2.3	
	1870 1885	MHz		_	1.0	2.0	
Maximum error vector magnitude			$EVM_{max}^{}3)}$				
	1852.6 1907.6	MHz		_	1.0	3.0	%
Minimum attenuation			$\boldsymbol{\alpha}_{\text{min}}$				
	50 1600	MHz		35	38	_	dB
	1600 1750	MHz		20	26	_	dB
	1750 1810	MHz		20	23	_	dB
	1810 1830	MHz		5	16	_	dB
	1930 1990	MHz		45	51	_	dB
	1930.24 1989.76	MHz		45	51	_	dB
	1990 2110	MHz		30	33	_	dB
	2110 2155	MHz		25	30	_	dB
	2190 2251	MHz		23	26	_	dB
	2400 2500	MHz		17	19	_	dB
	2500 2690	MHz		12	16	_	dB
	2690 3400	MHz		10	13	_	dB
	3400 3800	MHz		10	15	_	dB
	3800 5150	MHz		15	17	_	dB
	5150 6000	MHz		15	22	_	dB

See Sec. Matching circuit (p. 6). Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



Temperature range for specification $T_{\text{SPEC}} = -40 \,^{\circ}\text{C} \dots +95 \,^{\circ}\text{C}$ TX terminating impedance $Z_{\text{TX}} = 50 \,^{\circ}\text{Q}$ with par. 15 nH¹⁾

ANT terminating impedance $Z_{\rm ANT} = 50~\Omega$ RX terminating impedance $Z_{\rm RX} = 50~\Omega$

Characteristics ANT – RX				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{SPEC}} \end{array}$	
Center frequency			f _C				
	1850 1910	MHz		_	1880	_	MHz
Average insertion attenuation			$\alpha_{\text{INT,avg}}^{\qquad 2)}$				
	1850 1910	MHz		_	2.0	2.8	dB
Maximum insertion attenuation			$\boldsymbol{\alpha}_{\text{max}}$				
	1850 1910	MHz		_	2.0	3.3	dB
	1850.24 1909.76	MHz		_	2.0	3.2	dB
Amplitude ripple (p-p)			Δα				
	1850 1910	MHz		_	1.0	2.3	dB
Maximum VSWR			$VSWR_{max}$				
@ ANT port	1850 1910	MHz		_	2.0	2.3	
	1870 1905	MHz		_	1.0	2.0	
@ RX port	1850 1910	MHz		_	1.0	2.3	
	1870 1885	MHz		_	1.0	2.0	
Maximum error vector magnitude			EVM _{max} ³⁾				
	1852.6 1907.6	MHz	max	_	1.0	3.2	%
Minimum attenuation			$\boldsymbol{\alpha}_{\text{min}}$				
	50 1600	MHz		35	38	_	dB
	1600 1750	MHz		20	26	_	dB
	1750 1810	MHz		20	23	_	dB
	1810 1830	MHz		5	16	_	dB
	1930 1990	MHz		40	51	_	dB
	1930.24 1989.76	MHz		40	51	_	dB
	1990 2110	MHz		30	33	_	dB
	2110 2155	MHz		25	30	_	dB
	2190 2251	MHz		23	26	_	dB
	2400 2500	MHz		17	19	_	dB
	2500 2690	MHz		12	16	_	dB
	2690 3400	MHz		10	13	_	dB
	3400 3800	MHz		10	15	_	dB
	3800 5150	MHz		15	17	_	dB
	5150 6000	MHz		15	22	_	dB

¹⁾ See Sec. Matching circuit (p. 6).

Integrated attenuation α_{INT} : Averaged power $|S_{ij}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.

Error Vector Magnitude (EVM) based on definition in 3GPP TS 25.141.



6.3 TX - RX

Temperature range for specification $T_{\rm SPEC} = -10~{\rm ^{\circ}C}~...~+85~{\rm ^{\circ}C}$ TX terminating impedance $Z_{\rm TX} = 50~\Omega$ with par. 15 nH¹⁾

ANT terminating impedance $Z_{\text{ANT}} = 50 \ \Omega$ RX terminating impedance $Z_{\text{RX}} = 50 \ \Omega$

Characteristics TX - RX				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{\tiny SPEC}} \end{array}$	
Average isolation			α _{INT,avg} ²⁾				
	1850 1910	MHz		50	53	_	dB
	1930 1990	MHz		52	58	_	dB
Minimum isolation			α_{min}				
	1850 1910	MHz		48	52	_	dB
	1850.24 1909.76	MHz		48	52	_	dB
	1930 1990	MHz		50	57	_	dB
	1930.24 1989.76	MHz		50	57	_	dB

¹⁾ See Sec. Matching circuit (p. 6).

Integrated attenuation α_{INT} : Averaged power $|S_{ii}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.



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Temperature range for specification $T_{\text{SPEC}} = -40 \,^{\circ}\text{C} \,... +95 \,^{\circ}\text{C}$ TX terminating impedance $Z_{\text{TX}} = 50 \,^{\circ}\text{Q}$ with par. 15 nH¹⁾

ANT terminating impedance $Z_{\rm ANT} = 50~\Omega$ RX terminating impedance $Z_{\rm RX} = 50~\Omega$

Characteristics TX – RX				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{\tiny SPEC}} \end{array}$	
Average isolation			α _{INT,avg} ²⁾				
	1850 1910	MHz		50	53	<u> </u>	dB
	1930 1990	MHz		51	58	_	dB
Minimum isolation			$\alpha_{_{min}}$				
	1850 1910	MHz		44	52	<u> </u>	dB
	1850.24 1909.76	MHz		47	52	<u> </u>	dB
	1930 1990	MHz		43	57	_	dB
	1930.24 1989.76	MHz		43	57	_	dB

¹⁾ See Sec. Matching circuit (p. 6).

Integrated attenuation α_{INT} : Averaged power $|S_{ii}|^2$ over the center 4.5 MHz of LTE 5 MHz (25 RB) channels.



7 **Maximum ratings**

Operable temperature	T _{OP} = −40 °C +95 °C	
Storage temperature	T _{STG} ¹⁾ = −40 °C +95 °C	
DC voltage	$ V_{DC} ^{2)} = 0 \text{ V (max.)}$	
ESD voltage		
	$V_{ESD}^{3)} = 250 \text{ V (max.)}$	Human body model.
	$V_{ESD}^{4)} = 150 \text{ V (max.)}$	Machine model.
Input power	P _{IN}	
@ TX port: 1930 1990 MHz	30 dBm	5 MHz LTE downlink signal (25 RB) for 100000 h @ 55 °C. P _{IN} 30dBm average,
		41dBm peak. Source and load impedance 50Ω. ⁵⁾
@ TX port: other frequency ranges	10 dBm	Source and load impedance 50Ω .
Operating lifetime with output power at antenna 1930 1990 MHz	P _{OUT} 6) = 24 dBm	Continuous wave for 100000 h @ 55 °C.

Not valid for packaging material. Storage temperature for packaging material is -25 °C to +40 °C.

²⁾

In case of applied DC voltage blocking capacitors are mandatory.

According to JESD22-A114F (HBM – Human Body Model), 1 negative & 1 positive pulse.

According to JESD22-A115B (MM – Machine Model), 10 negative & 10 positive pulses. 3)

Expected lifetime according to accelerated power durability measurement, and wear out models.

According to accelerated high temperature operating life (HTOL) test.

8 Transmission coefficients

8.1 TX - ANT 0.0 1.0 2.0 1.990 .143 3.0 4.0 5.0 1920 1960 1980 2000 1940 f/MHz0.0 20.0 40.0 60.0 80.0 <u>+</u> 1750 1800 1850 1900 2000 1950 2050 2100 f/MHz 0.0 20.0 40.0 60.0 80.0 1000 2000 3000 4000 5000 6000

Figure 4: Attenuation TX – ANT.

f/MHz

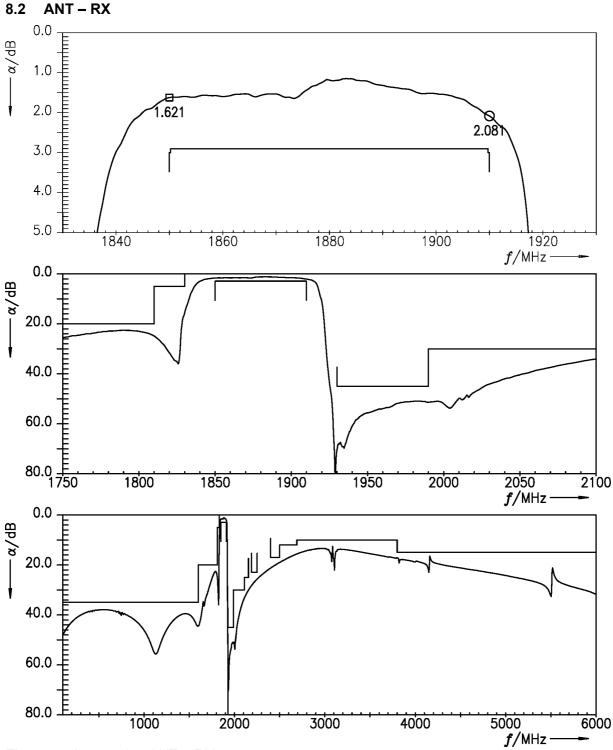


Figure 5: Attenuation ANT – RX.

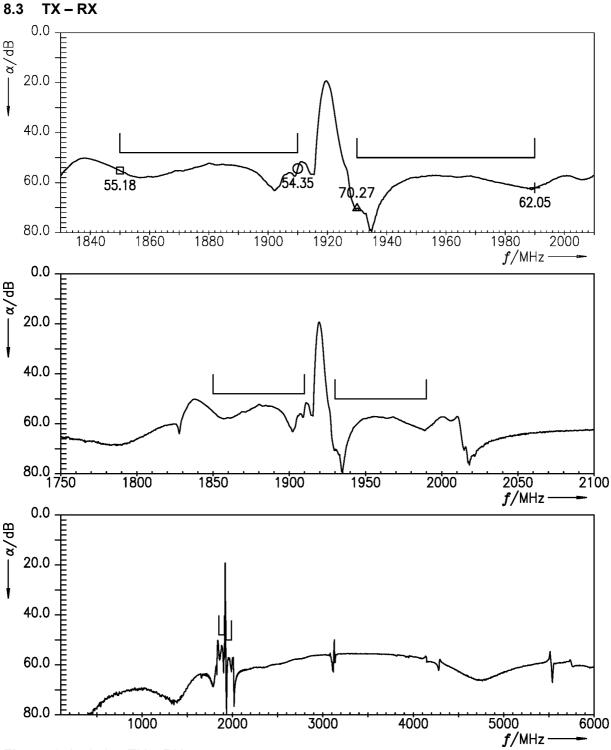


Figure 6: Isolation TX – RX.

9 Reflection coefficients

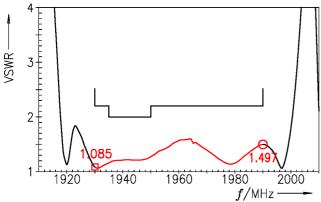
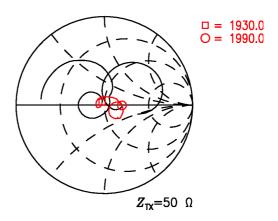


Figure 7: Reflection coefficient at TX port.



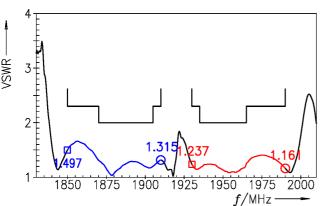
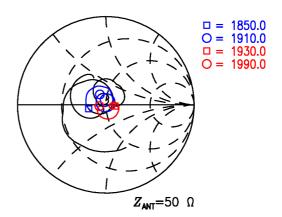


Figure 8: Reflection coefficient at ANT port.



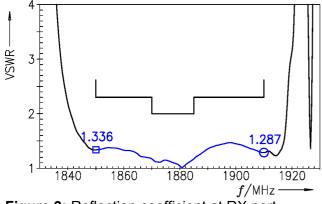
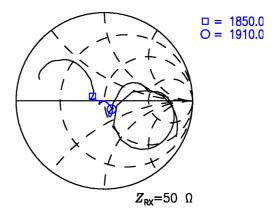


Figure 9: Reflection coefficient at RX port.



10 EVMs

10.1 TX - ANT 4 2 1.10 1.10 0.385 0 f/MHz

Figure 10: Error vector magnitude TX – ANT.

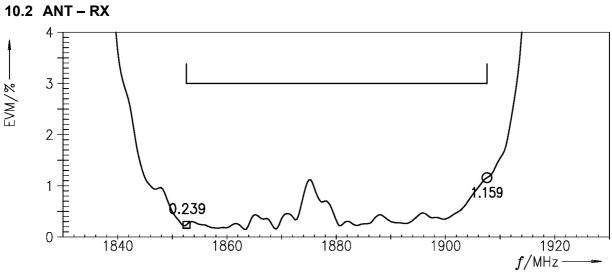


Figure 11: Error vector magnitude ANT – RX.

11 Packing material

11.1 Tape

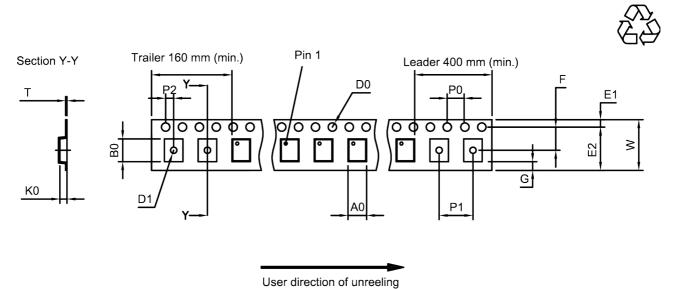


Figure 12: Drawing of tape (first-angle projection) with tape dimensions according to Table 1.

A_0	2.25±0.05 mm	E ₂	6.25 mm (min.)	P ₁	4.0±0.1 mm
B ₀	2.75±0.05 mm	F	3.5±0.05 mm	P ₂	2.0±0.05 mm
D_0	1.5+0.1/-0 mm	G	0.75 mm (min.)	Т	0.25±0.03 mm
D ₁	1.0 mm (min.)	K_0	0.6±0.05 mm	W	8.0+0.3/-0.1 mm
E ₁	1.75 _{±0.1} mm	P ₀	4.0±0.1 mm		

Table 1: Tape dimensions.

11.2 Reel with diameter of 180 mm

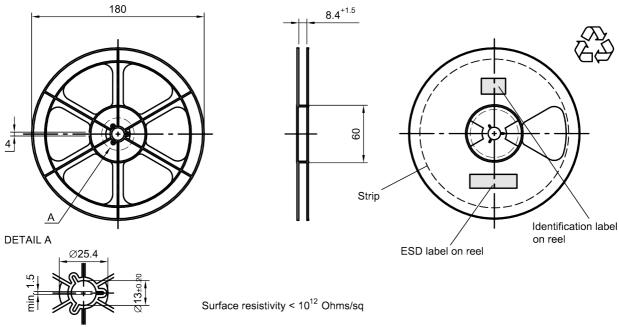


Figure 13: Drawing of reel (first-angle projection) with diameter of 180 mm.

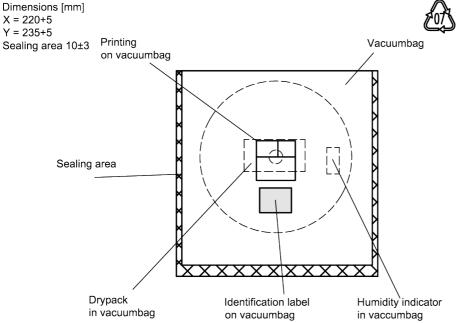


Figure 14: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

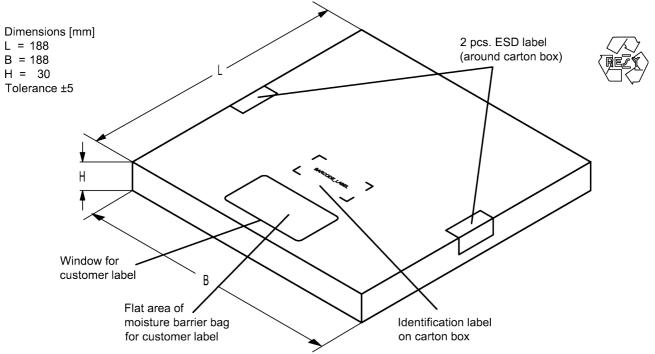


Figure 15: Drawing of folding box for reel with diameter of 180 mm.

11.3 Reel with diameter of 330 mm

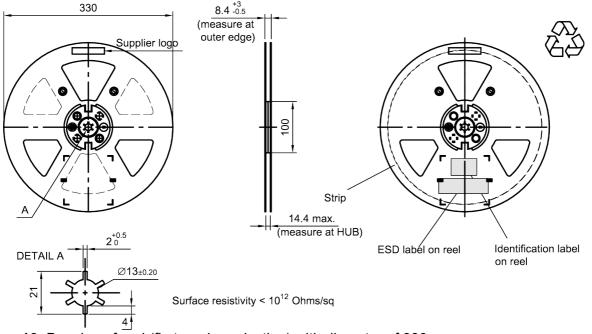


Figure 16: Drawing of reel (first-angle projection) with diameter of 330 mm.

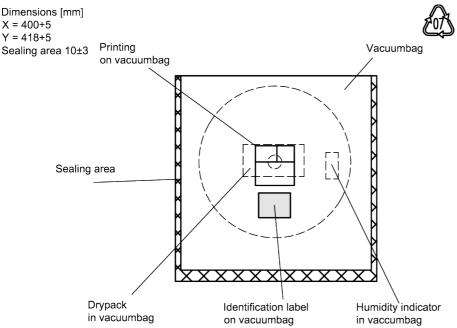


Figure 17: Drawing of moisture barrier bag (MBB) for reel with diameter of 330 mm.

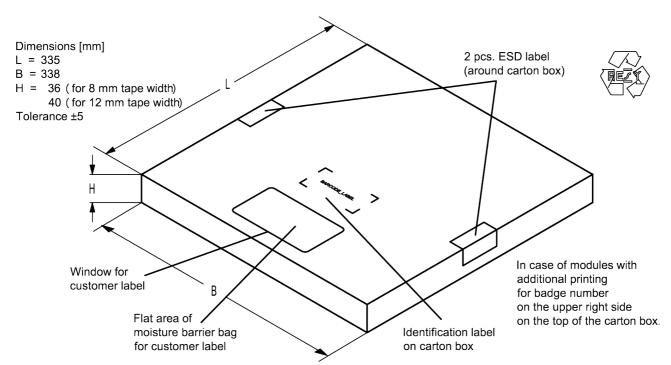


Figure 18: Drawing of folding box for reel with diameter of 330 mm.

12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

■ Type number:

The 4 digit type number of the ordering code, e.g., B3xxxxB**1234**xxxx, is encoded by a special BASE32 code into a 3 digit marking.

Example of decoding type number marking on device in decimal code.

16J => 1234 1 x 32^2 + 6 x 32^1 + 18 (=J) x 32^0 = 1234

The BASE32 code for product type B8024 is 7TR.

■ Lot number:

The last 5 digits of the lot number, e.g., are encoded based on a special BASE47 code into a 3 digit marking.

Example of decoding lot number marking on device in decimal code.

5UY => 12345 $5 \times 47^2 + 27 (=U) \times 47^1 + 31 (=Y) \times 47^0 =$ 12345

Adopted BASE32 code for type number							
Decimal	Base32	Decimal	Base32				
value	code	value	code				
0	0	16	G				
1	1	17	Н				
2	2	18	J				
3	3	19	K				
4	4	20	M				
5	5	21	N				
6	6	22	Р				
7	7	23	Q				
8	8	24	R				
9	9	25	S				
10	Α	26	Т				
11	В	27	V				
12	С	28	W				
13	D	29	X				
14	E	30	Y				
15	F	31	Z				

Adopted BASE47 code for lot number				
Decimal	Base47	Decimal	Base47	
value	code	value	code	
0	0	24	R	
1	1	25	S	
2	2	26	Т	
3	3	27	U	
4	4	28	V	
5	5	29	W	
6	6	30	Х	
7	7	31	Y	
8	8	32	Z	
9	9	33	b	
10	Α	34	d	
11	В	35	f	
12	С	36	h	
13	D	37	n	
14	E	38	r	
15	F	39	t	
16	G	40	V	
17	Н	41	\	
18	J	42	?	
19	K	43	{	
20	L	44	}	
21	M	45	<	
22	N	46	>	
23	Р			

Table 2: Lists for encoding and decoding of marking.

13 Soldering profile

The recommended soldering process is in accordance with IEC $60068-2-58-3^{rd}$ edit and IPC/JEDEC J-STD-020B.

ramp rata	≤ 3 K/s	
ramp rate	2 3 N/S	
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s	
T > 220 °C	30 s to 70 s	
T > 230 °C	min. 10 s	
T > 245 °C	max. 20 s	
<i>T</i> ≥ 255 °C	-	
peak temperature T_{peak}	250 °C +0/-5 °C	
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s	
cooling rate	≤ 3 K/s	
soldering temperature T	measured at solder pads	

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

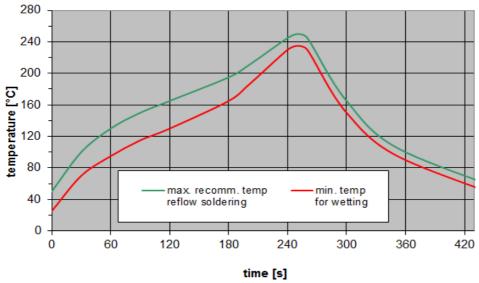


Figure 19: Recommended reflow profile for convection and infrared soldering – lead-free solder.

14 Annotations

14.1 Matching coils

See TDK inductor pdf-catalog http://www.tdk.co.jp/tefe02/coil.htm#aname1 and Data Library for circuit simulation http://www.tdk.co.jp/etvcl/index.htm.

14.2 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

14.3 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.

14.4 Ordering codes and packing units

Ordering code	Packing unit
B39192B8024P810	5000 pcs

Table 4: Ordering codes and packing units.

15 Cautions and warnings

15.1 Display of ordering codes for RF360 products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of RF360, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under www.rf360jv.com/orderingcodes.

15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

15.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.



16 Important notes

The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, RF360 Europe GmbH and its affiliates are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an RF360 product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
- 3. The warnings, cautions and product-specific notes must be observed.
- 4. In order to satisfy certain technical requirements, some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous). Useful information on this will be found in our Material Data Sheets on the Internet (www.rf360jv.com/material). Should you have any more detailed questions, please contact our sales offices.
- 5. We constantly strive to improve our products. Consequently, the products described in this publication may change from time to time. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order. We also reserve the right to discontinue production and delivery of products. Consequently, we cannot guarantee that all products named in this publication will always be available.
 - The aforementioned does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.