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FMC-HDMI: Dual HDMI Input Expansion Card

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Description

Features

What's Included

Support

The FMC-HDMI extends FMC-compatible FPGA systems with two HDMI Type A input ports. It's extremely useful for image or video processing applications. One of the ports features a fully-fledged HDMI 1.4 receiver with CEC, HDCP, audio, and 3D TV support. The other port provides cable equalization and buffering for HDMI signals, leaving the decoding up to the user.

Which port is more appropriate to use depends on the application. The receiver port is user-friendly, hiding the complexities of the HDMI protocol. Its simple pixel bus interface spans a wide range of voltages and is easy to design in any FPGA environment. The receiver also hides the implementation details of auxiliary features, like CEC or audio embedded in the video stream. The second port provides signal conditioning only, so the HDMI protocol needs to be implemented in the FPGA. This offers video-processing flexibility at the price of complexity. Also, the TMDS I/O standard defined by the HDMI protocol requires a 3.3V VADJ, which might limit the compatibility with certain carrier cards. The EEPROM pre-programmed with a ready-to-use EDID should prove to be a good starting point. Both ports are backwards-compatible with DVI.

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