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BarGraph 3 Click



PID: MIKROE-3264

BarGraph 3 click is equipped with a five-segment LED bar graph display, notable for its strong and uniform illumination of the segments. Each segment consists of three internal LEDs with a common cathode, while the anodes of all LEDs are connected in a single point. This reduces the number of the display bar pins to just six. The common anode is connected to the PWM pin of the microbus via the P-type MOSFET, which can use the PWM signal from the microcontroller to change the intensity of the display. The fact that there are as many as three LEDs per segment makes the segments evenly illuminated, and the red color of the light on the dark background makes segments clearly visible.

It comes in the package which also includes the mikroSDK $^{\text{m}}$ software and a library with all the functions. The Click board $^{\text{m}}$ comes as a fully tested and approved prototype, making it a reliable device ready to use on the development board.

The integrated circuit labeled 74HC595 controls the bar graph display and it has a very wide application in electronics. After the 8-bit data is loaded into the internal registry of this chip through the SPI interface, the data is translated to the parallel 8-bit output by activating the STCP pin, thus controlling the display. Bar graph displays are often used to create various types of VU meters, status indicators, different types of counters and similar devices.

How does it work?

When it comes to driving an array of LED segments, using so-called shift-register ICs is almost unavoidable. This Click board™ uses a single <u>74HC595 IC</u>, a tri-state, serial-in, parallel-out, 8-bit shift-register with output latch, from <u>Texas Instruments</u>. It is used to drive the SMA-

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ISO 9001: 2015 certification of quality management system (QMS).

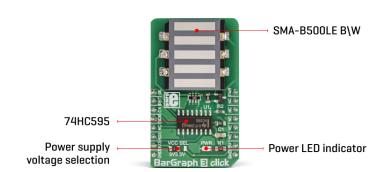


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B500LE a monochrome (red) 5 segment bar graph array, from American Opto Plus LED corporation. The 74HC595 ICs is comprised of a D-type internal storage register, and a serial-to-parallel shift register, both 8 bits wide. Each of these registers has its own clock line, making it possible to clock in the desired data in, and then clock it out to the parallel output pins when



The SMA-B500LE bar graph LED array has 5 red LED segments. Each segment contains three LEDs, with their cathodes connected in a single point and routed out as the single common cathode pin. This results with a bar graph display that has only six pins, even though it uses 15 LED elements in total. Similarly, all the anodes of the LED segments are routed to a single pin, which is connected to the drain of the P channel MOSFET, while its source is connected to the VCC. Driving the gate of the MOSFET using the PWM pin of the mikroBUS™ allows dimming of the LED bar graph display, by changing the pulse width of the applied PWM signal.

The Click board™ communicates with the host MCU over the SPI interface, routed to the mikroBUS™ MOSI and SCK pins, labeled as SDI and SCK on this Click board™, respectively. Five bits of information are pushed through the serial data input pin (DS) of the 74HC595 IC, routed to the SDI pin. The construction of the SPI interface is such that it operates with 8-bit long words, so the whole data word needs to be clocked in before it is latched on the output. However, the values of the bits that correspond to the non-connected pins of the 74HC595 IC will be disregarded.

The Output Enable pin (#OE) is routed to the AN pin of the mikroBUS™, and it is labeled as OE. If this pin is at the HIGH logic level, the outputs Q0 to Q7S of the 74HC595 IC will be set at HIGH-Z (high impedance mode) meaning that they will become disconnected. Regardless of the logic state on other pins, the outputs will not change from this state, until #OE is brought down to a LOW logic level. Memory content and the logic states at the output pins will be unaffected, meaning that the OE can be used to turn the segments of the bar graph on or off without affecting their states (like a simple SPST switch in series with the LED segment of the bar graph)

After the data word has been clocked in, the master SPI clock should be stopped, and the CS pin should be driven to a HIGH logic level. The CS pin of the mikroBUS™ is routed to the STCP pin of the 74HC595 IC. A rising edge on the STCP input pin of the 74HC595 IC will latch the data from the internal storage register to the output pins, changing the states of its parallel output pins (Q0 to Q7). If a specific bit in the internal storage is 0, the state on the appropriate pin of the 74HC595 IC will become LOW. With their anodes connected to the positive voltage level

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already (provided that the P-type MOSFET is open), the segment will be lit. This means that the logical 0 lights up a segment, while 1 turns it off.

The #MR pin is used to clear the data in the internal storage register of the 74HC595 IC. The LOW logic level on this pin will clear the content of this storage register, but it will not turn off the outputs which are already activated. The #MR pin is routed to the RST pin of the mikroBUS $^{\text{TM}}$ and it is pulled to a HIGH logic level by the onboard resistor.

BarGraph 3 click can work with both 3.3V and 5V MCUs. The operating voltage selection can be done via the onboard SMD jumper, labeled as VCC SEL.

Specifications

Туре	Bargraph,LED Segment
Applications	It can be used for displaying of various signal or status properties, for building VU-meters, and various types of gauges and signal indicators
On-board modules	74HC595, 8-bit serial-in, parallel-out shift registers with output latches, from Texas Instruments; SMA-B500LE, a monochrome (white) 5 segment bar graph array, from American Opto Plus LED corporation
Key Features	Clean and bright five-segment LED bar graph, with uniform light distribution, simple to use with included software library functions. Controlled by the popular 74HC595 shift register IC, dimmable with the PWM signal applied over the mikroBUS™ PWM pin.
Interface	GPIO,PWM,SPI
Feature	No ClickID
Compatibility	mikroBUS™
Click board size	M (42.9 x 25.4 mm)
Input Voltage	3.3V or 5V

The Pinout diagram

This table shows how the pinout on **BarGraph 3 Click** corresponds to the pinout on the mikroBUS $^{\text{m}}$ socket (the latter shown in the two middle columns).

Notes	Pin	nikro* BUS				Pin	Notes
Output Enable	OE	1	AN	PWM	16	PWM	PWM dimming
Data Clear	RST	2	RST	INT	15	INT	
Latch IN	CS	3	CS	RX	14	NC	
SPI Clock	SCK	4	SCK	TX	13	NC	
	NC	5	MISO	SCL	12	NC	

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Time-saving embedded tools

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SPI IN	SDI	6	MOSI	SDA	11	NC	
Power supply	3V3	7	3.3V	5V	10	5V	Power Supply
Ground	GND	8	GND	GND	9	GND	Ground

Onboard settings and indicators

Label	Name	Default	Description
PWR	PWR	-	Power LED indicator
VCC SEL	VCC SEL	Left	Power supply voltage
			selection: left position
			3.3V, right position 5V

Software support

We provide a library for the BarGraph 3 click on our <u>LibStock page</u>, as well as a demo application (example), developed using MikroElektronika <u>compilers</u>. The demo can run on all the main MikroElektronika <u>development boards</u>.

Library Description

The library initializes and defines SPI bus driver and drivers that offer a choice for writing data in registers and reading data from registers. The library includes functions for logging values on the BarGraph display. The user can enable and disable the chip with the function bargraph3 enable(), which is supported in the library.

Key functions:

- void bargraph3 display(uint8 t ctrl, uint8 t dir, uint8 t counter) Displays function
- void bargraph3 enable(uint8 t state) Functions for enabling the chip

Examples description

The application is composed of the three sections:

- System Initialization Initializes SPI module and sets AN, RST, CS and PWM pin as OUTPUT
- Application Initialization Initialization driver init, enable device and set PWM
- Application Task (code snippet) Counter passes through the loop and logs the value of the counter on the bar graph di

The full application code, and ready to use projects can be found on our <u>LibStock</u> page.

Other mikroE Libraries used in the example:

SPI

Additional notes and information

Depending on the development board you are using, you may need <u>USB UART click</u>, <u>USB UART 2 click</u> or <u>RS232 click</u> to connect to your PC, for development systems with no UART to USB interface available on the board. The terminal available in all MikroElektronika <u>compilers</u>, or Mikroe produces entire development toolchains for all major microcontroller architectures.

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any other terminal application of your choice, can be used to read the message.

mikroSDK

This click board is supported with <u>mikroSDK</u> - MikroElektronika Software Development Kit. To ensure proper operation of mikroSDK compliant click board demo applications, mikroSDK should be downloaded from the <u>LibStock</u> and installed for the compiler you are using.

For more information about mikroSDK, visit the official page.

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Applications	It can be used for displaying of various signal or status properties, for building VU-meters, and various types of gauges and signal indicators
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Resources

mikroBUS™

mikroSDK

Click board™ Catalog

Click Boards™

Downloads

74HC595 datasheet

BarGraph 3 click example on Libstock

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BarGraph 3 click 2D and 3D files

SMA-B500LE datasheet

BarGraph 3 schematic

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