

DC Brushless Fan Motor Driver

Standard Single-phase Full wave Fan Motor Driver


BA6424AFS
General Description

This is the summary of application for BA6424AFS. BA6424AFS incorporates lock protection and automatic restart circuit.

Features

- Lock protection and auto restart
- Rotating speed pulse signal (FG) output
- Lock alarm signal (AL) output

Applications

- General consumer electronics, BD player, DVR, STB
- Office equipment, Copier, FAX Laser Printer etc.

Package(s)

SSOP-A16

W(Typ.) x D(Typ.) x H(Max.)

6.60mm x 6.20mm x 1.71mm


Absolute maximum ratings

Parameter	Symbol	Ratings	Unit
Supply voltage	Vcc	30	V
Power dissipation	Pd	812.5*	mW
Operation temperature	Topr	-40 to +100	°C
Storage temperature	Tstg	-55 to +150	°C
Output current	Iomax	1.0**	A
Output voltage	Vout	30	V
FG signal output voltage	Vfg	30	V
AL signal output voltage	Val	30	V
Junction temperature	Tjmax	150	°C

* Reduce by 6.5mW/°C over 25°C. (On 70.0mm×70.0mm×1.6mm glass epoxy board)

** This value is not to exceed Pd.

Operating Conditions

Parameter	Symbol	Ratings	Unit
Operating supply voltage range	Vcc	6.0 to 28.0	V
Hall input voltage range	Vh	2.5 to Vcc	V

○Product structure : Silicon monolithic integrated circuit ○This product is not designed protection against radioactive rays

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●Electrical characteristics (Unless otherwise specified Ta=25°C, Vcc=12V)

Parameter	Symbol	Limits			Unit	Conditions	Characteristics
		Min.	Typ.	Max.			
Circuit current	Icc	2.7	5.4	8.1	mA	At output OFF	Fig.1
Charge current of capacitor for lock detection	Ildc	1.55	3.10	4.65	μ A	Vld=1.8V	-
Discharge current of capacitor for lock detection	Ildd	0.33	0.66	0.99	μ A	Vld=1.8V	-
Charge-discharge current ratio of capacitor for lock detection	rcd	3.0	4.7	6.4	-	$rcd=Ildc/Ildd$	-
Clamp voltage of capacitor for lock detection	Vldcl	2.00	2.48	3.00	V		-
Charge current of capacitor for lock detection	Vldcp	0.70	0.99	1.30	V		-
Output L voltage	Vol	-	0.8	1.2	V	Io=200mA	Fig.2
Output H voltage	Voh	-	0.9	1.4	V	Io=-200mA Voltage between output and Vcc	Fig.3
FG output L voltage	Vfgl	-	0.1	0.3	V	Ilg=10mA	Fig.4
FG output leak current	Ifgl	-	0	10	μ A	Vfg=30V	-
AL output L voltage	Vall	-	0.1	0.3	V	Ial=10mA	Fig.4
AL output leak current	Iall	-	0	10	μ A	Val=30V	-
Hall input - output offset voltage	Vofs	-20	0	20	mV		-
Hall input - FG offset voltage	Vfgofs	-45	-25	-5	mV		-

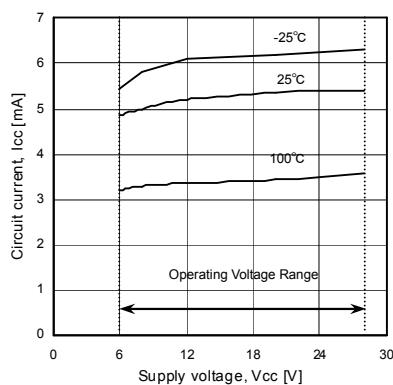
●Reference Data


Fig.1 Circuit current

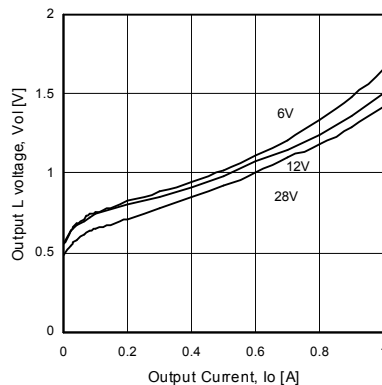


Fig.2 Output L voltage

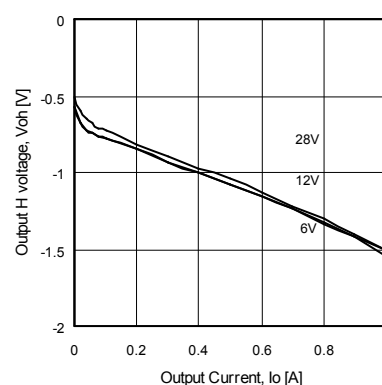


Fig.3 Output H voltage

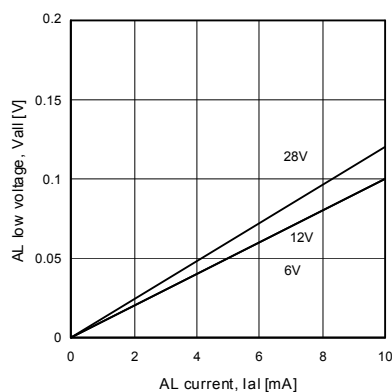
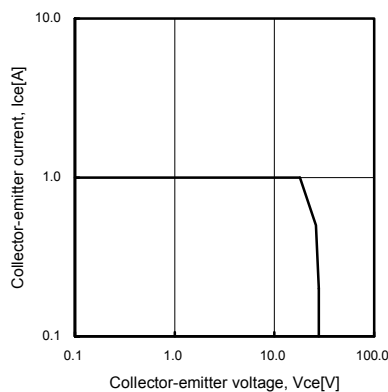
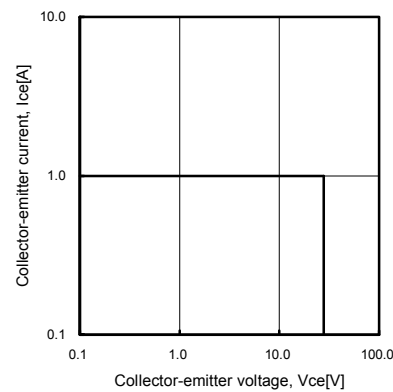
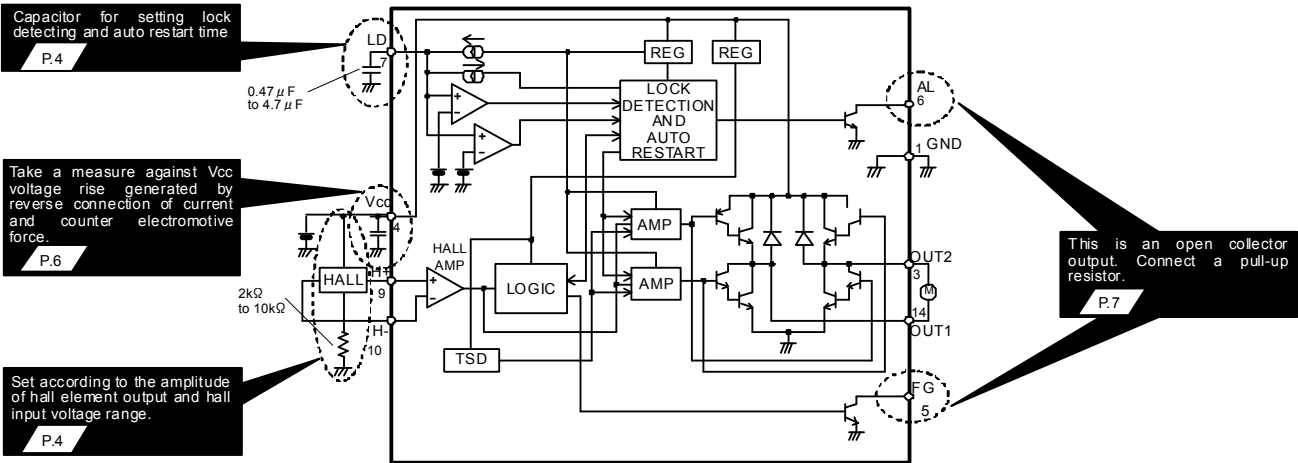


Fig.4 FG/AL output L voltage

Fig.5 Output Tr ASO (upper)
(TON=100msec)Fig.6 Output Tr ASO (lower)
(TON=100msec)

●Block diagram, application circuit, and pin assignment (Constant etc are for reference)



PIN No.	Terminal name	Function
1	GND	GND terminal
2	N.C.	
3	OUT2	Motor output terminal2
4	Vcc	Power supply terminal
5	FG	Rotating speed pulse signal output terminal
6	AL	Lock alarm signal output terminal
7	LD	Lock detection and auto restart capacitor connecting terminal
8	N.C.	
9	H+	Hall input terminal+
10	H-	Hall input terminal-
11	N.C.	
12	N.C.	
13	N.C.	
14	OUT1	Motor output terminal1
15	N.C.	
16	N.C.	

●Truth table

H+	H-	OUT1	OUT2	FG
H	L	H	L	H
L	H	L	H	L

AL normal operation
 :L(output is ON)
lock detection
 :H(output is OFF)

●Description of operations

1) Lock protection and automatic restart

Lock detection ON time (Ton) and lock detection OFF time (Toff) is set by charging and discharging of external capacitor of LD terminal.

$$T_{on} \text{ (Lock detection ON time)} = \frac{C \cdot (V_{ldcl} - V_{ldcp})}{I_{ldc}}$$

$$T_{off} \text{ (Lock detection OFF time)} = \frac{C \cdot (V_{ldcl} - V_{ldcp})}{I_{dd}}$$

C : Capacity of capacitor equipped externally on LD terminal

Vldcl : LD terminal clamping voltage

Vldcp : LD terminal comparator voltage

Ildc : LD terminal charging current

I_ldd : LD terminal discharging current

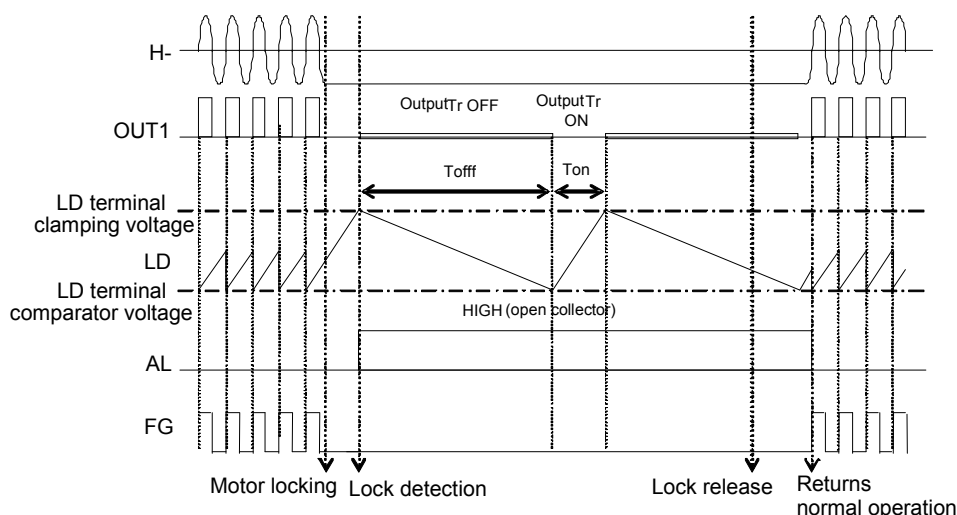


Fig.7 Lock protection timing chart

*BA6424AFS returns normal operation by the input of hall signal during Toff.

2) Hall input setting

Hall input voltage range is shown in operating conditions.

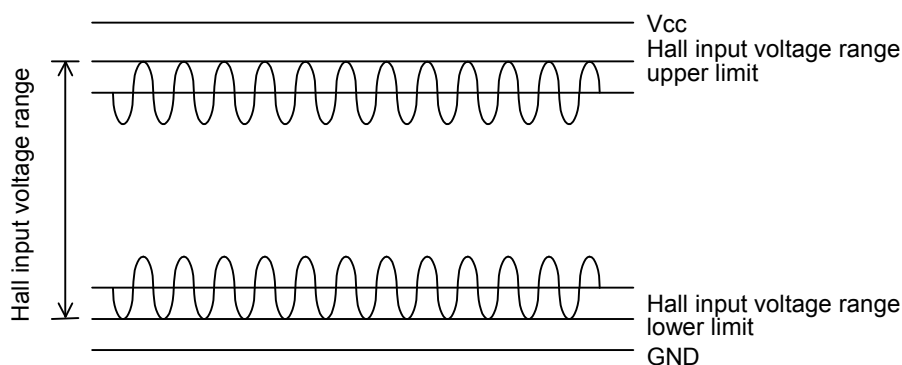


Fig.8 Hall input voltage range

Adjust the value of hall element bias resistor R1 in Fig.9 so that the input voltage of a hall amplifier is input in "hall input voltage range" including signal amplitude.

○Reducing the noise of hall signal

Hall element may be affected by Vcc noise or the like depending on the wiring pattern of board. In this case, place a capacitor like C1 in Fig.9. In addition, when wiring from the hall element output to IC hall input is long, noise may be loaded on wiring. In this case, place a capacitor like C2 in Fig.9.

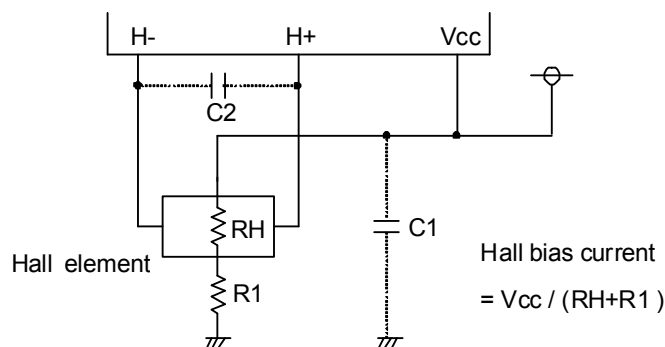


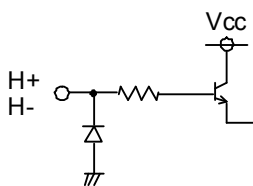
Fig.9 Application near hall signal

* This IC doesn't have hall input hysteresis.

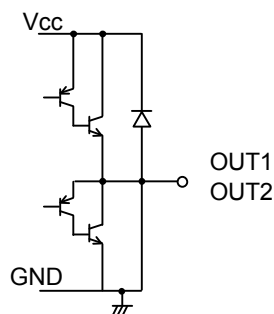
Note the hall input so that the FG signal do not chatter by the noise of the hall signal.

●Equivalent circuit

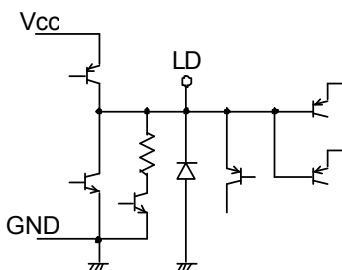
1) Hall input terminal



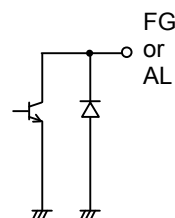
2) Motor output terminal



3) Lock detection and automatic restart capacitor connecting terminal



4) FG/AL signal output terminal



● Safety measure

1) Reverse connection protection diode

Reverse connection of power results in IC destruction as shown in Fig.10. When reverse connection is possible, reverse connection protection diode must be added between power supply and Vcc.

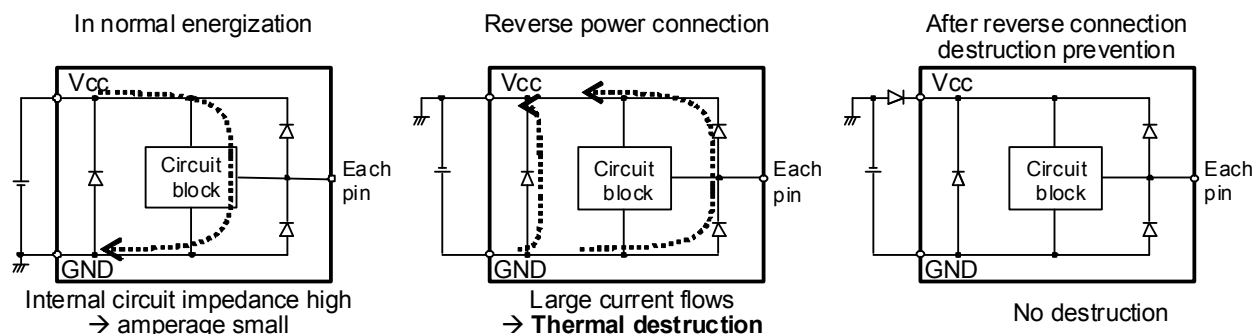


Fig.10 Flow of current when power is connected reversely

2) Measure against Vcc voltage rise by back electromotive force

Back electromotive force (Back EMF) generates regenerative current to power supply. However, when reverse connection protection diode is connected, Vcc voltage rises because the diode prevents current flow to power supply.

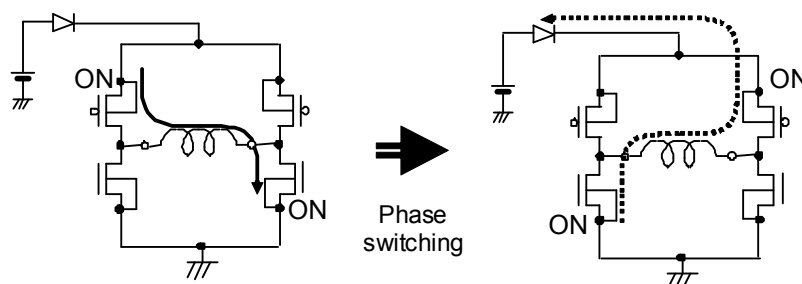


Fig.11 Vcc voltage rise by back electromotive force

When the absolute maximum rated voltage may be exceeded due to voltage rise by back electromotive force, place (A) Capacitor or (B) Zener diode between Vcc and GND. If necessary, add both (C).

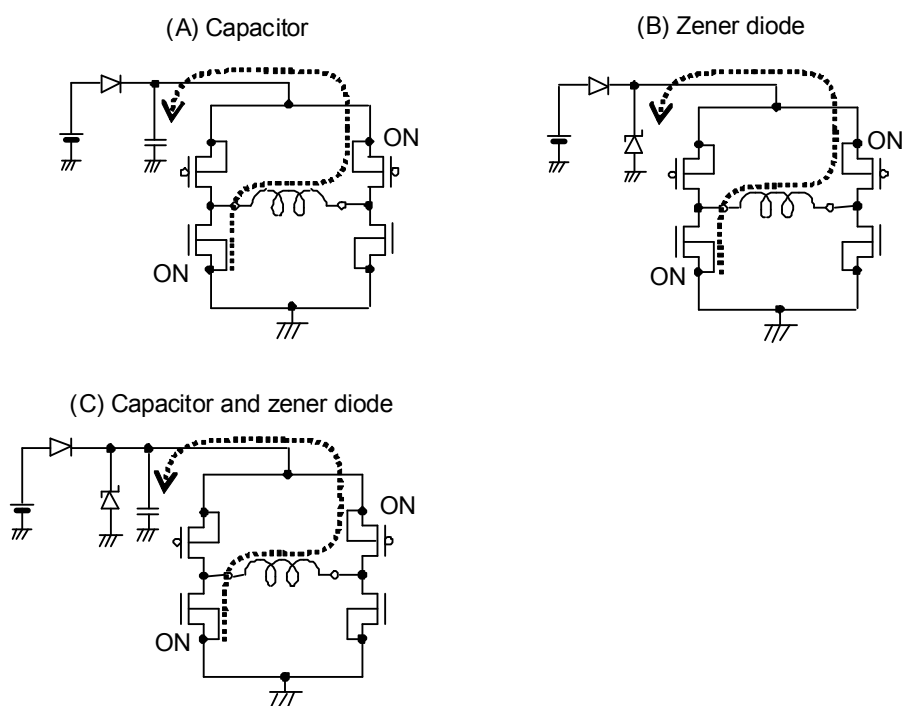


Fig.12 Measure against Vcc voltage rise

3) Problem of GND line PWM switching

Do not perform PWM switching of GND line because GND terminal potential cannot be kept to a minimum.

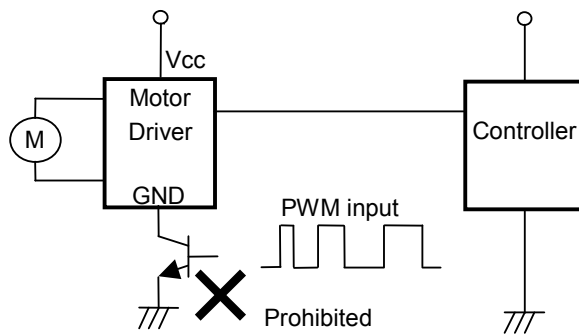


Fig.13 GND Line PWM switching prohibited

4) FG and AL output

FG and AL output is an open collector and requires pull-up resistor.

The IC can be protected by adding resistor R1. An excess of absolute maximum rating, when FG or AL output terminal is directly connected to power supply, could damage the IC.

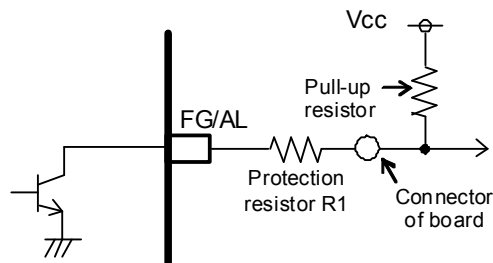


Fig.14 Protection of FG and AL terminal

●Calculation of power consumed by IC

Power consumed by this IC P_c is approximately calculated as follows:

$$P_c = P_{c1} + P_{c2} + P_{c3}$$

- P_{c1} : Power consumption by circuit current

$$P_{c1} = V_{cc} \times I_{cc}$$

- P_{c2} : Power consumption at output stage

$$P_{c2} = V_{OL} \times I_o + V_{OH} \times I_o$$

V_{OL} is L voltage of output terminal 1 and 2.

V_{OH} is H voltage of output terminal 1 and 2.

I_o is the current flowing to output terminal 1 and 2.

- P_{c3} : Power consumption at AL

$$P_{c3} = V_{AL} \times I_{AL}$$

V_{AL} is L voltage of AL output.

I_{AL} is the current of AL.

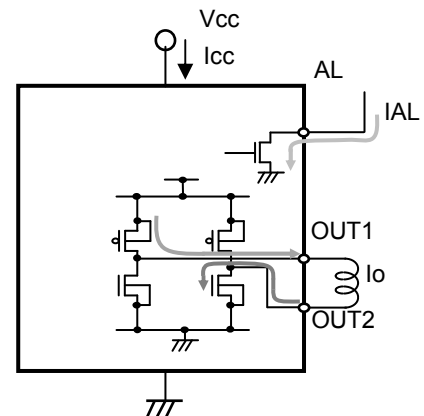


Fig.15 Calculation of power consumed by IC

Power consumption by IC greatly changes with use condition of IC such as power supply voltage and output current. Consider thermal design so that the maximum power dissipation on IC package is not exceeded.

●Thermal derating curve

Power dissipation (total loss) indicates the power that can be consumed by IC at $T_a = 25^\circ\text{C}$ (normal temperature). IC is heated when it consumes power, and the temperature of IC chip becomes higher than ambient temperature. The temperature that can be accepted by IC chip depends on circuit configuration, manufacturing process, etc., and consumable power is limited. Power dissipation is determined by the temperature allowed in IC chip (maximum junction temperature) and thermal resistance of package (heat dissipation capability). The maximum junction temperature is in general equal to the maximum value in the storage temperature range.

Heat generated by consumed power of IC is radiated from the mold resin or lead frame of package. The parameter which indicates this heat dissipation capability (hardness of heat release) is called heat resistance, represented by the symbol θ_{ja} [$^\circ\text{C}/\text{W}$]. The temperature of IC inside the package can be estimated by this heat resistance. Fig.16 shows the model of heat resistance of the package.

Heat resistance θ_{ja} , ambient temperature T_a , junction temperature T_j , and power consumption P can be calculated by the equation below:

$$\theta_{ja} = (T_j - T_a) / P \quad [^\circ\text{C}/\text{W}]$$

Thermal derating curve indicates power that can be consumed by IC with reference to ambient temperature. Power that can be consumed by IC begins to attenuate at certain ambient temperature. This gradient is determined by thermal resistance θ_{ja} .

Thermal resistance θ_{ja} depends on chip size, power consumption, package ambient temperature, packaging condition, wind velocity, etc., even when the same package is used. Thermal derating curve indicates a reference value measured at a specified condition. Fig.17 shows a thermal derating curve (Value when mounting FR4 glass epoxy board 70 [mm] x 70 [mm] x 1.6 [mm] (copper foil area below 3 [%]))

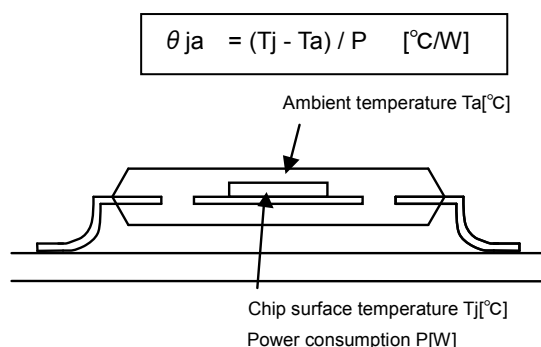
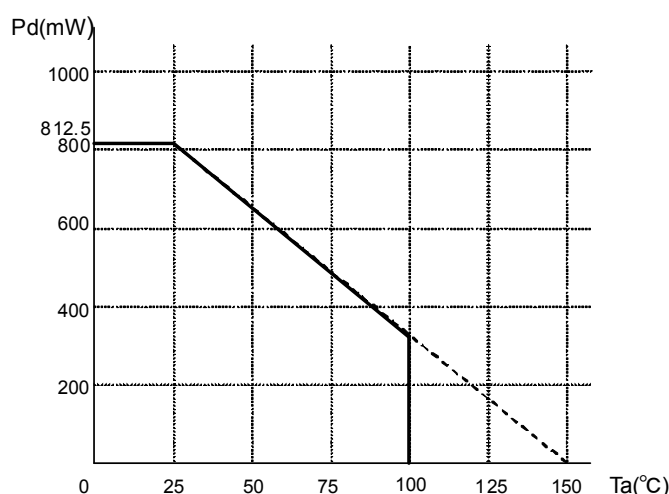


Fig.16 Thermal resistance



*Reduce by 6.5 mW/ $^\circ\text{C}$ over 25 $^\circ\text{C}$.
(On 70.0mm x 70.0mm x 1.6mm glass epoxy board)

Fig.17 Thermal derating curve

●Cautions on use

- 1) Absolute maximum ratings
Devices may be destroyed when supply voltage or operating temperature exceeds the absolute maximum ratings. Because the cause of this damage cannot be identified as a short circuit or an open circuit, if any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses..
- 2) Connecting the power supply connector backward
Connecting of the power supply in reverse polarity can damage IC. Take precautions when connecting the power supply lines. An external direction diode can be added.
- 3) Power supply line
Back electromotive force causes regenerated current to power supply line, therefore take a measure such as placing a capacitor between power supply and GND for routing regenerated current. And fully ensure that the capacitor characteristics have no problem before determine a capacitor value. (when applying electrolytic capacitors, capacitance characteristic values are reduced at low temperatures)
- 4) GND potential
It is possible that the motor output terminal may deflect below GND terminal because of influence by back electromotive force of motor. The potential of GND terminal must be minimum potential in all operating conditions, except that the levels of the motor outputs terminals are under GND level by the back electromotive force of the motor coil. Also ensure that all terminals except GND and motor output terminals do not fall below GND voltage including transient characteristics. Malfunction may possibly occur depending on use condition, environment, and property of individual motor. Please make fully confirmation that no problem is found on operation of IC.
- 5) Thermal design
Use a thermal design that allows for a sufficient margin in light of the power dissipation(Pd) in actual operating conditions.
- 6) Inter-pin shorts and mounting errors
Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error or if pins are shorted together.
- 7) Actions in strong electromagnetic field
Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.
- 8) ASO
When using the IC, set the output transistor so that it does not exceed absolute maximum ratings or ASO.
- 9) Thermal shut down circuit
The IC incorporates a built-in thermal shutdown circuit (TSD circuit). Operation temperature is 175°C(typ.) and has a hysteresis width of 25°C(typ.). When IC chip temperature rises and TSD circuit works, the output terminal becomes an open state. TSD circuit is designed only to shut the IC off to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operation this circuit or use the IC in an environment where the operation of this circuit is assumed.
- 10) Testing on application boards
When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.
- 11) GND wiring pattern
When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.
- 12) Capacitor between output and GND
When a large capacitor is connected between output and GND, if Vcc is shorted with 0V or GND for some cause, it is possible that the current charged in the capacitor may flow into the output resulting in destruction. Keep the capacitor between output and GND below 100uF.
- 13) IC terminal input
When Vcc voltage is not applied to IC, do not apply voltage to each input terminal. When voltage above Vcc or below GND is applied to the input terminal, parasitic element is actuated due to the structure of IC. Operation of parasitic element causes mutual interference between circuits, resulting in malfunction as well as destruction in the last. Do not use in a manner where parasitic element is actuated.
- 14) In use
We are sure that the example of application circuit is preferable, but please check the character further more in application to a part which requires high precision. In using the unit with external circuit constant changed, consider the variation of externally equipped parts and our IC including not only static character but also transient character and allow sufficient margin in determining

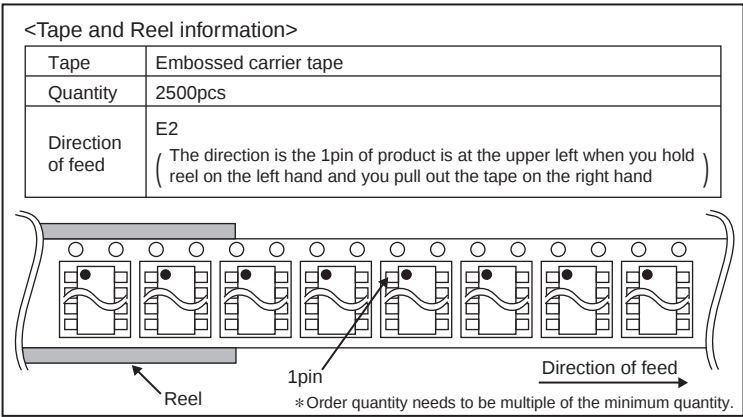
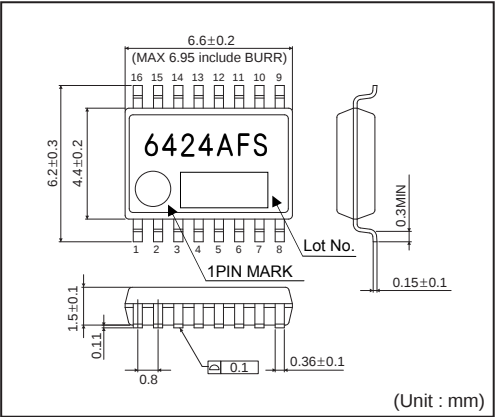
Status of this document

The Japanese version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document formal version takes priority

●Physical Dimension

SSOP-A16



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JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
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For details, please refer to ROHM Mounting specification

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2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
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4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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