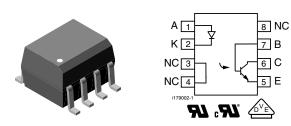


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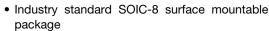
Vishay Semiconductors

Optocoupler, Phototransistor Output, With Base Connection in SOIC-8 Package



FEATURES

Isolation test voltage, 4000 V_{RMS}





· Compatible with dual wave, vapor phase and IR



reflow soldering Material categorization:

for definitions of compliance please see

LINKS TO ADDITIONAL RESOURCES







DESCRIPTION

The IL211AT, IL212AT, IL213AT are optically coupled pairs with a GaAs infrared LED and silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

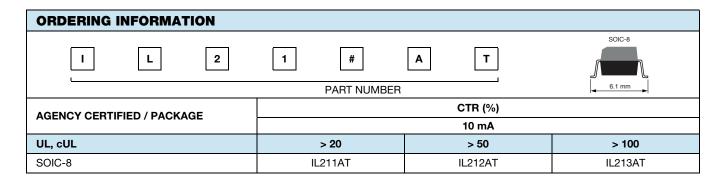
The IL211AT, IL212AT, IL213AT comes in a standard SOIC-8 small outline package for surface mounting which makes it ideally suited for high density applications with limited space. In addition to eliminating through-holes requirements, this package conforms to standards for surface mounted devices.

A choice of 20 %, 50 %, and 100 % minimum CTR at I_F = 10 mA makes these optocouplers suitable for a variety of different applications.

AGENCY APPROVALS

www.vishay.com/doc?99912

- UL
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1





Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Peak reverse voltage		V _R	6	V			
Forward continuous current		I _F	60	mA			
Power dissipation		P _{diss}	90	mW			
Derate linearly from 25 °C			1.2	mW/°C			
OUTPUT							
Collector emitter breakdown voltage		BV _{CEO}	30	V			
Emitter collector breakdown voltage		BV _{ECO}	7	V			
Collector base breakdown voltage		V _{CBO}	70	V			
I _{CMAX} . DC		I _{CMAX. DC}	50	mA			
I _{CMAX} .	t < 1 ms	I _{CMAX} .	100	mA			
Power dissipation		P _{diss}	150	mW			
Derate linearly from 25 °C			2	mW/°C			
COUPLER							
Isolation test voltage		V _{ISO}	4000	V _{RMS}			
Total package dissipation	LED and detector	P _{tot}	240	mW			
Derate linearly from 25 °C			3.2	mW/°C			
Storage temperature		T _{stg}	-55 to +150	°C			
Operating temperature		T _{amb}	-55 to +100	°C			
Soldering time	At 260 °C		10	s			

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I _F = 10 mA	V _F	-	1.3	1.5	V	
Reverse current	V _R = 6 V	I _R	-	0.1	100	μΑ	
Capacitance	$V_R = 0 V$	Co	=.	13	-	pF	
OUTPUT							
Collector emitter breakdown voltage	I _C = 10 μA	BV _{CEO}	30	-	-	V	
Emitter collector breakdown voltage	$I_E = 10 \mu A$	BV _{ECO}	7	-	-	V	
Collector dark current	V _{CE} = 10 V	I _{CEO}	-	5	50	nA	
Collector emitter capacitance	V _{CE} = 0 V	C _{CE}	-	10		pF	
COUPLER							
Saturation voltage, collector emitter	I _F = 10 mA	V _{CEsat}	-	-	0.4	V	
Isolation test voltage	1 s	V _{ISO}	4000	-	-	V _{RMS}	
Capacitance (input to output)		C _{IO}	=.	0.5	50	pF	
Resistance (input to output)		R _{IO}	-	100	-	GΩ	
Collector emitter breakdown voltage	I _C = 10 μA	BV _{CEO}	30	-	-	V	

Note

Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.



Vishay Semiconductors

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio		IL211AT	CTR	20	50	-	%
	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	IL212AT	CTR	50 80	-	%	
		IL213AT	CTR	100	130	-	%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Switching time	I_C = 2 mA, R_L = 100 Ω , V_{CC} = 10 V		t _{on} , t _{off}	-	3	-	μs

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification	According to IEC 68 part 1		-	55 / 100 / 21	-			
Comparative tracking index		CTI	175	-	399			
V _{IOTM}			6000	-	-	V		
V _{IORM}			560	-	-	V		
P _{SO}			-	-	350	mW		
I _{SI}			-	-	150	mA		
T _{SI}			-	-	165	°C		
Creepage distance			4	-	-	mm		
Clearance distance			4	-	-	mm		
Insulation thickness			0.2	-	=	mm		

Note

• As per IEC 60747-5-5, § 7.4.3.8.1, this optocoupler is suitable for "Safe Electrical Insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

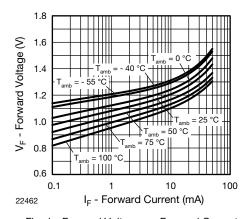


Fig. 1 - Forward Voltage vs. Forward Current

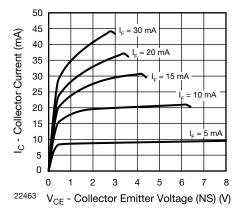


Fig. 2 - Collector Current vs. Collector Emitter Voltage (non-saturated)



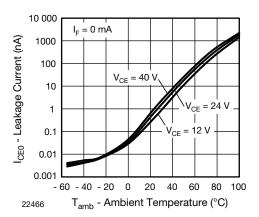


Fig. 3 - Leakage Current vs. Ambient Temperature

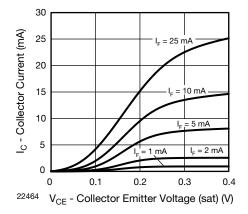


Fig. 4 - Collector Current vs. Collector Emitter Voltage (saturated)

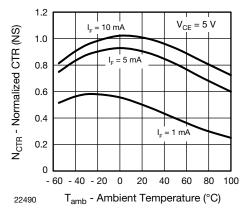


Fig. 5 - Normalized CTR (non-saturated) vs. Ambient Temperature

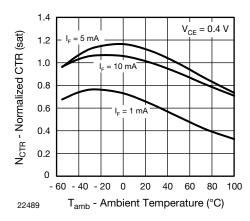


Fig. 6 - Normalized CTR (saturated) vs. Ambient Temperature

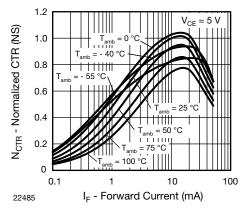


Fig. 7 - Normalized CTR (non-saturated) vs. Forward Current

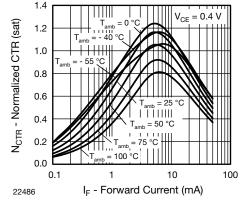


Fig. 8 - Normalized CTR (saturated) vs. Forward Current



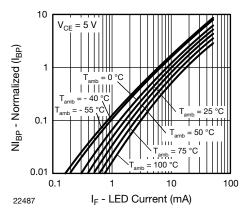


Fig. 9 - Normalized Photocurrent vs. LED Current

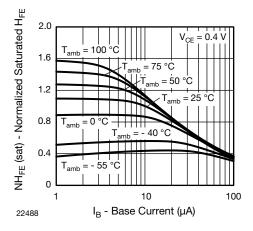


Fig. 10 - Normalized Saturated H_{FE} vs. Base Current

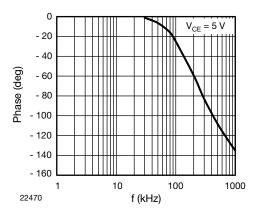


Fig. 11 - F_{CTR} vs. Phase Angle

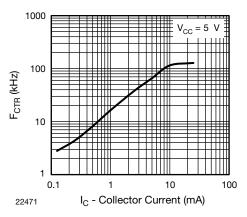


Fig. 12 - F_{CTR} vs. I_C

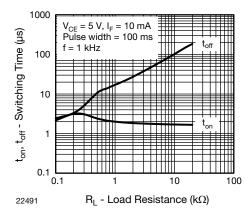


Fig. 13 - Switching Time vs. Load Resistance

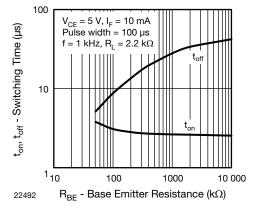


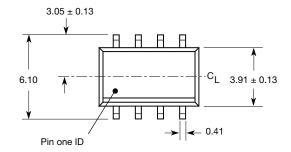
Fig. 14 - Switching Time vs. Base Emitter Resistance

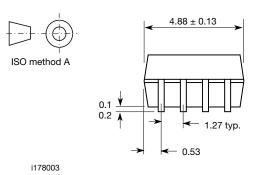
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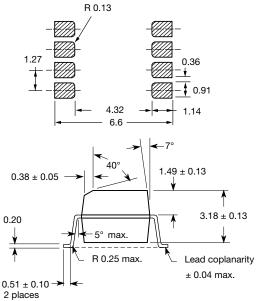


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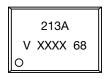
PACKAGE DIMENSIONS in millimeters







PACKAGE MARKING (example)



Notes

- XXXX = LMC (lot marking code)
- Tape and reel suffix (T) is not part of the package marking





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