

Multilayer Ceramic Chip Capacitors

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C4532X7R2A105K230KA



TDK item description ? C4532X7R2A105KT****

Applications



Commercial Grade

Please refer to Part No. [CGA8N2X7R2A105K230KA](#) for Automotive use.

Feature

Mid

Mid Voltage (100 to 630V)

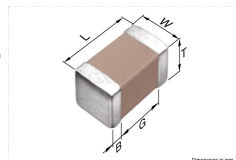
Series

C4532 [EIA 1812]

Status



Production (Not Recommended for New Design)



Images are for reference only and show exemplary products.

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Documents

- [Catalog](#)
- [Specification](#)
- [RoHS Certificate](#)
- [SVHC/REACH Certificate](#)
- [Characterization Sheet](#)
- [Promotion Video](#)
- [Sample Kits](#)

Technical Support Tools

- [S-parameter](#)
- [SPICE Netlist \(Simple\)](#)
- [SPICE Netlist \(Precision\)](#)
- [Equivalent Circuit Model](#)
- [DC Bias/Superimposition Model \(HSPICE\)](#)

Size

Length(L)	4.50mm ±0.40mm
Width(W)	3.20mm ±0.40mm
Thickness(T)	2.30mm ±0.20mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	
Recommended Land Pattern (PA)	3.10mm to 3.70mm
Recommended Land Pattern (PB)	1.20mm to 1.40mm
Recommended Land Pattern (PC)	2.40mm to 3.20mm

Electrical Characteristics

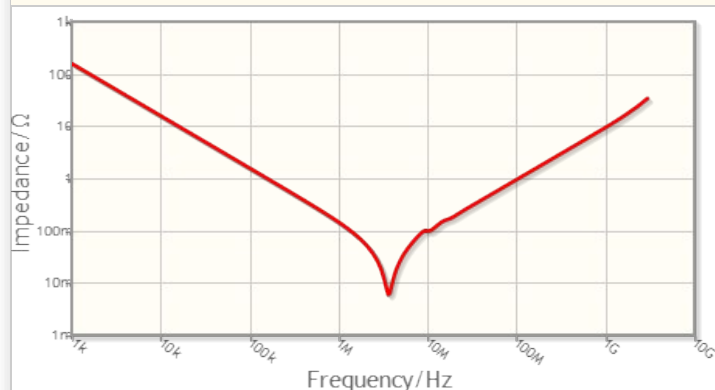
Capacitance	1μF ±10%
Rated Voltage	100VDC
Temperature Characteristic ?	X7R(±15%)
Dissipation Factor (Max.)	3%
Insulation Resistance (Min.)	500MΩ

Other

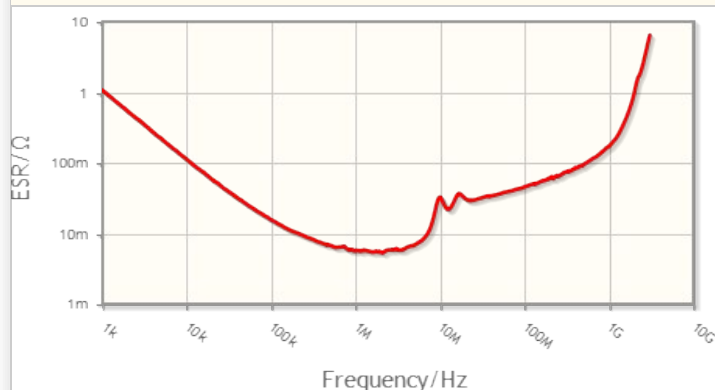
Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	500pcs

Characteristic Graph (This is reference data, and does not guarantee the products characteristics.)

Impedance



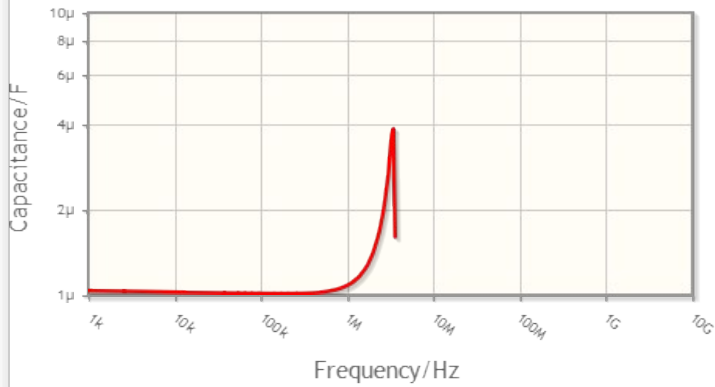
ESR



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Change settings

Capacitance

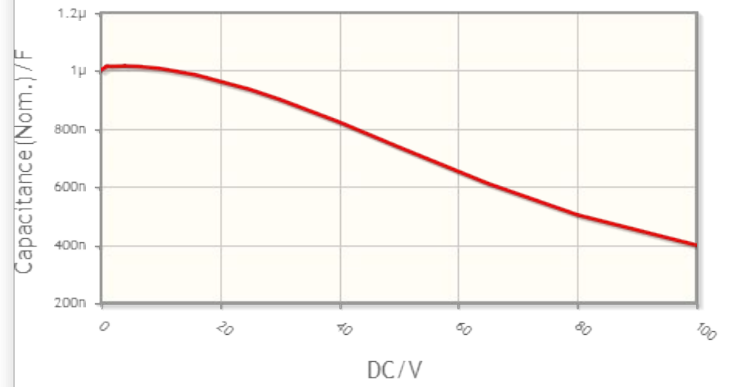


C4532X7R2A105K230KA

Change settings

Change settings

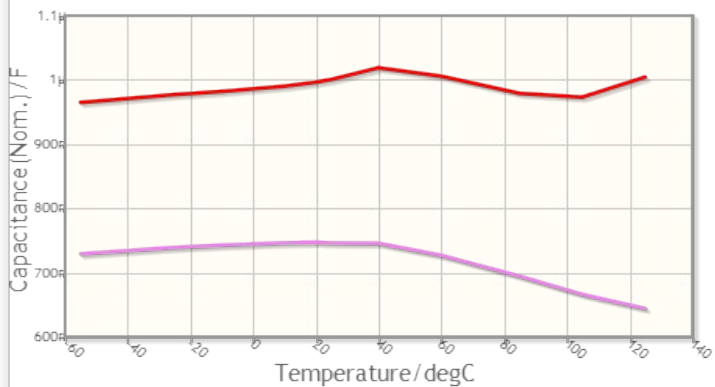
DC Bias Characteristic



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Change settings

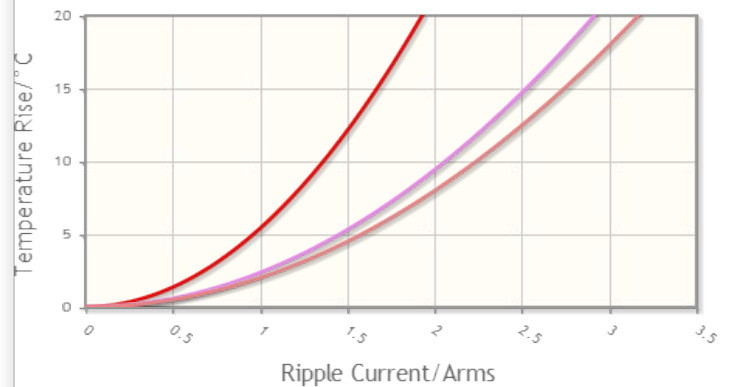
Temperature Characteristic



C4532X7R2A105K230KA(No Bias)
C4532X7R2A105K230KA(DC Bias = 50V)

Change settings

Ripple Temperature Rising



C4532X7R2A105K230KA(100k Hz)
C4532X7R2A105K230KA(500k Hz)
C4532X7R2A105K230KA(1M Hz)

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