QB-R5F100SL-TB

As a CPU board usable in connection with the on-chip debugging emulator E1 , this product achieves simple and easy evaluation of the target MCU.

Product Info | Design Support | Documentation | Ordering



Features

- Built-in RL78/G13 (R5F100SLAFB).
- A 20 MHz oscillator mounted as main clock, high-speed operation enabled (@ 2.7 to 5.5 V).
- Equipped with universal area (2.54 mm pitch).
- Supports both flash memory programming and on-chip debugging (using TOOL0 pin).
- Highly extendable; peripheral board connectors are equipped with microcontroller pins.
- Lead-free (Pb-free) product.

Target Devices

Family	Group
RL78 Family	RL78/G13

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