

## Description

The DIODES™ AP22916 is a small, low leakage, single P-channel power MOSFET designed for low-power consumption and load-switching applications. This power MOSFET has a typical  $R_{DS(ON)}$  of 60mΩ at 5V, allowing increased load current handling capacity with a low forward voltage drop. Multiple voltages correspond to different time options to support various system load conditions. The trigger of the load switch ON pin can be controlled to be enabled or disabled by an external low voltage digital signal for sequence control application. Smart pull down feature is built in the ON pin. Once the enable voltage is higher than  $V_{IH}$ , it will disconnect to avoid power loss.  $V_{IN}$  and  $V_{OUT}$  are isolated during OFF state with the TRCB (true reverse current blocking) feature.

The AP22916 load switch is designed to operate from 1.3V to 5.5V, making it ideal for 1.3V, 1.8V, 2.5V, 3.6V, and 5V systems. The typical quiescent supply current is only 0.5μA.

The AP22916 is available in the wafer-level chip-scale 4-pin, X1-WLB0808-4 0.78mm x 0.78mm x 0.455mm, 0.4mm pitch package. The device is characterized for operation over a temperature range of -40°C to +85°C.

## Features

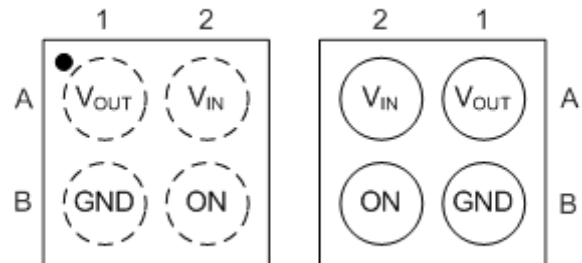
- Wide Input Voltage Range: 1.3V to 5.5V
- Low On-Resistance
  - 280mΩ Typical @1.3V
  - 135mΩ Typical @1.8V
  - 65mΩ Typical @3.6V
  - 54mΩ Typical @5.0V
- Continuous Current Capability up to 2A
- True Reverse Current Blocking (TRCB)
- Discharging Resistor on  $V_{OUT}$  When Disabled
- Ultra-Low Quiescent Current 0.5μA
- Active-High Control Pin
  - Minimum 1.0V  $V_{IH}$  of ON
- ESD Protection:
  - Human Body Model: 2kV
  - Charged Device Model: 1kV
- Package:
  - X1-WLB0808-4 with Backside Laminate
  - 0.78mm x 0.78mm, 0.4mm Ball Pitch
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.**

<https://www.diodes.com/quality/product-definitions/>

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

## Pin Assignments

**X1-WLB0808-4**



**Top View**

**Bottom View**

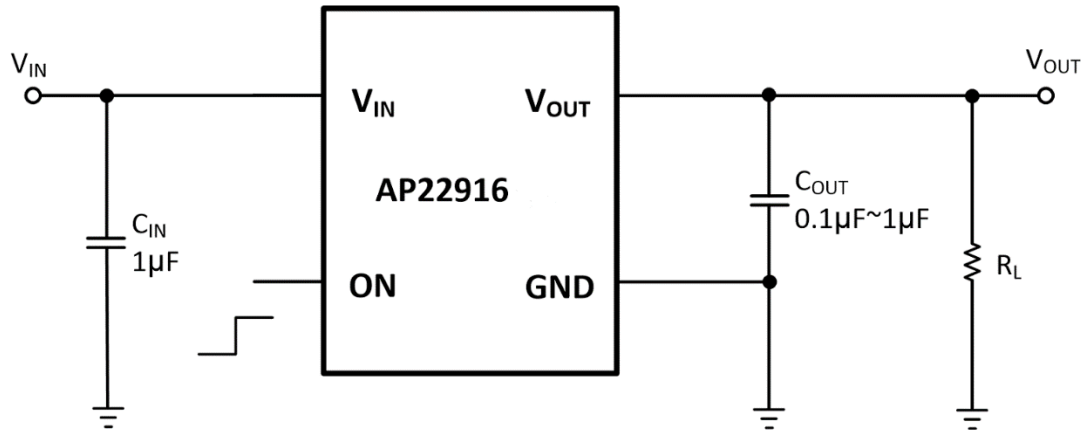
## Applications

- Mobile devices and smart phones
- Portable media devices
- Wearable devices
- Advanced notebooks, UMPC, and MID
- Portable medical devices
- GPS and navigation equipment

## Part Comparison Table

Version	Timing	Output Discharge	Enable
AP22916B	Fast	Yes	Active High
AP22916C	Slow	Yes	Active High
AP22916D	Fast	No	Active High
AP22916E	Slow	No	Active High

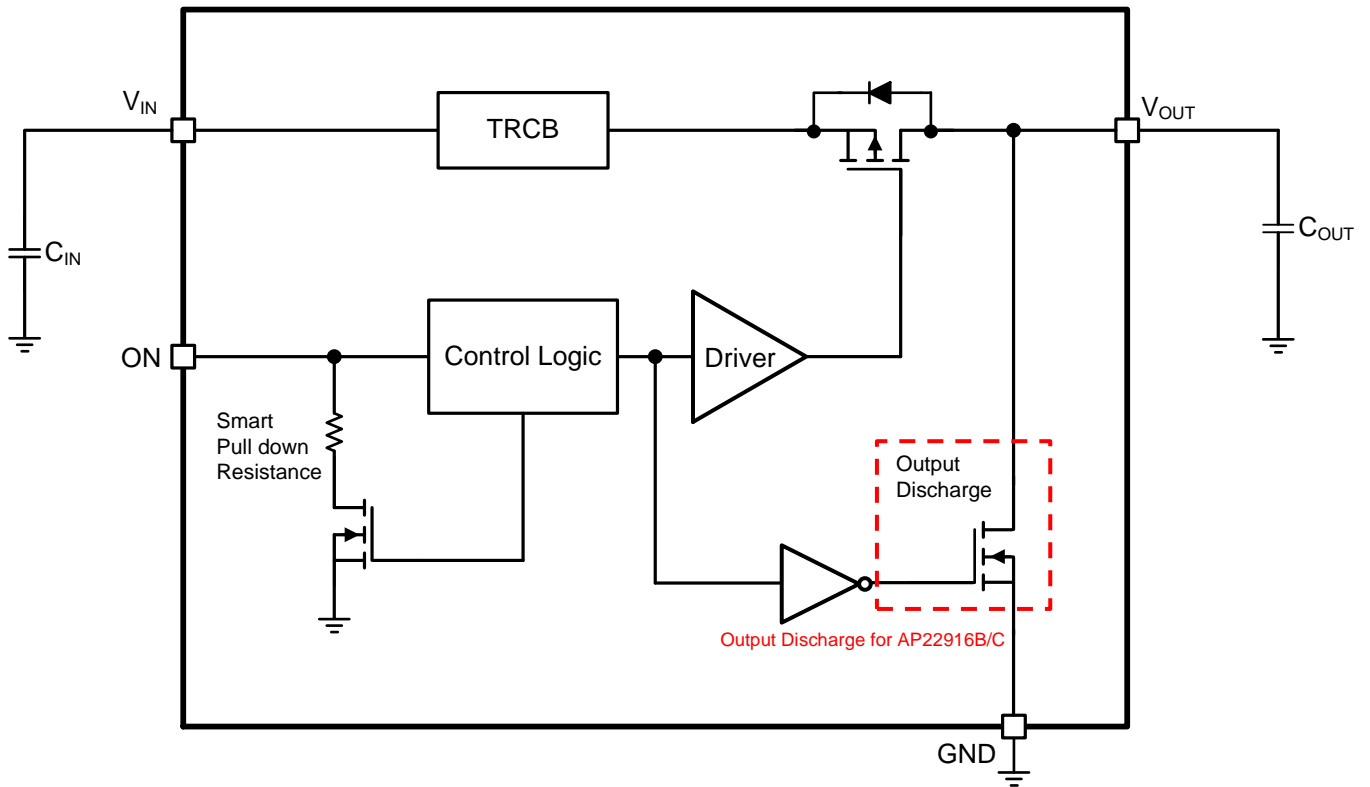
## Typical Applications Circuit



## Pin Descriptions

Pin Name	Pin Number	Function
$V_{OUT}$	A1	Voltage output pin. This is the pin to the P-channel MOSFET drain connection. Bypass to ground through a 0.1 $\mu$ F or 1 $\mu$ F capacitor.
$V_{IN}$	A2	Voltage input pin. This is the pin to the P-channel MOSFET source. Bypass to ground through a 1 $\mu$ F capacitor.
GND	B1	Ground
ON	B2	Enable input

## Functional Block Diagram



## Absolute Maximum Ratings (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Symbol	Parameter	Ratings	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
$V_{IN}$	Input Voltage	-0.3 to 6	V
$V_{OUT}$	Output Voltage	-0.3 to 6	V
$V_{ON}$	ON Voltage	-0.3 to 6	V
$I_{LOAD}$	Maximum Continuous Load Current	2	A
$I_{LOAD}$	Maximum Pulse Load Current, Pulse $<300\mu\text{s}$ , 2% Duty Cycle	2.5	A
$T_J$	Maximum Junction Temperature	+125	$^\circ\text{C}$
$T_{ST}$	Storage Temperature Range	-65 to +150	$^\circ\text{C}$
$P_D$	Power Dissipation	510	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 4)	195	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case (Note 5)	38	$^\circ\text{C/W}$
$T_{LEAD}$	Maximum Lead temperature (10-s soldering time)	260	$^\circ\text{C}$

Notes: 4. The JEDEC high-K (2s2p) board used to derive this data was a 3 inch x 3 inch, multilayer board with 1oz internal power and ground planes with 2oz copper traces on top and bottom of the board.  
5. Thermal resistance from junction to case.

Caution: Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time.

## Recommended Operating Conditions (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
$V_{IN}$	Input Voltage	1.3	5.5	V
$V_{ON}$	ON Voltage Range	0	5.5	V
$V_{OUT}$	Output Voltage	1.3	5.5	V
$I_{OUT}$	Output Current while $V_{in} \geq 1.5\text{V}$	0	2.0	A
	Output Current while $V_{in} \leq 1.5\text{V}$	0	1.0	A
$V_{IH}$	ON High-Level Input Voltage	1.0	5.5	V
$V_{IL}$	ON Low-Level Input Voltage	0	0.35	V
$T_A$	Operating Ambient Temperature	-40	+85	$^\circ\text{C}$

**Electrical Characteristics** ( $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ,  $V_{IN} = 1.3$  to  $5.5\text{V}$ ,  $V_{ON} = V_{IN}$  (Enabled),  $V_{ON} = 0\text{V}$  (Disabled),  $C_{IN} = 1\mu\text{F}$ ,  $C_{OUT} = 0.1\mu\text{F}$ , unless otherwise specified. Typical values are at  $25^{\circ}\text{C}$ ) (Note 6)

Symbol	Parameters	Test Conditions	Min	Typ	Max	Unit
$I_Q$	Input Quiescent Current	$I_{OUT} = 0\text{mA}$ , $V_{ON}$ Enabled	—	0.3	0.5	$\mu\text{A}$
$I_{SHDN}$	Input Shutdown Current	$R_L = 1\text{M}\Omega$ , $V_{ON}$ Disabled, $V_{IN} = 5.0\text{V}$	+25°C	—	40	nA
			-40°C to +85°C	—	225	
		$R_L = 1\text{M}\Omega$ , $V_{ON}$ Disabled, $V_{IN} = 1.8\text{V}$	+25°C	—	5	
			-40°C to +85°C	—	20	
$R_{DS(ON)}$	Switch On-resistance, $I_{OUT} = 200\text{mA}$	$V_{IN} = 5.0\text{V}$	+25°C	—	54	mΩ
			-40°C to +85°C	—	70	
			-40°C to +105°C	—	75	
		$V_{IN} = 3.6\text{V}$	+25°C	—	65	
			-40°C to +85°C	—	85	
			-40°C to +105°C	—	90	
		$V_{IN} = 1.8\text{V}$	+25°C	—	135	
			-40°C to +85°C	—	165	
			-40°C to +105°C	—	180	
		$V_{IN} = 1.3\text{V}$	+25°C	—	280	
			-40°C to +85°C	—	320	
			-40°C to +105°C	—	350	
$R_{ON}$	Smart Pull Down Resistance	$V_{ON}$ Disabled	—	750	—	kΩ
$V_{RCB}$	TRCB Trigger Voltage	$V_{ON}$ Enabled, $V_{OUT} > V_{IN}$	—	25	—	mV
$I_{RCB}$	TRCB Activation Current	$V_{IN} = 3.3\text{V}$ , $V_{ON}$ Enabled, $V_{OUT} > V_{IN}$	—	-650	—	mA
$t_{RCB}$	TRCB Response Time	$V_{ON}$ Enabled, $V_{OUT} > V_{IN} + 200\text{mV}$	—	10	—	$\mu\text{s}$
$I_{IN\_RCB}$	TRCB Reverse Leakage Current (Current from $V_{IN}$ )	$V_{ON}$ Enabled, $V_{OUT} - V_{IN} > V_{RCB}$	-300	—	—	nA
$R_{DIS}$	Output Discharge On Resistance	$V_{ON}$ Disabled, $I_{OUT} = 1\text{mA}$	—	150	—	Ω

Note: 6. Specifications are over  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$  and are guaranteed by characterization and design.

**Timing Characteristics** (The typical characteristics in the following table applies over the entire recommended power supply voltage range of 1.3V to 5.5V at 25°C with a load of  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$ , unless otherwise specified.) (Note 7)

Symbol	Parameters	Test Conditions	Min	Typ	Max	Unit
<b>AP22916B</b>						
$t_{ON}$	Output Turn-on	$V_{IN} = 5.0V$	—	85	—	$\mu s$
		$V_{IN} = 3.6V$	—	110	—	
		$V_{IN} = 1.8V$	—	250	—	
		$V_{IN} = 1.3V$	—	480	—	
$t_R$	Output Rise Time	$V_{IN} = 5.0V$	—	42	—	$\mu s$
		$V_{IN} = 3.6V$	—	52	—	
		$V_{IN} = 1.8V$	—	95	—	
		$V_{IN} = 1.3V$	—	180	—	
$SR_{ON}$	Slew Rate	$V_{IN} = 5.0V$	—	90	—	mV/ $\mu s$
		$V_{IN} = 3.6V$	—	52	—	
		$V_{IN} = 1.8V$	—	13	—	
		$V_{IN} = 1.3V$	—	5	—	
$t_{OFF}$	Output Turn-off Time	$V_{IN} = 5.0V$	—	6.4	—	$\mu s$
		$V_{IN} = 3.6V$	—	8	—	
		$V_{IN} = 1.8V$	—	16	—	
		$V_{IN} = 1.3V$	—	25	—	
$t_F$	Output Fall Time	$C_{OUT} = 0.1\mu F$ , $R_L = 10\Omega$	—	2.3	—	$\mu s$
		$C_{OUT} = 1\mu F$ , $R_L = \text{Open}$	—	357	—	

Note: 7. Rise and fall time of the control signal are less than 100ns.

Symbol	Parameters	Test Conditions	Min	Typ	Max	Unit
<b>AP22916C</b>						
$t_{ON}$	Output Turn-on	$V_{IN} = 5.0V$	—	1400	—	$\mu s$
		$V_{IN} = 3.6V$	—	1700	—	
		$V_{IN} = 1.8V$	—	3800	—	
		$V_{IN} = 1.3V$	—	6800	—	
$t_R$	Output Rise Time	$V_{IN} = 5.0V$	—	750	—	$\mu s$
		$V_{IN} = 3.6V$	—	900	—	
		$V_{IN} = 1.8V$	—	1500	—	
		$V_{IN} = 1.3V$	—	2800	—	
$SR_{ON}$	Slew Rate	$V_{IN} = 5.0V$	—	5	—	mV/ $\mu s$
		$V_{IN} = 3.6V$	—	3.2	—	
		$V_{IN} = 1.8V$	—	1	—	
		$V_{IN} = 1.3V$	—	0.4	—	
$t_{OFF}$	Output Turn-off Time	$V_{IN} = 5.0V$	—	7.1	—	$\mu s$
		$V_{IN} = 3.6V$	—	8	—	
		$V_{IN} = 1.8V$	—	16	—	
		$V_{IN} = 1.3V$	—	25	—	
$t_F$	Output Fall Time	$C_{OUT} = 0.1\mu F$ , $R_L = 10\Omega$	—	2.3	—	$\mu s$
		$C_{OUT} = 10\mu F$ , $R_L = \text{Open}$	—	4490	—	

Note: 7. Rise and fall time of the control signal are less than 100ns.

**Timing Characteristics** (The typical characteristics in the following table applies over the entire recommended power supply voltage range of 1.3V to 5.5V at 25°C with a load of  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$ , unless otherwise specified.) (Note 7) (continued)

Symbol	Parameters	Test Conditions	Min	Typ	Max	Unit
<b>AP22916D</b>						
$t_{ON}$	Output Turn-on	$V_{IN} = 5.0V$		85		$\mu s$
		$V_{IN} = 3.6V$		110		
		$V_{IN} = 1.8V$		250		
		$V_{IN} = 1.3V$		480		
$t_R$	Output Rise Time	$V_{IN} = 5.0V$		50		$\mu s$
		$V_{IN} = 3.6V$		60		
		$V_{IN} = 1.8V$		110		
		$V_{IN} = 1.3V$		210		
$SR_{ON}$	Slew Rate	$V_{IN} = 5.0V$		90		$mV/\mu s$
		$V_{IN} = 3.6V$		55		
		$V_{IN} = 1.8V$		15		
		$V_{IN} = 1.3V$		5		
$t_{OFF}$	Output Turn-off Time	$V_{IN} = 5.0V$		9		$\mu s$
		$V_{IN} = 3.6V$		12		
		$V_{IN} = 1.8V$		18		
		$V_{IN} = 1.3V$		35		
$t_F$	Output Fall Time	$C_{OUT} = 0.1\mu F$ , $R_L = 10\Omega$		13		$\mu s$

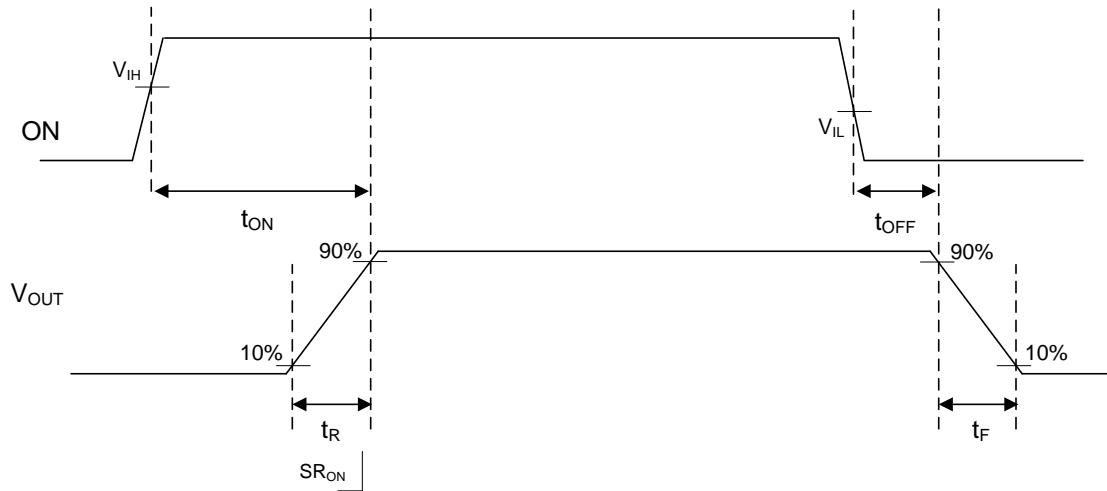
Note: 7. Rise and fall time of the control signal are less than 100ns.

Symbol	Parameters	Test Conditions	Min	Typ	Max	Unit
<b>AP22916E</b>						
$t_{ON}$	Output Turn-on	$V_{IN} = 5.0V$		1300		$\mu s$
		$V_{IN} = 3.6V$		1700		
		$V_{IN} = 1.8V$		3950		
		$V_{IN} = 1.3V$		7200		
$t_R$	Output Rise Time	$V_{IN} = 5.0V$		750		$\mu s$
		$V_{IN} = 3.6V$		930		
		$V_{IN} = 1.8V$		1750		
		$V_{IN} = 1.3V$		3300		
$SR_{ON}$	Slew Rate	$V_{IN} = 5.0V$		5		$mV/\mu s$
		$V_{IN} = 3.6V$		3		
		$V_{IN} = 1.8V$		0.8		
		$V_{IN} = 1.3V$		0.3		
$t_{OFF}$	Output Turn-off Time	$V_{IN} = 5.0V$		8		$\mu s$
		$V_{IN} = 3.6V$		10		
		$V_{IN} = 1.8V$		15		
		$V_{IN} = 1.3V$		35		
$t_F$	Output Fall Time	$C_{OUT} = 0.1\mu F$ , $R_L = 10\Omega$		13		$\mu s$

Note: 7. Rise and fall time of the control signal are less than 100ns.

**Timing Characteristics** (The typical characteristics in the following table applies over the entire recommended power supply voltage range of 1.3V to 5.5V at 25°C with a load of  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$ , unless otherwise specified.) (Note 7) (continued)

**Timing for Power-Up and Power-Down Operation**

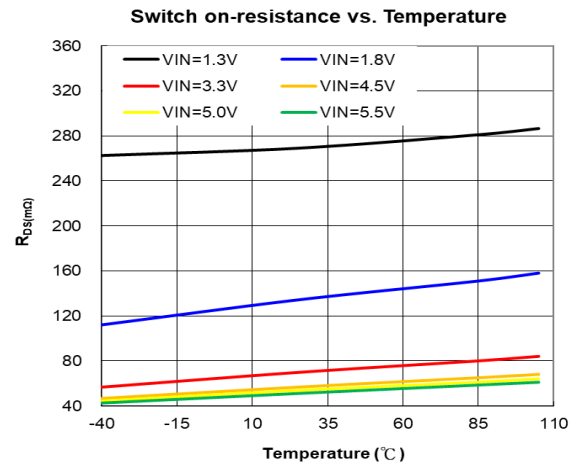
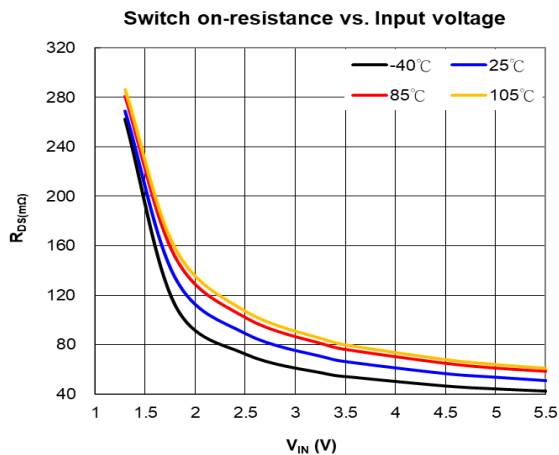
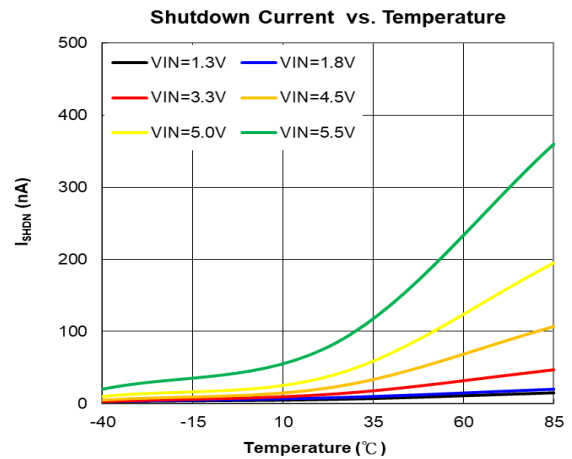
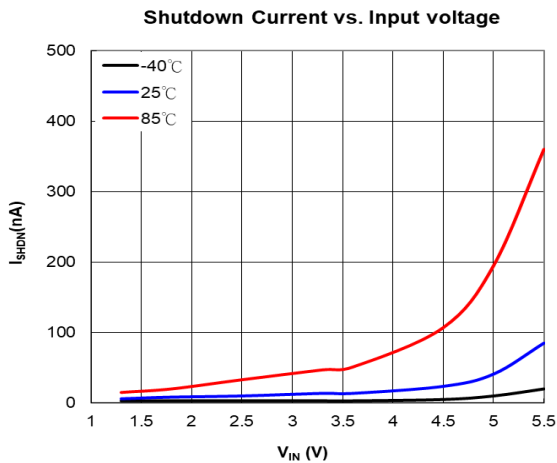
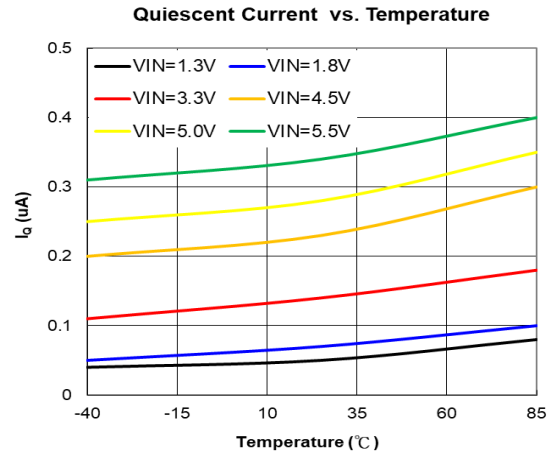
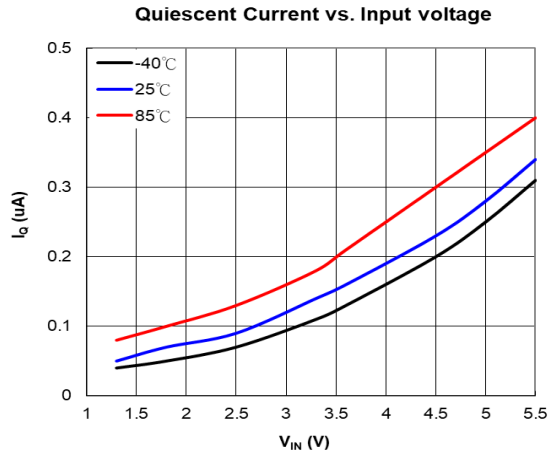


Output Rise ( $t_R$ ), Fall ( $t_F$ ), Turn On ( $t_{ON}$ ) and Turn Off ( $t_{OFF}$ ) Time

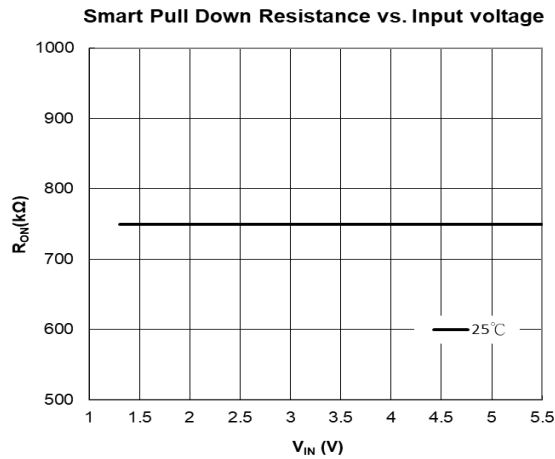
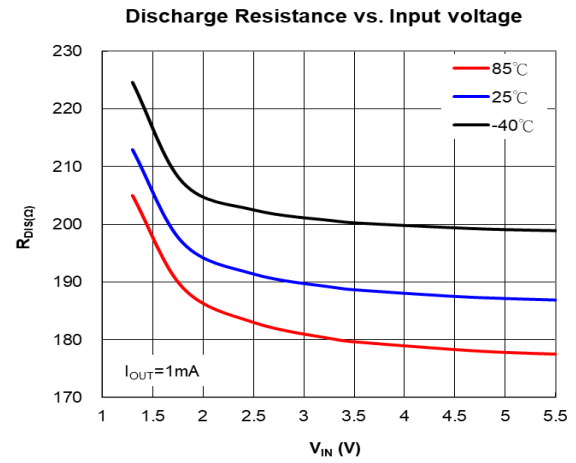
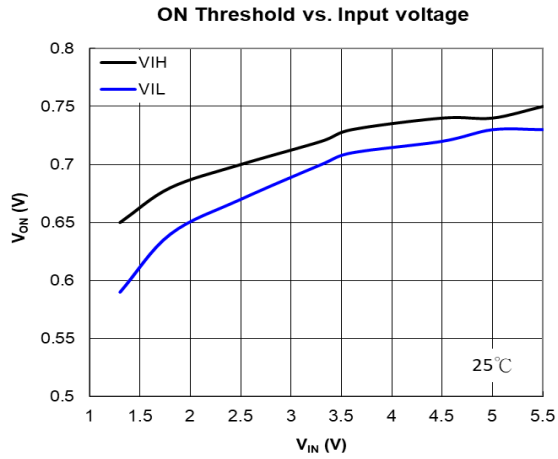
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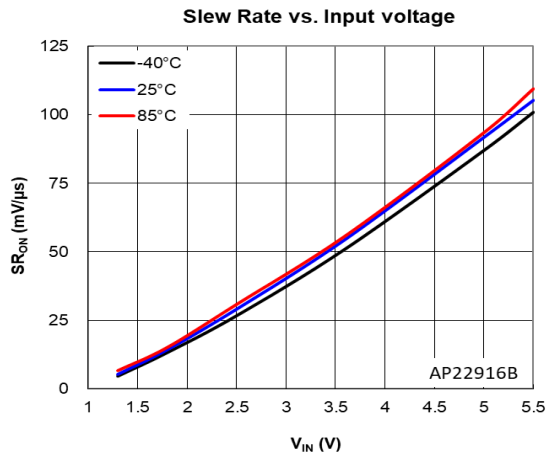
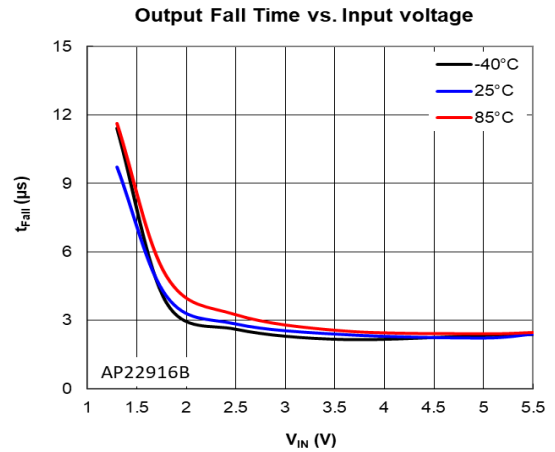
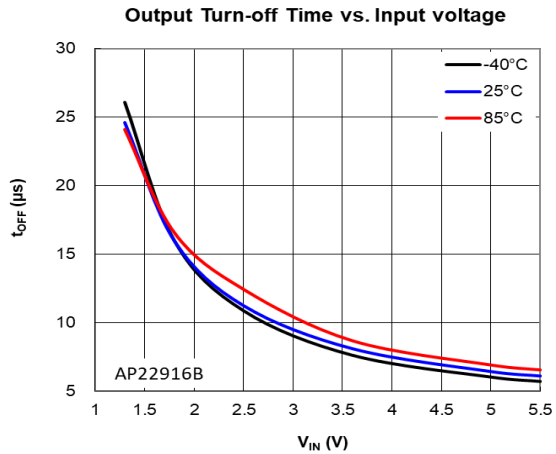
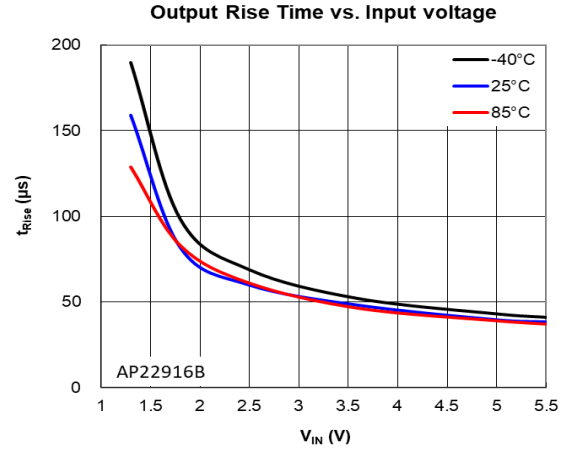
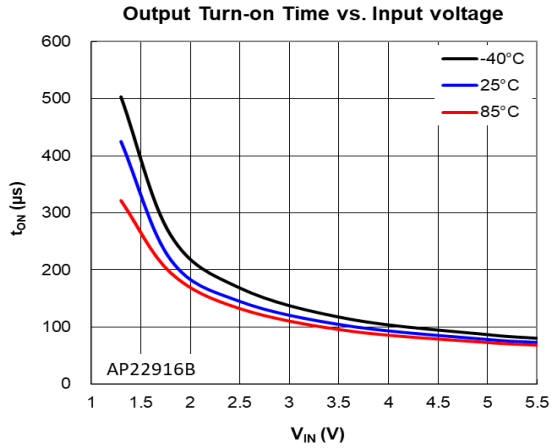
**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.)



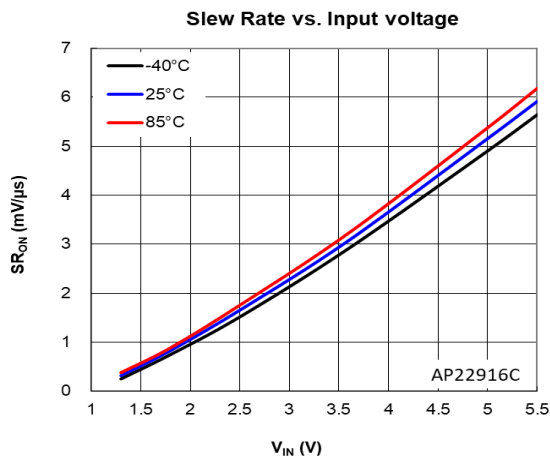
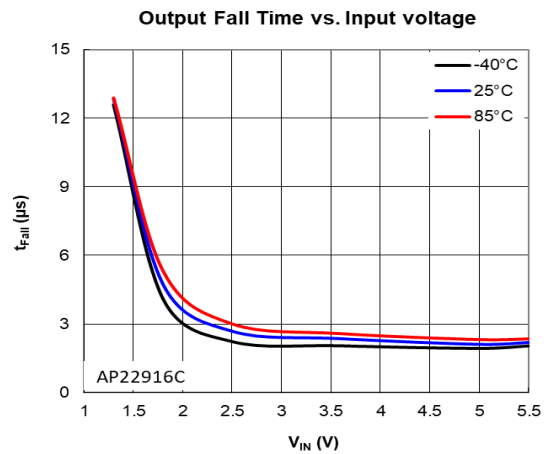
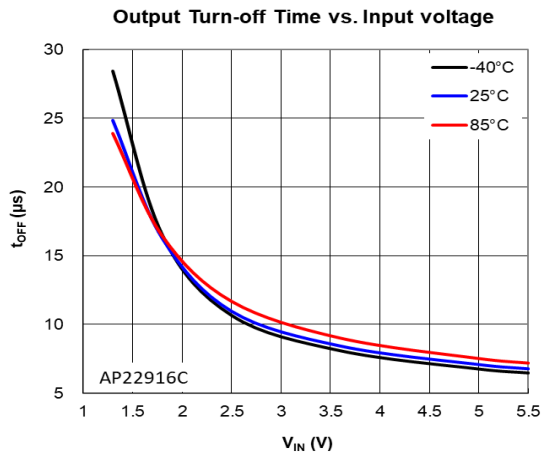
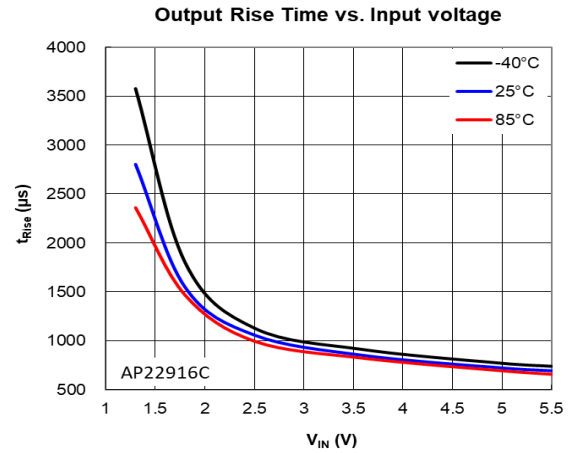
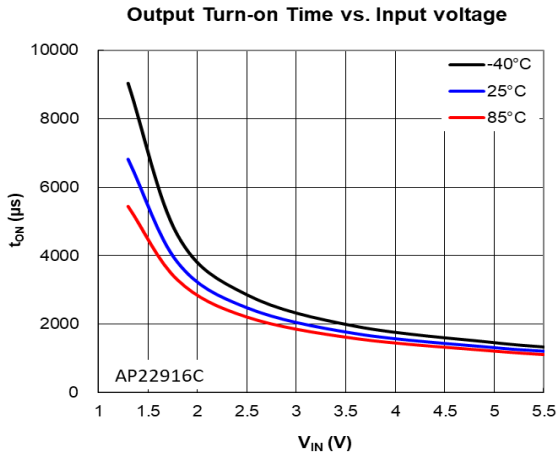
**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)



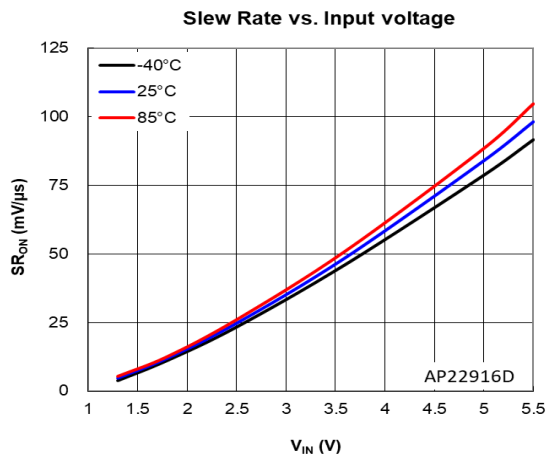
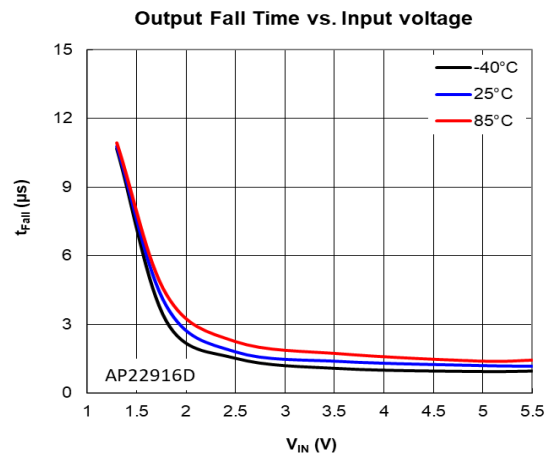
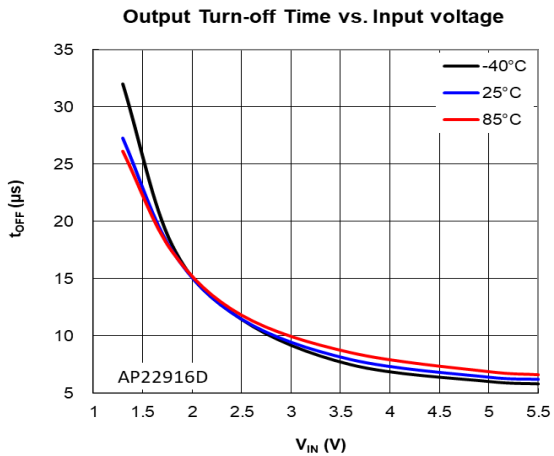
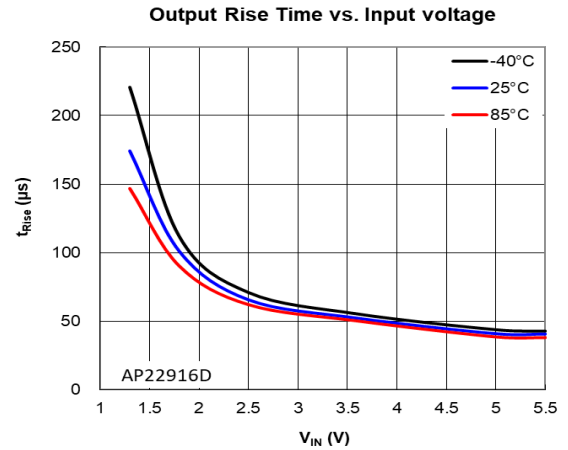
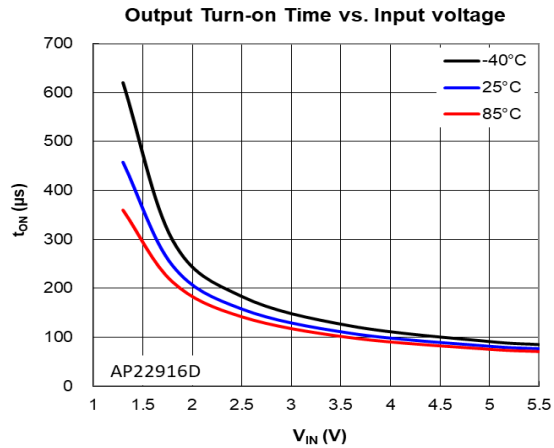
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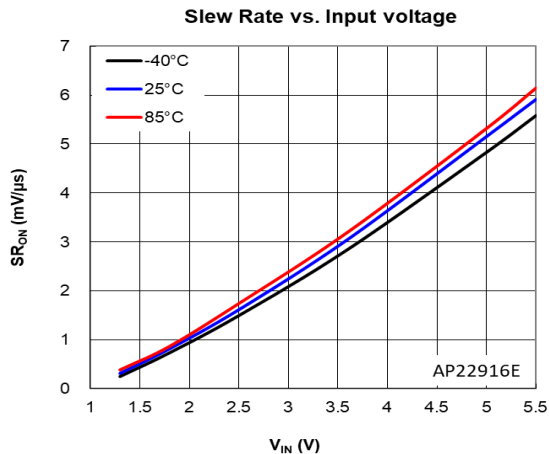
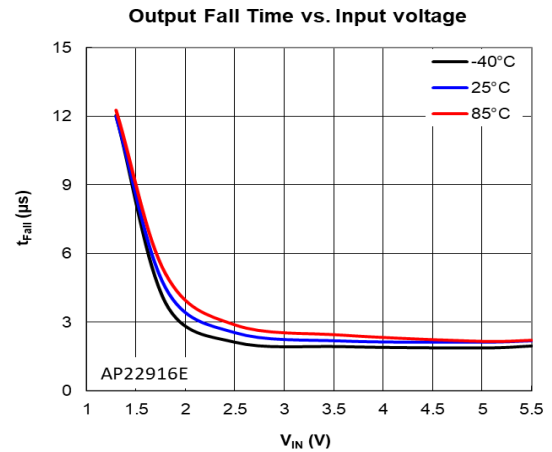
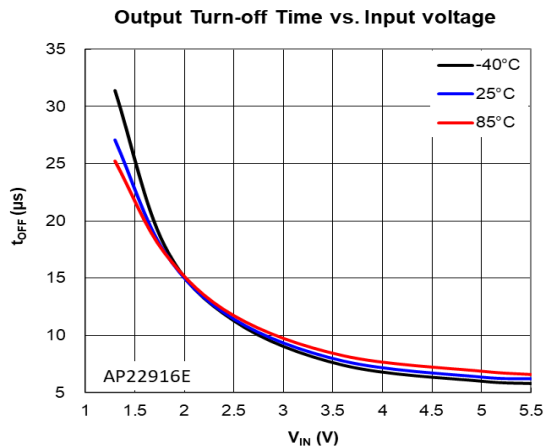
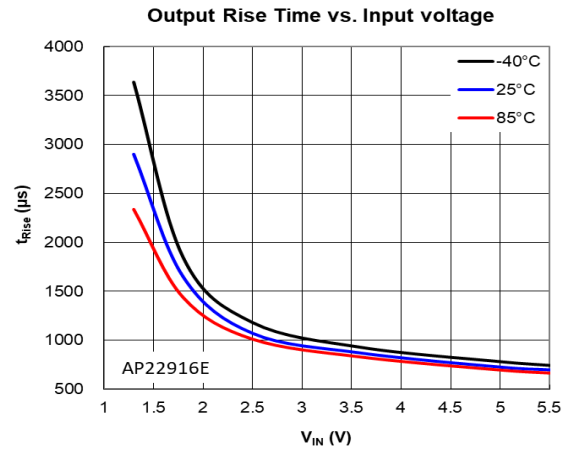
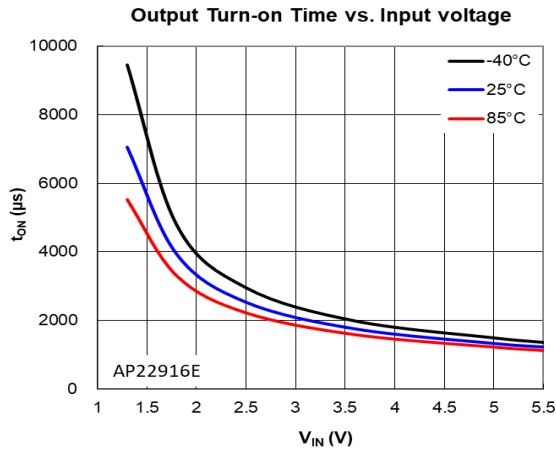
**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)



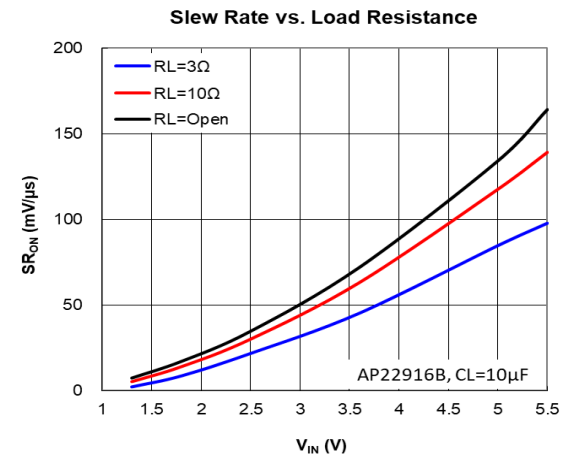
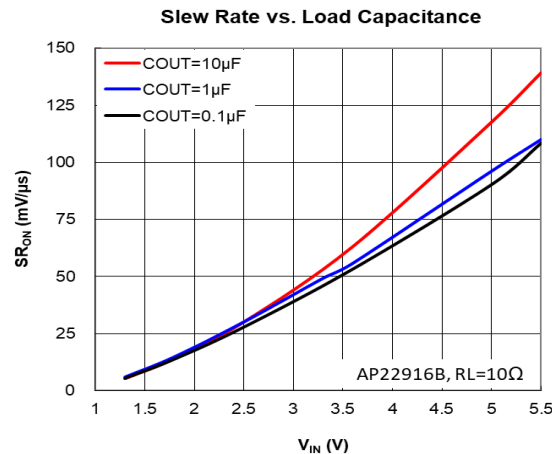
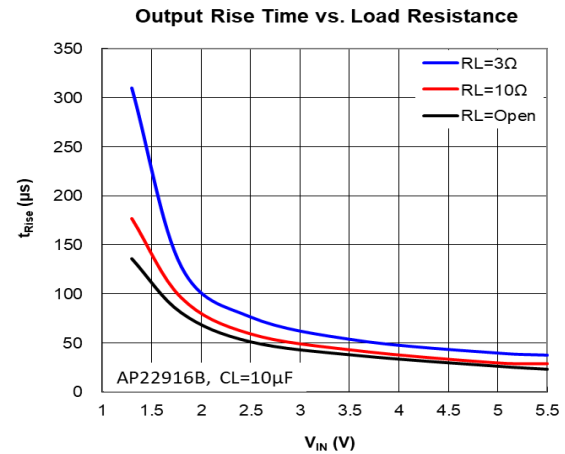
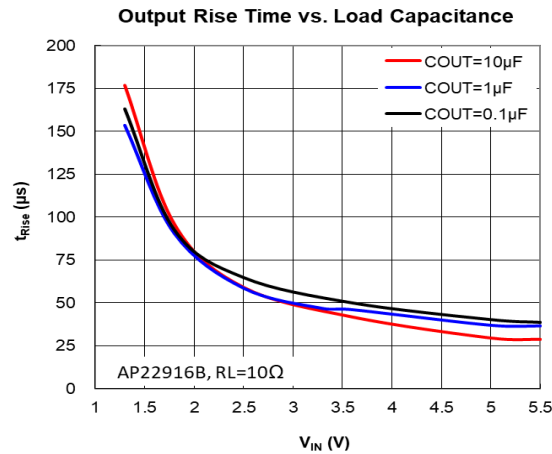
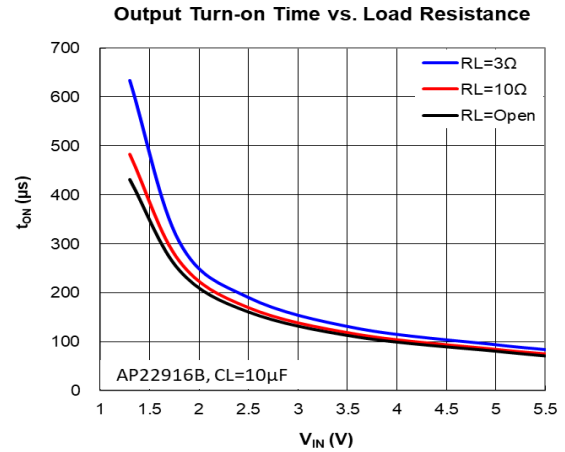
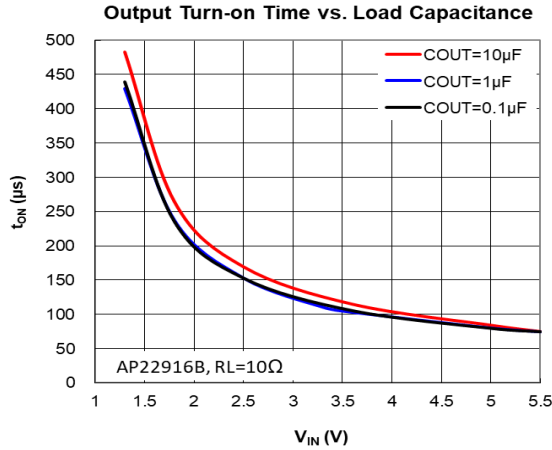
**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)



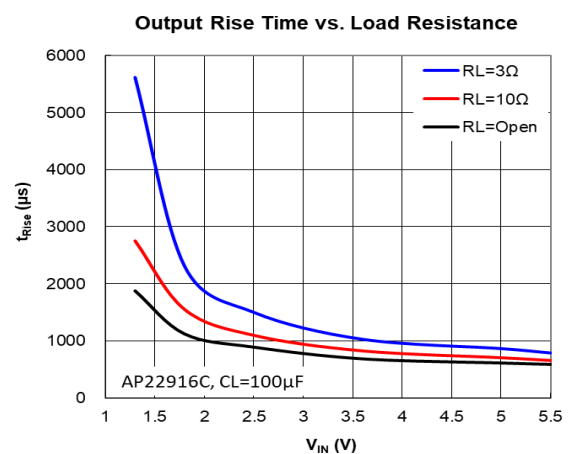
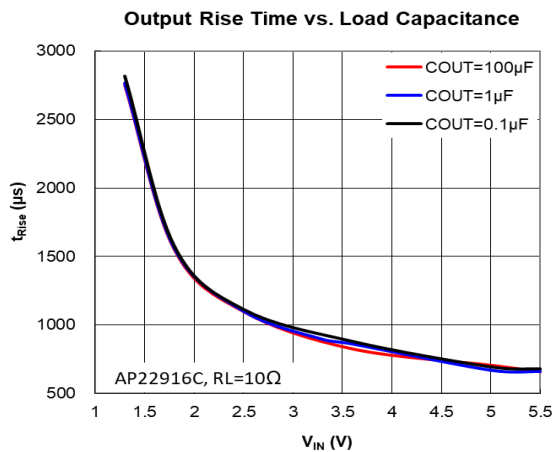
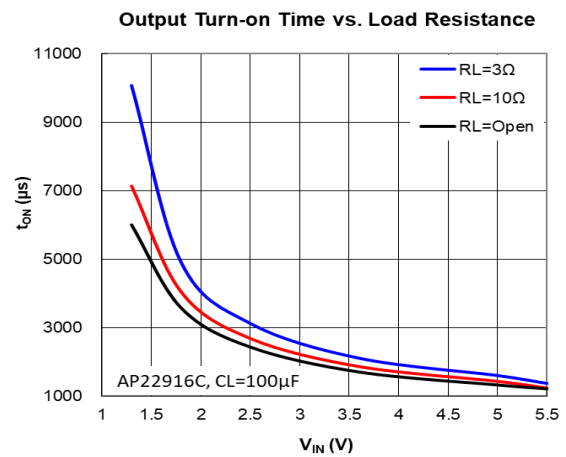
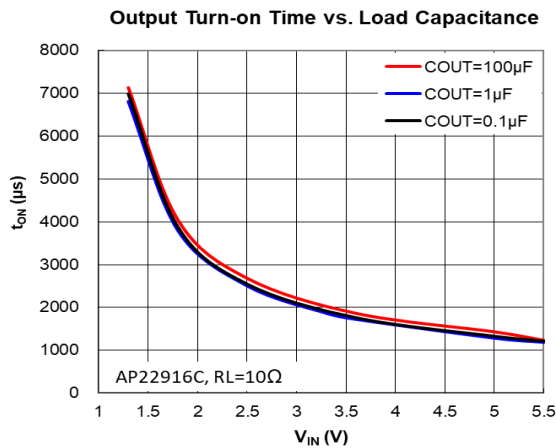
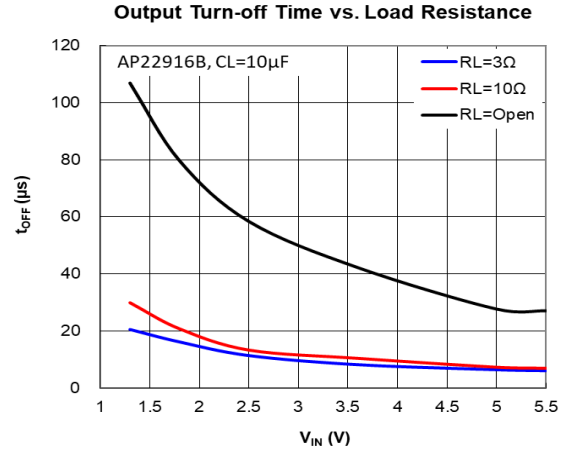
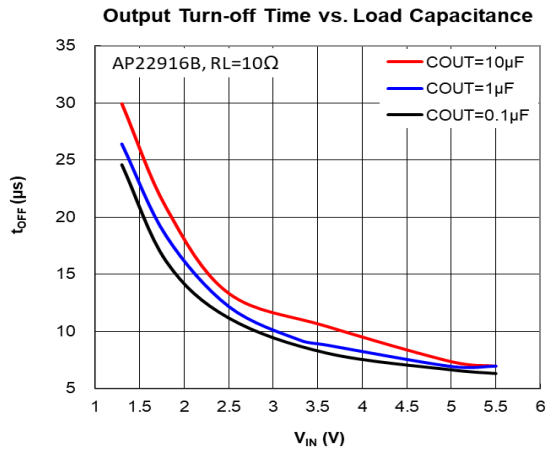
**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)



**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)

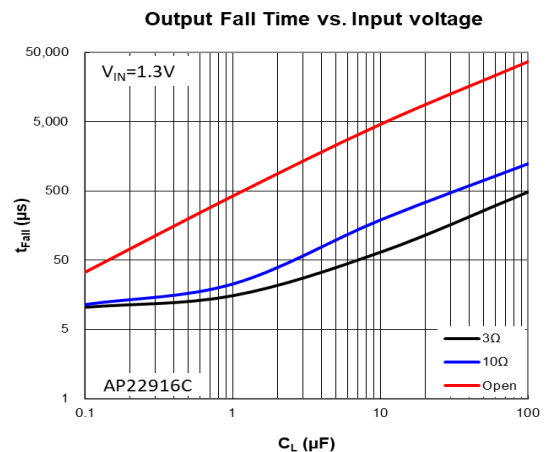
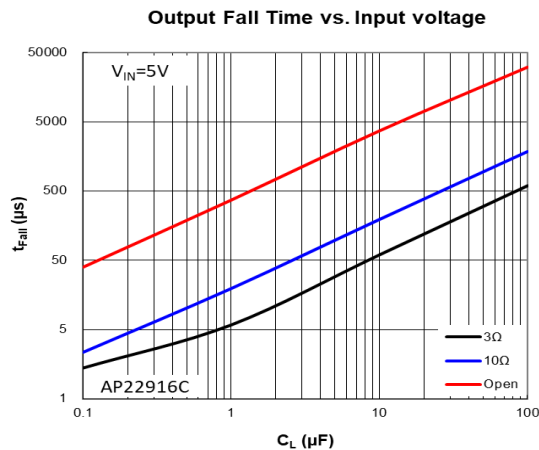
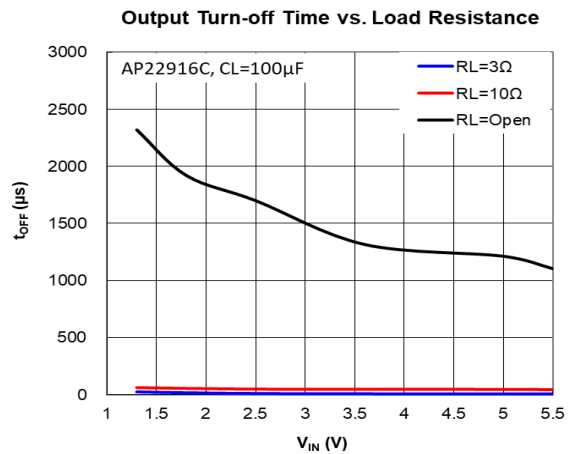
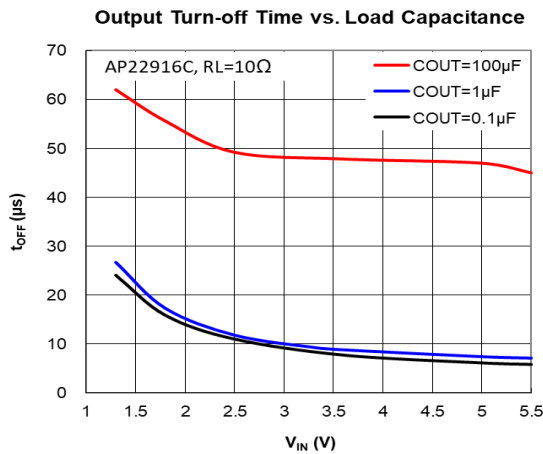
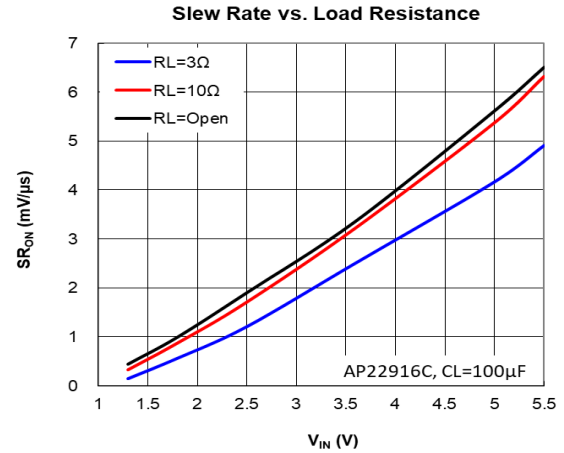
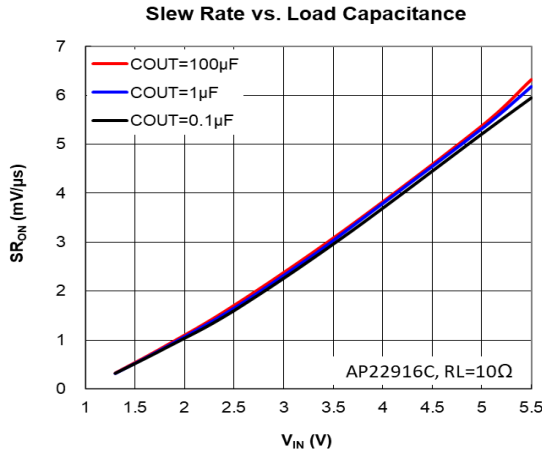


**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)

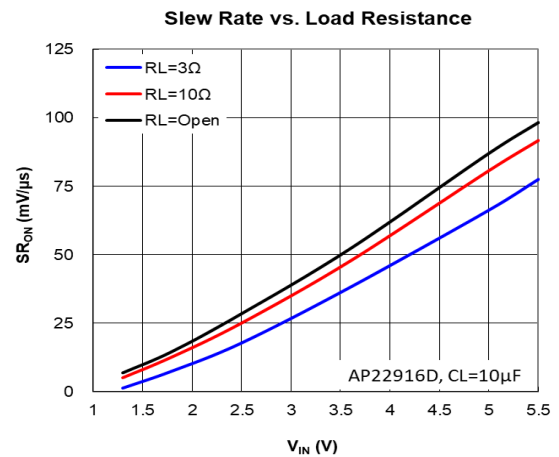
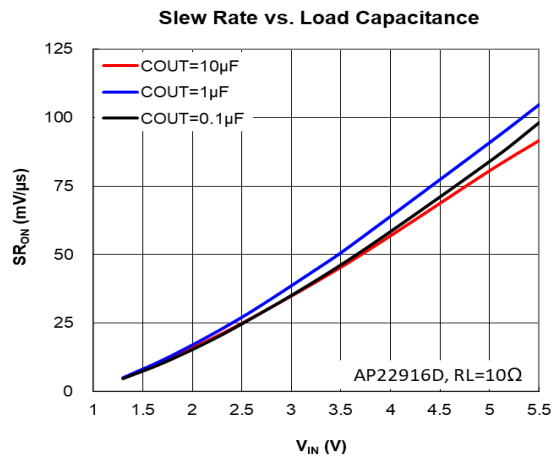
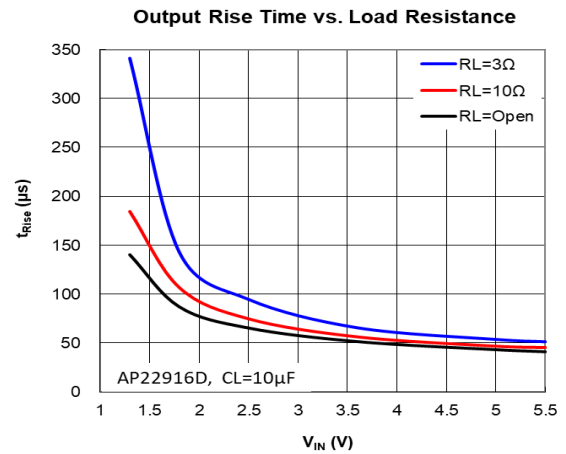
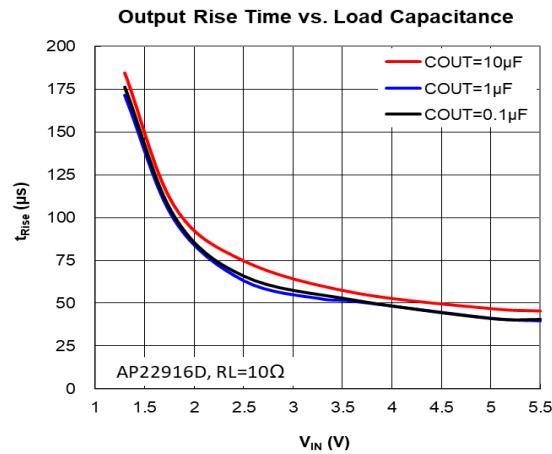
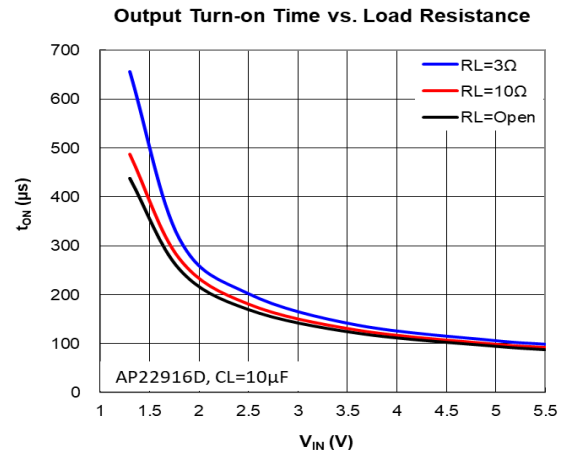
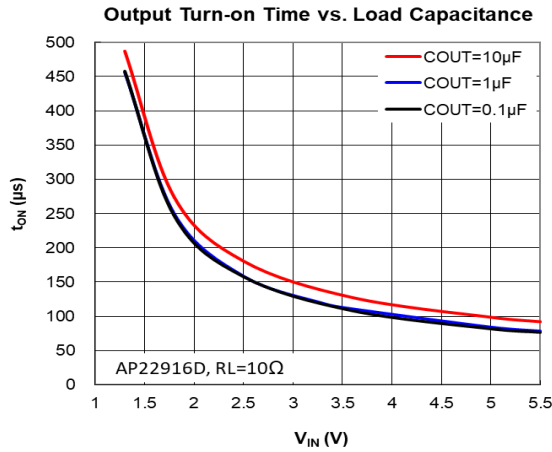




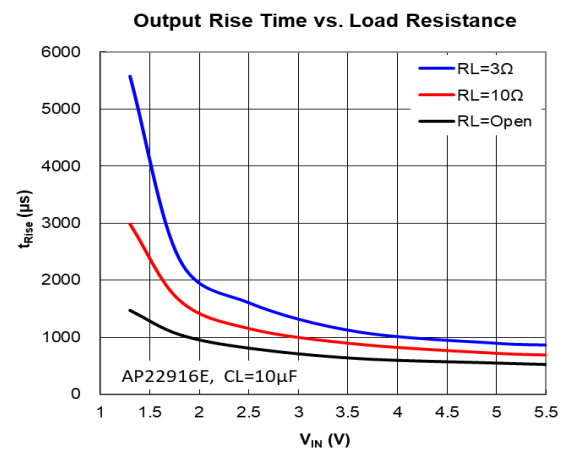
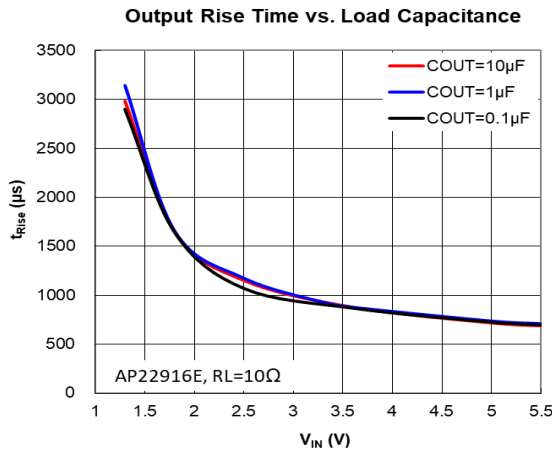
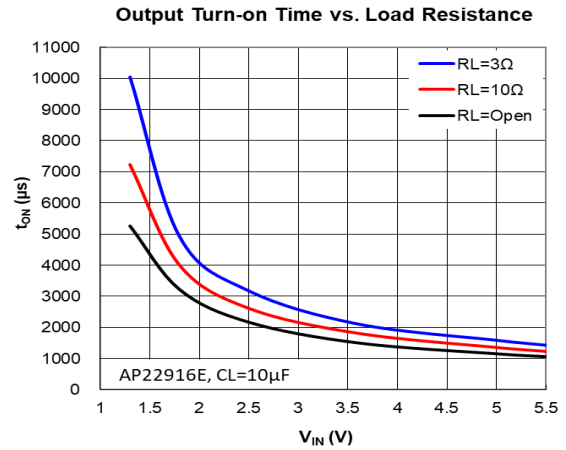
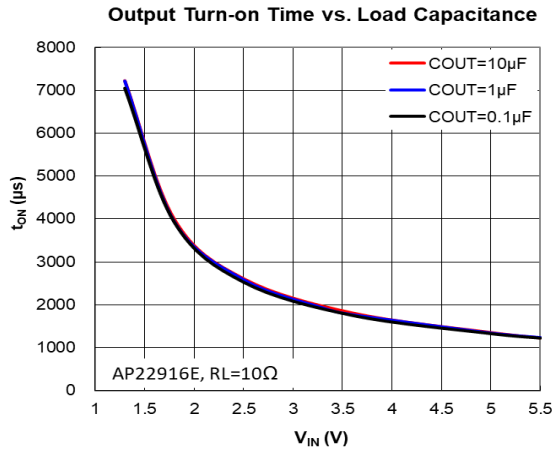
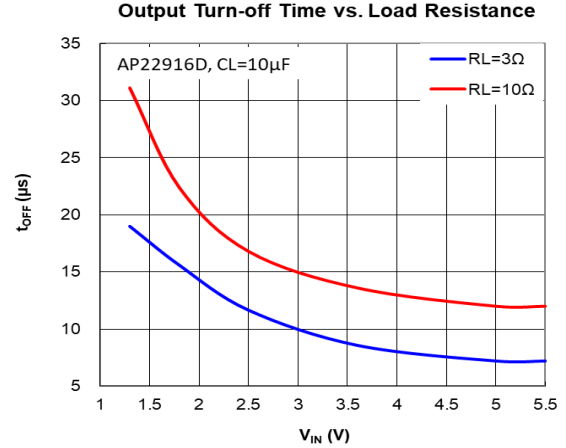
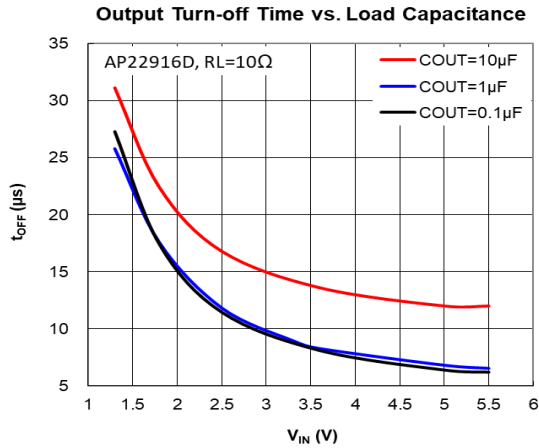
**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)



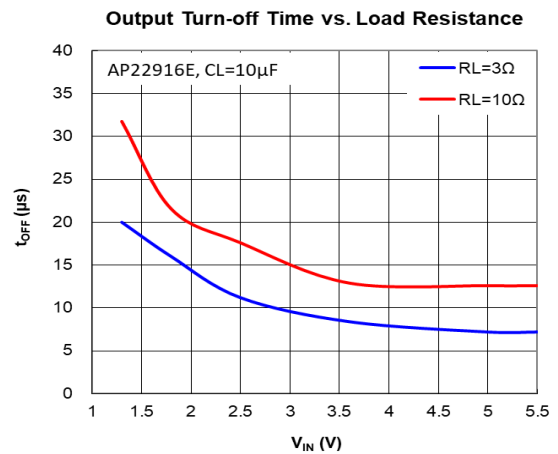
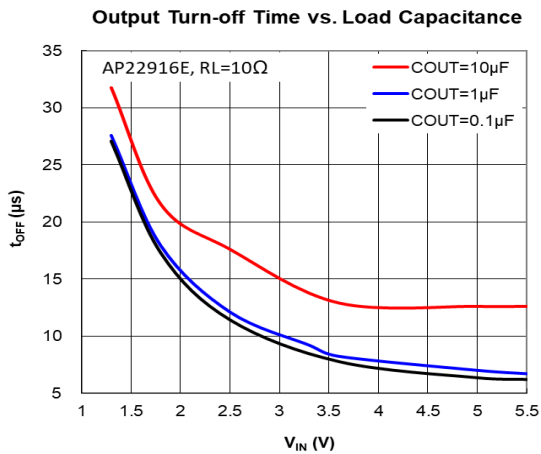
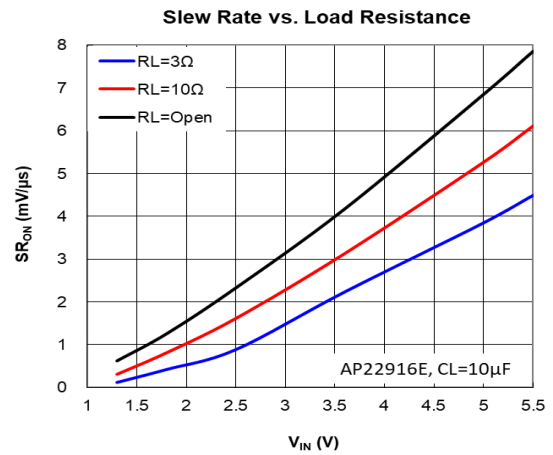
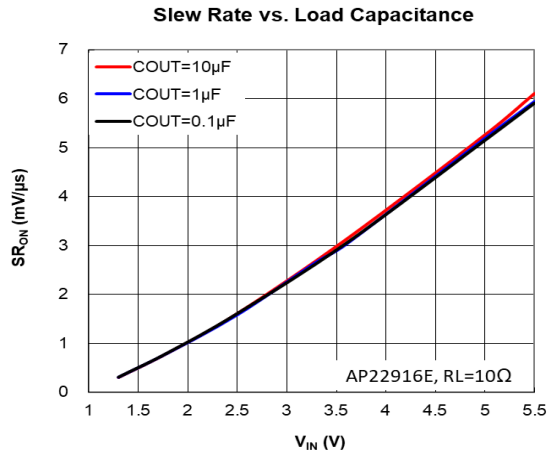
**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)



**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)



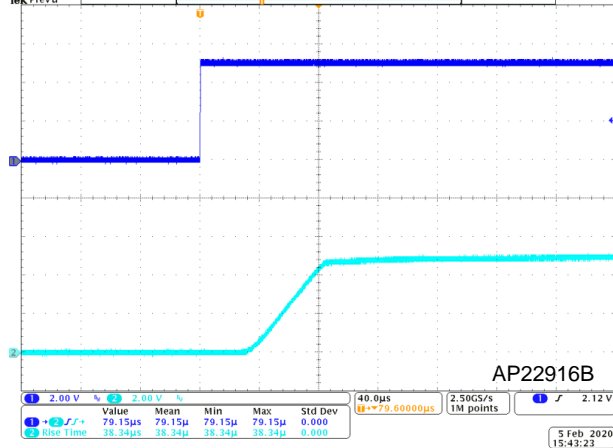
**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ . All devices in this section are for 25°C, unless otherwise specified.) (continued)



**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ , unless otherwise specified.)

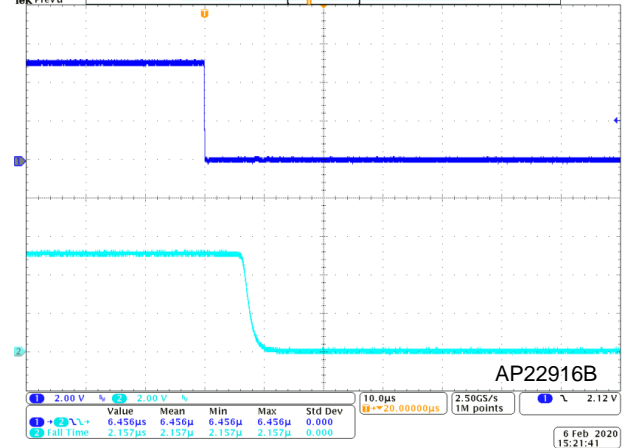
**Output Turn-On Response**

$V_{IN} = 5.0V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



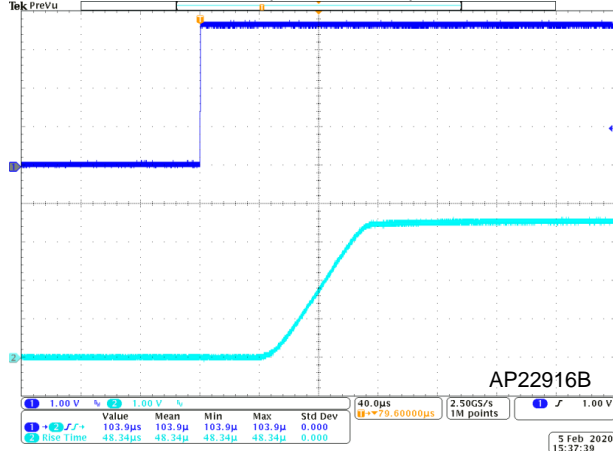
**Output Turn-Off Response**

$V_{IN} = 5.0V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



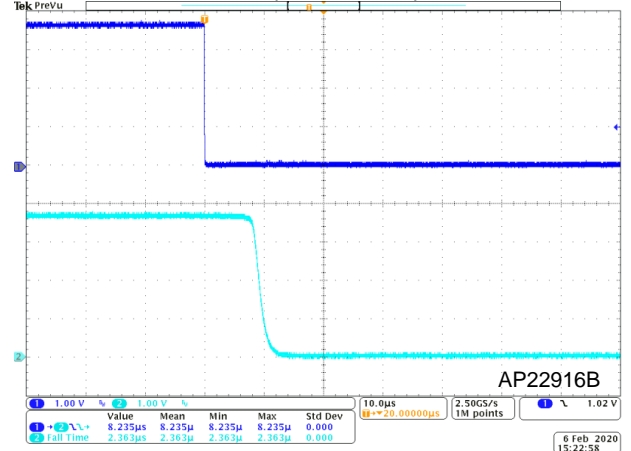
**Output Turn-On Response**

$V_{IN} = 3.6V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



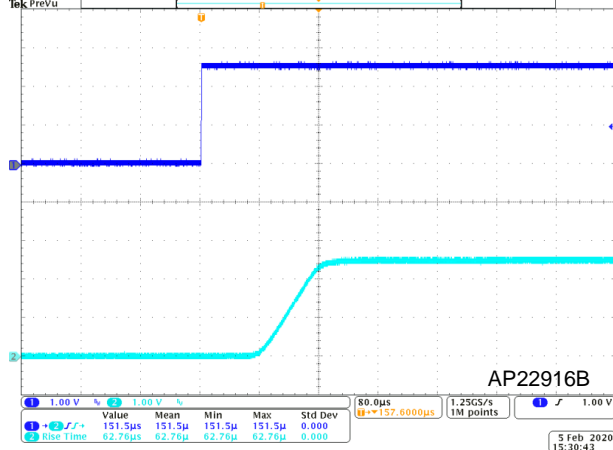
**Output Turn-Off Response**

$V_{IN} = 3.6V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



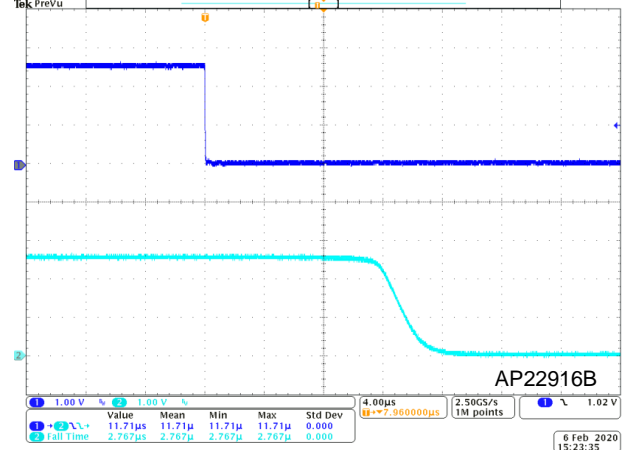
**Output Turn-On Response**

$V_{IN} = 2.5V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



**Output Turn-Off Response**

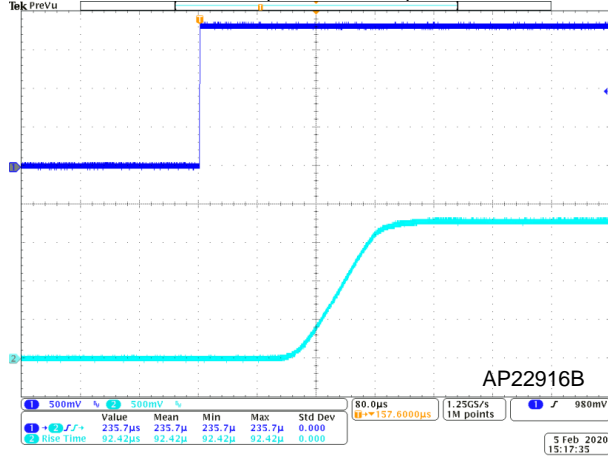
$V_{IN} = 2.5V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ , unless otherwise specified.) (continued)

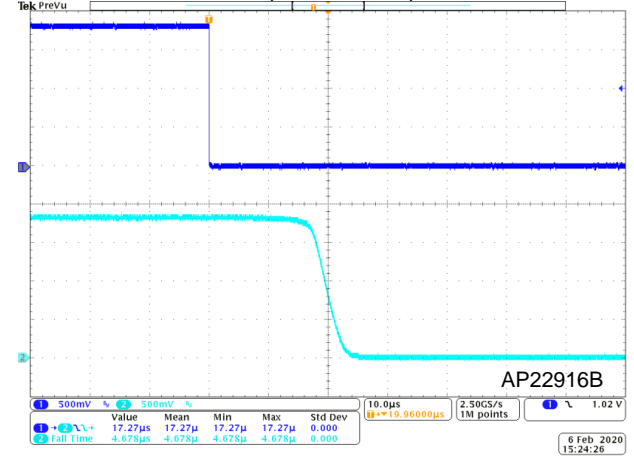
**Output Turn On Response**

$V_{IN} = 1.8V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



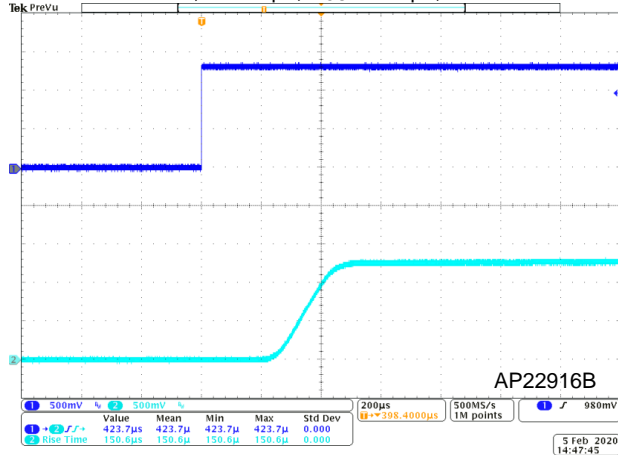
**Output Turn Off Response**

$V_{IN} = 1.8V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



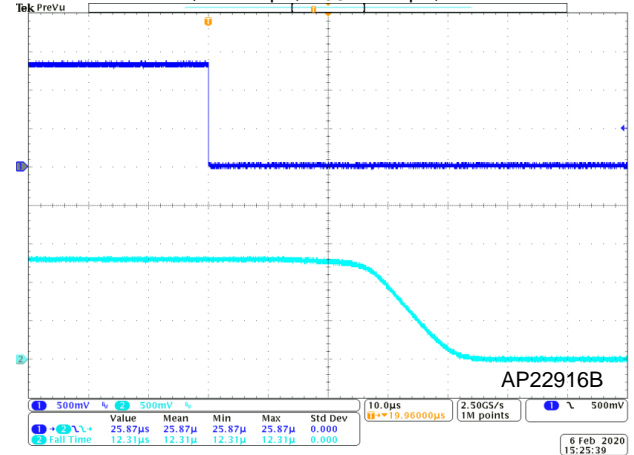
**Output Turn On Response**

$V_{IN} = 1.3V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



**Output Turn Off Response**

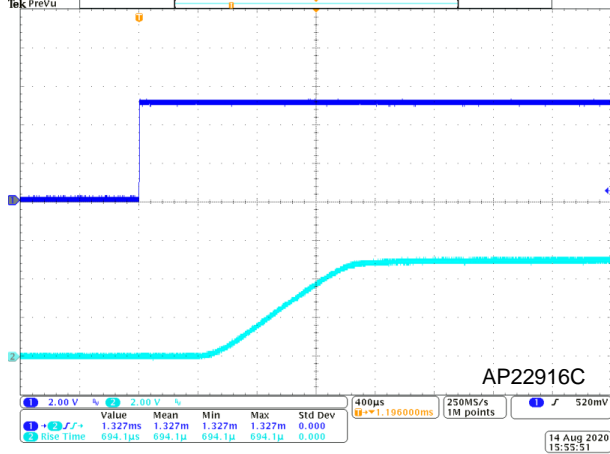
$V_{IN} = 1.3V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ , unless otherwise specified.) (continued)

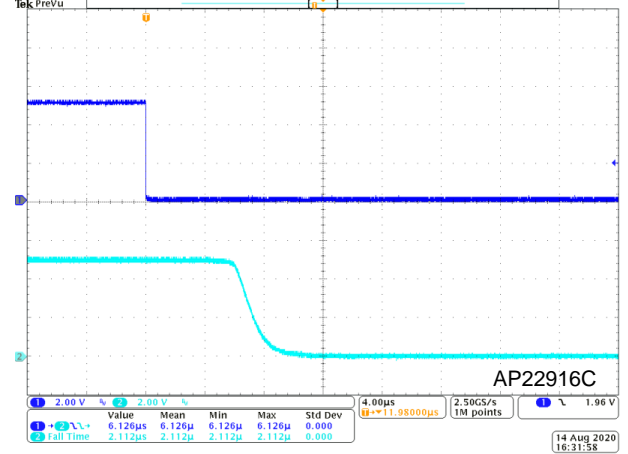
**Output Turn On Response**

$V_{IN} = 5.0V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



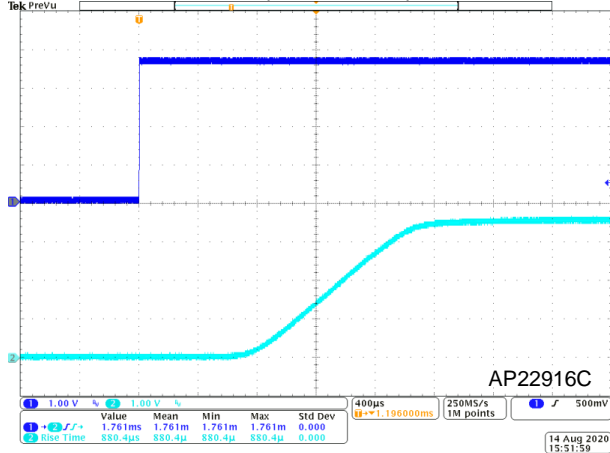
**Output Turn Off Response**

$V_{IN} = 5.0V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



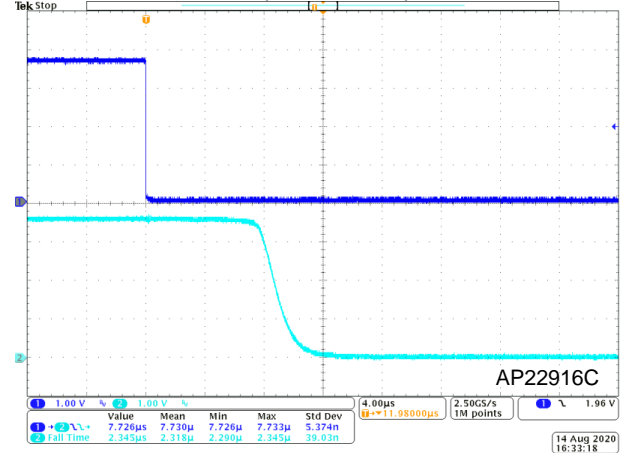
**Output Turn On Response**

$V_{IN} = 3.6V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



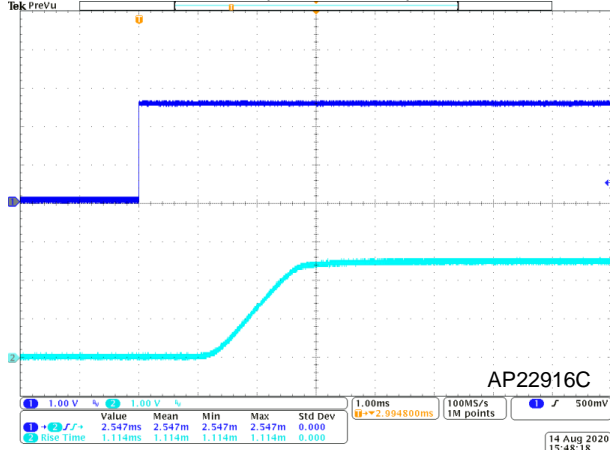
**Output Turn Off Response**

$V_{IN} = 3.6V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



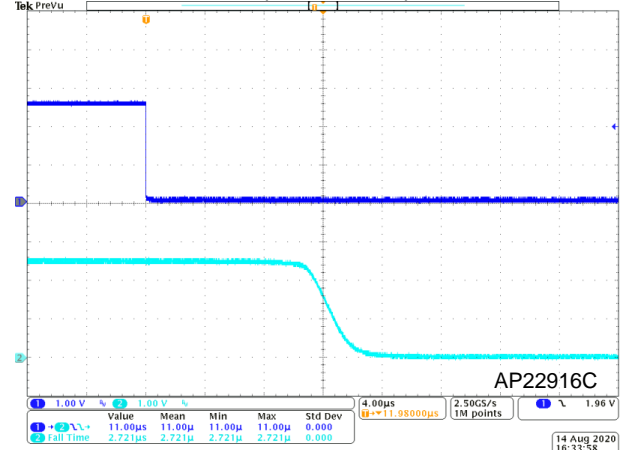
**Output Turn On Response**

$V_{IN} = 2.5V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



**Output Turn Off Response**

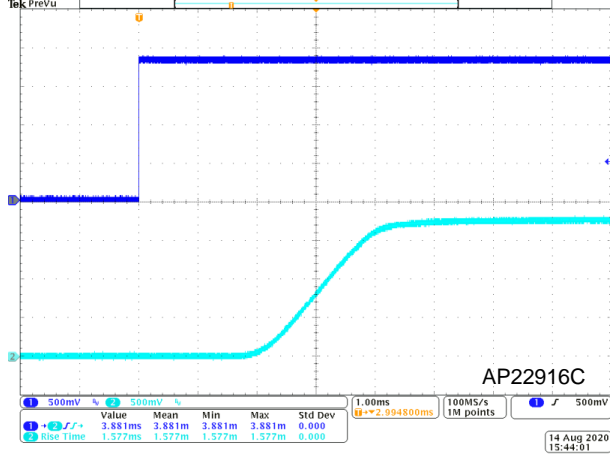
$V_{IN} = 2.5V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ , unless otherwise specified.) (continued)

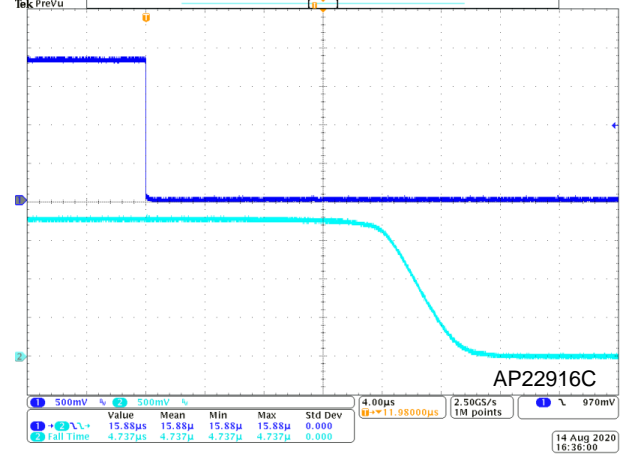
**Output Turn On Response**

$V_{IN} = 1.8V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



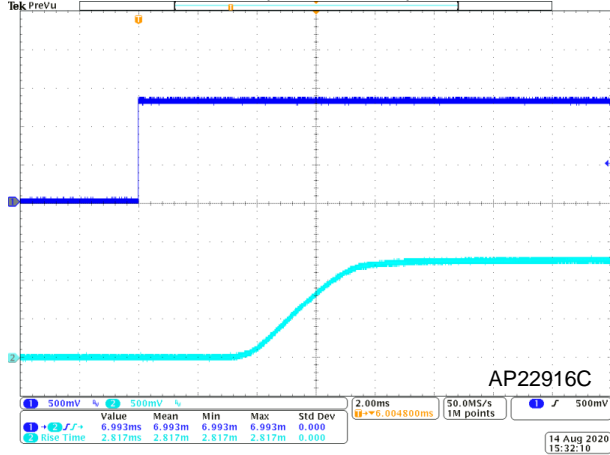
**Output Turn Off Response**

$V_{IN} = 1.8V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



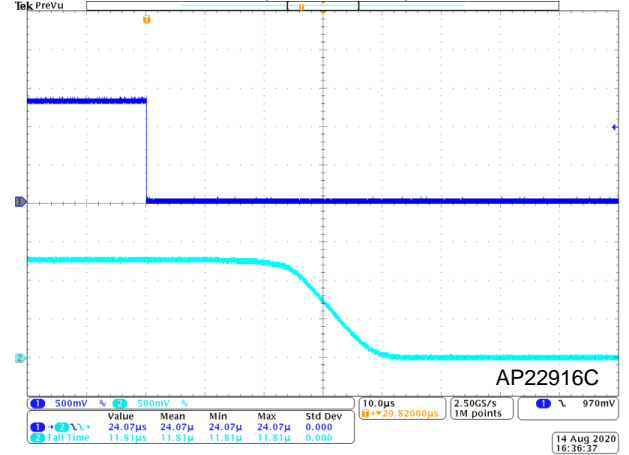
**Output Turn On Response**

$V_{IN} = 1.3V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



**Output Turn Off Response**

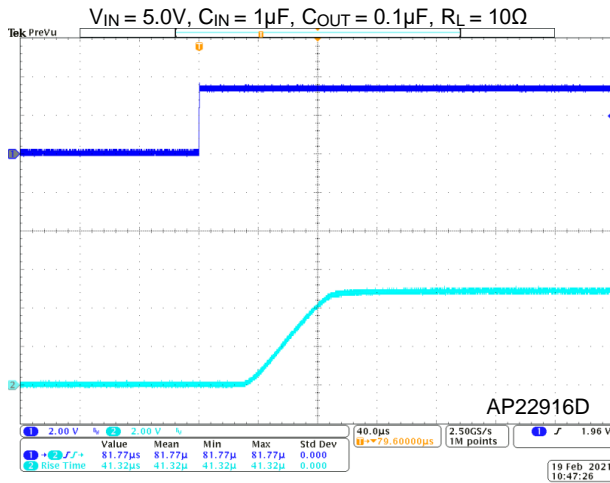
$V_{IN} = 1.3V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



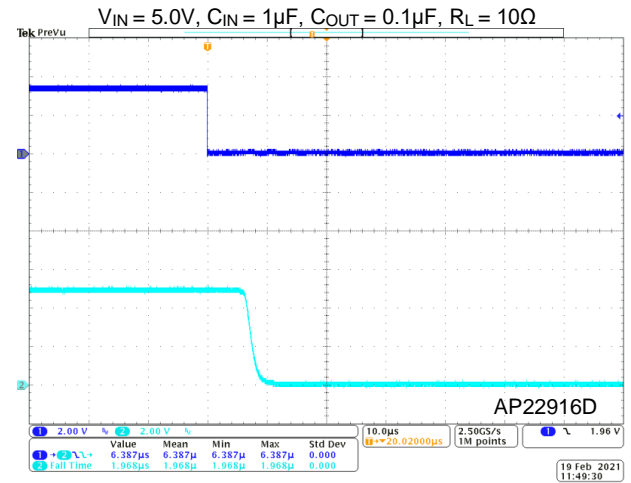


**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ , unless otherwise specified.) (continued)

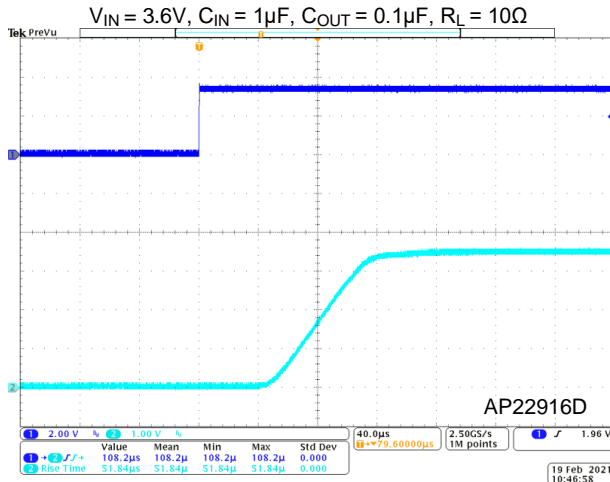
**Output Turn-On Response**



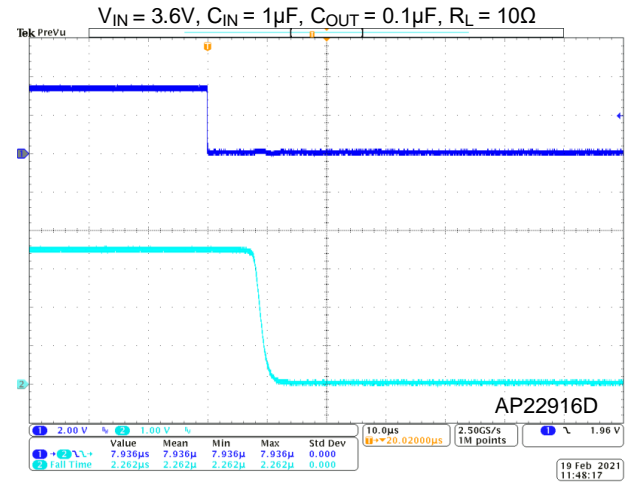
**Output Turn-Off Response**



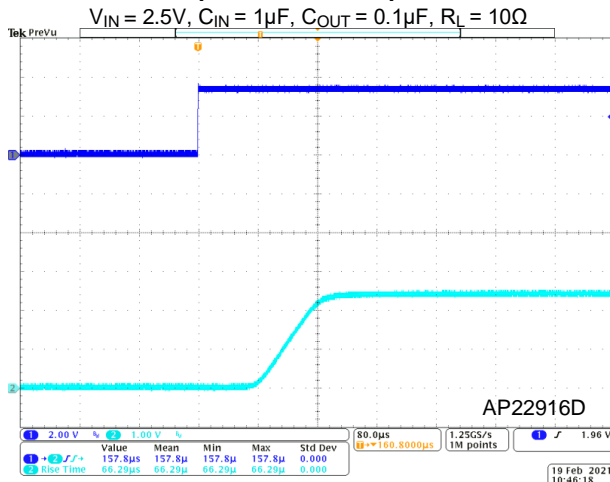
**Output Turn-On Response**



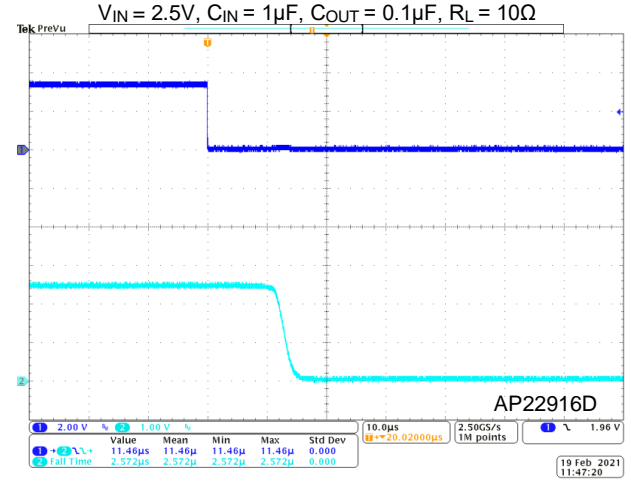
**Output Turn-Off Response**



**Output Turn-On Response**



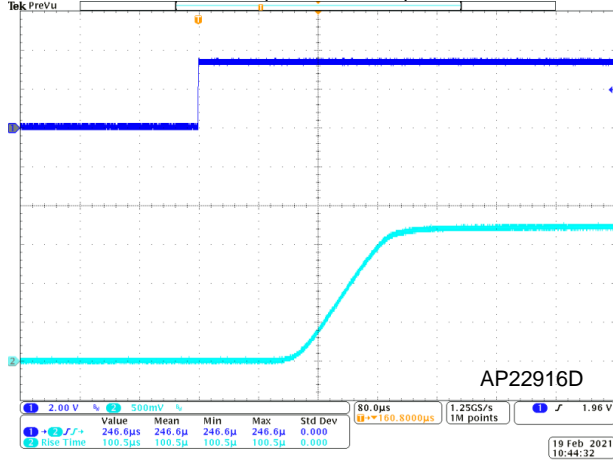
**Output Turn-Off Response**



**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ , unless otherwise specified.) (continued)

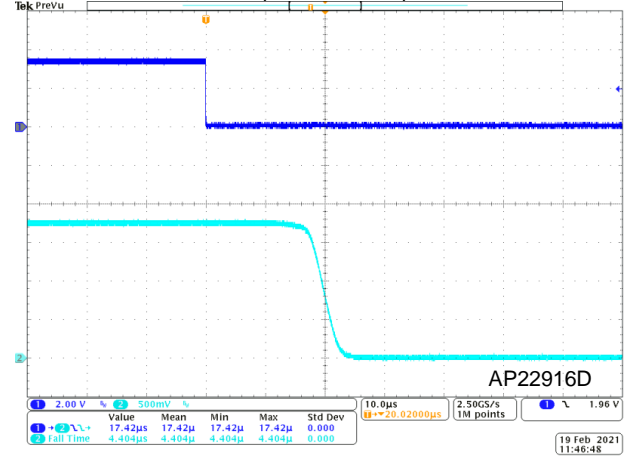
**Output Turn On Response**

$V_{IN} = 1.8V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



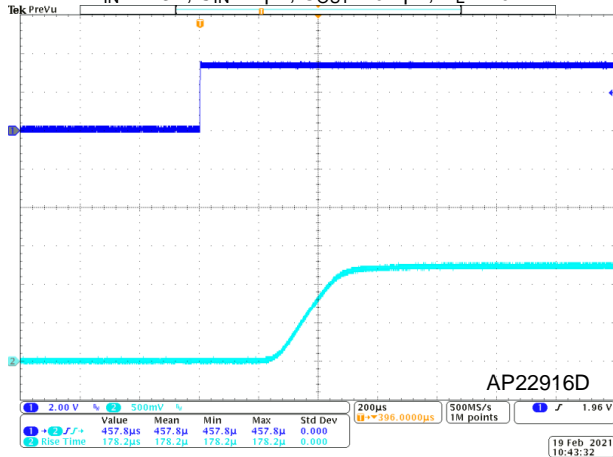
**Output Turn Off Response**

$V_{IN} = 1.8V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



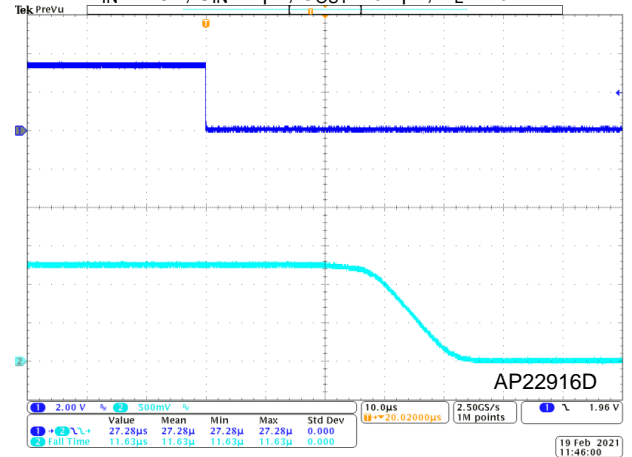
**Output Turn On Response**

$V_{IN} = 1.3V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



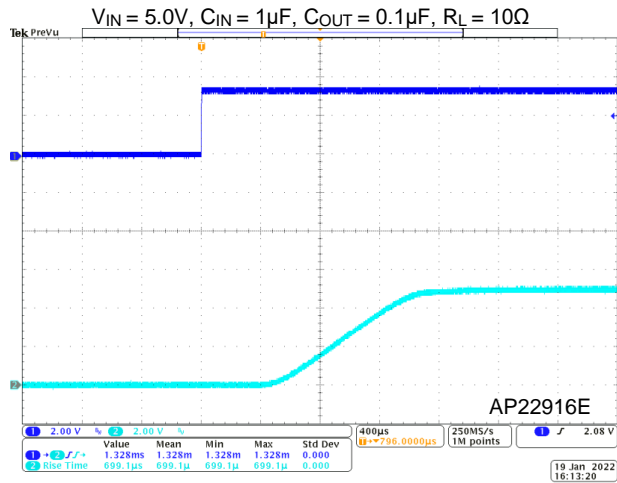
**Output Turn Off Response**

$V_{IN} = 1.3V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$

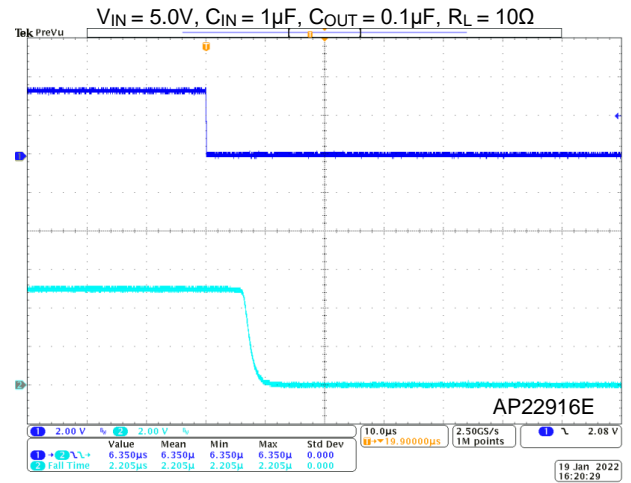


**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ , unless otherwise specified.) (continued)

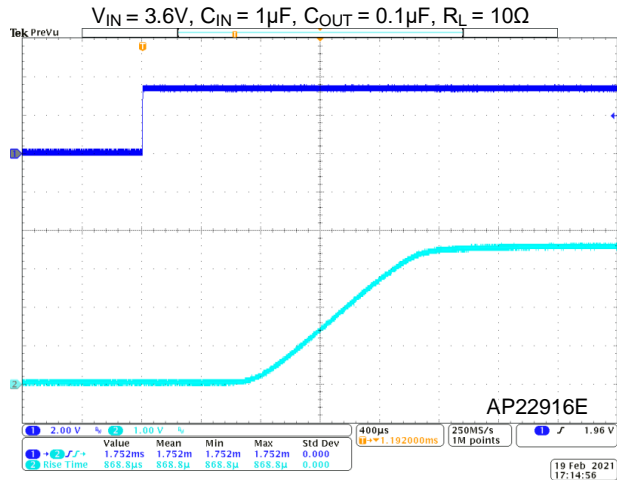
**Output Turn-On Response**



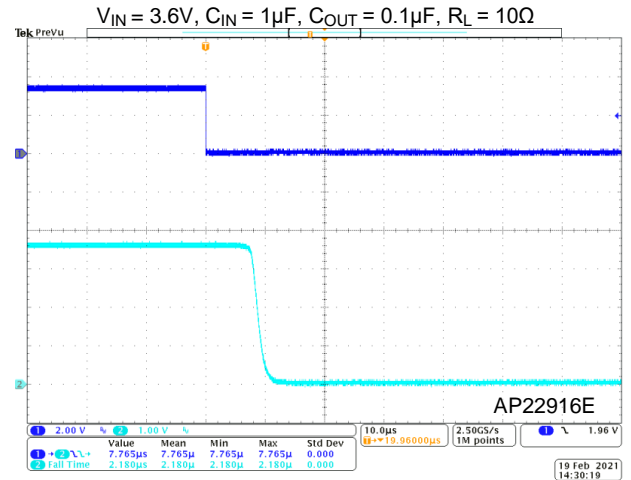
**Output Turn-Off Response**



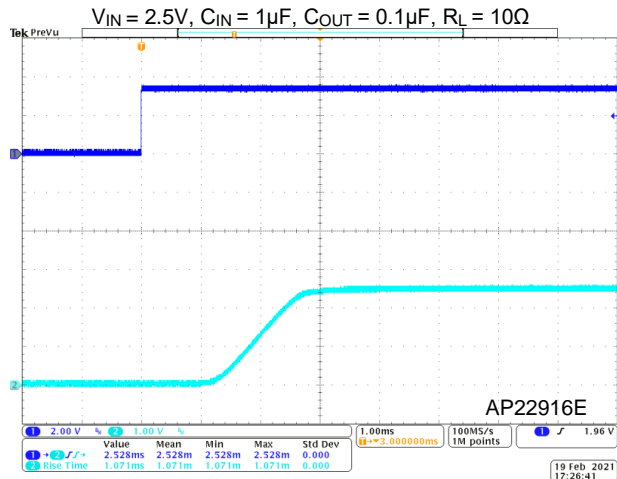
**Output Turn-On Response**



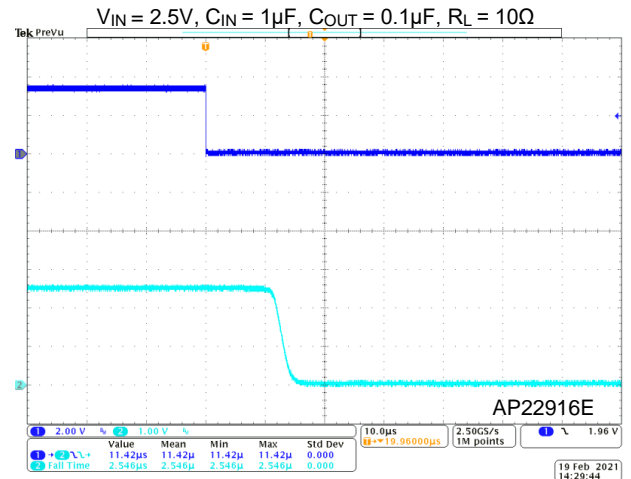
**Output Turn-Off Response**



**Output Turn-On Response**



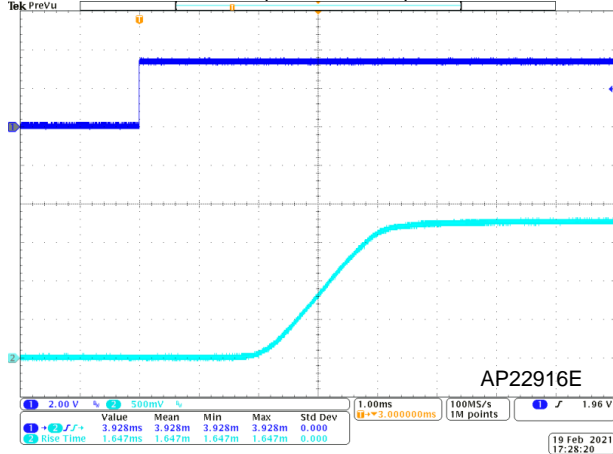
**Output Turn-Off Response**



**Typical Performance Characteristics** ( $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ , unless otherwise specified.) (continued)

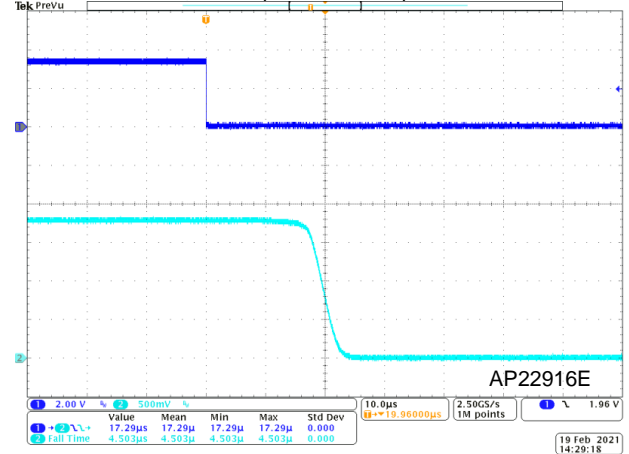
**Output Turn On Response**

$V_{IN} = 1.8V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



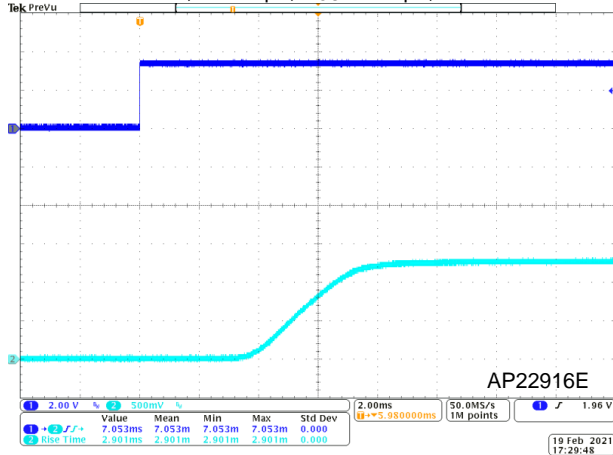
**Output Turn Off Response**

$V_{IN} = 1.8V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



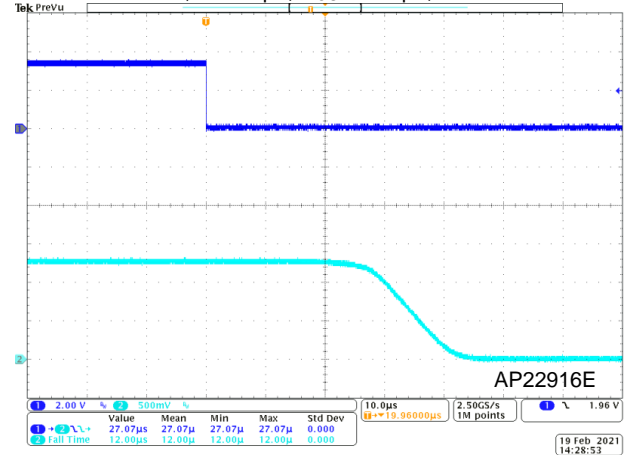
**Output Turn On Response**

$V_{IN} = 1.3V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



**Output Turn Off Response**

$V_{IN} = 1.3V$ ,  $C_{IN} = 1\mu F$ ,  $C_{OUT} = 0.1\mu F$ ,  $R_L = 10\Omega$



## Application Information

### Input Capacitor

A 1 $\mu$ F capacitor is recommended to connect between the  $V_{IN}$  and GND pins to decouple input power supply glitch and noise. The input capacitor has no specific type or ESR (equivalent series resistance) requirement. However, for higher current applications, ceramic capacitors are recommended due to their capability to withstand input current surges from low impedance sources, such as batteries in portable applications. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both  $V_{IN}$  and GND.

### Output Capacitor

A 0.1 $\mu$ F to 1 $\mu$ F capacitor is recommended to connect between the  $V_{OUT}$  and GND pins to stabilize and accommodate load transient condition. The output capacitor has no specific type or ESR requirement. The amount of capacitance may be increased without limit. For PCB layout, the output capacitor must be placed as close as possible to the  $V_{OUT}$  and GND pins, and the traces must be kept as short as possible.

### Enable/Shutdown Operation

The AP22916B/C is turned on by setting the ON pin high, and is turned off by pulling it low. To ensure proper operation, the signal source used to drive the ON pin must be able to swing above and below the specified turn-on/off voltage thresholds listed in the *Electrical Characteristics* section under  $V_{IL}$  and  $V_{IH}$ .

### True Reverse Current Blocking

An internal reverse voltage comparator disables the power-switch when the output voltage ( $V_{OUT}$ ) is driven higher than the input voltage ( $V_{IN}$ ), by  $V_{RCB}$ , to quickly (10 $\mu$ s typ.) stop the flow of current towards the input side of the switch.

Reverse current protection is always active, even when the power switch is disabled. Additionally, undervoltage lockout (UVLO) protection turns the switch off if the input voltage is too low.

### Discharge Operation

The AP22916/C offers a discharge option that helps to discharge the output charge when disabled.

### Power Dissipation

The maximum IC junction temperature should be restricted to +125°C under normal operating conditions. The device power dissipation and proper sizing of the thermal plane are critical to avoid thermal shutdown and ensure reliable operation. Power dissipation of the device depends on input voltage and load conditions, and can be calculated by:

$$P_D = I_{OUT}^2 \times R_{DS(on)} \quad (1)$$

However, the maximum power dissipation that can be handled by the device depends on the maximum junction to ambient thermal resistance, maximum ambient temperature, and maximum device junction temperature, which can be approximated by the equation below:

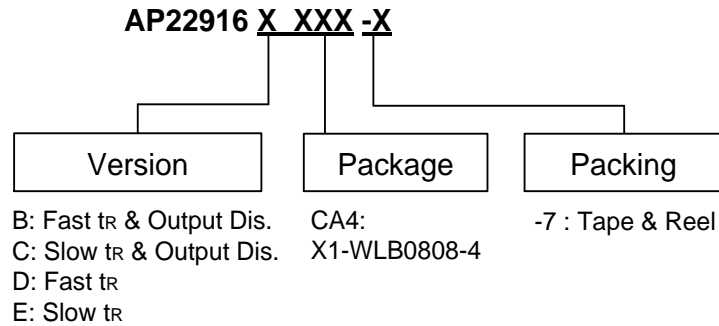
$$P_{D(MAX)} = \frac{(125^\circ\text{C} - T_A)}{\theta_{JA}} \quad (2)$$

### Layout Guideline

Good PCB layout is important for improving the thermal performance of the device. All trace lengths should be kept as short as possible. The input ( $V_{IN}$ ) and output ( $V_{OUT}$ ) PCB traces should be as wide as possible to reduce stray impedance.

Use a ground plane to enhance the power dissipation capability of the device if applicable. Place input and output capacitors close to the device to minimize the effects of parasitic inductance.

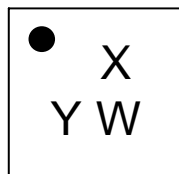
## Ordering Information



Part Number	Package Code	Package	7" Tape and Reel	
			Quantity	Part Number Suffix
AP22916BCA4-7	CA4	X1-WLB0808-4	3,000/Tape & Reel	-7
AP22916CCA4-7	CA4	X1-WLB0808-4	3,000/Tape & Reel	-7
AP22916DCA4-7	CA4	X1-WLB0808-4	3,000/Tape & Reel	-7
AP22916ECA4-7	CA4	X1-WLB0808-4	3,000/Tape & Reel	-7

## Marking Information

(Top View)



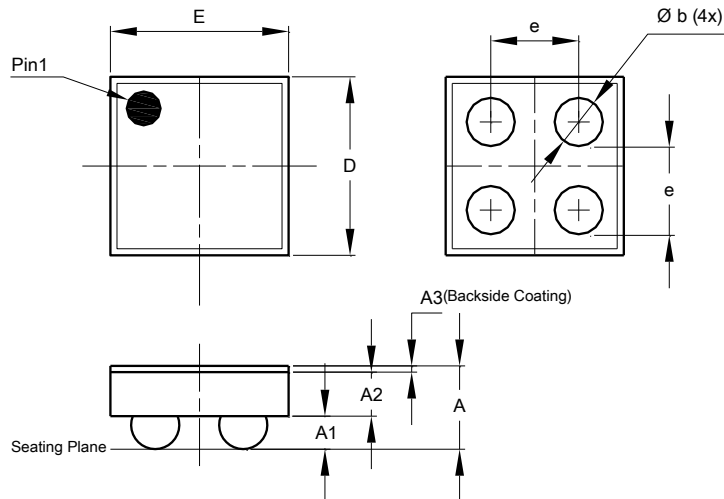
X : Identification Code  
Y : Year : 0~9  
W : Week : A~Z : 1~26 week;  
a~z : 27~52 week; z represents  
52 and 53 week

Part Number	Package	Identification Code
AP22916BCA4-7	X1-WLB0808-4	5
AP22916CCA4-7	X1-WLB0808-4	6
AP22916DCA4-7	X1-WLB0808-4	7
AP22916ECA4-7	X1-WLB0808-4	8

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### X1-WLB0808-4

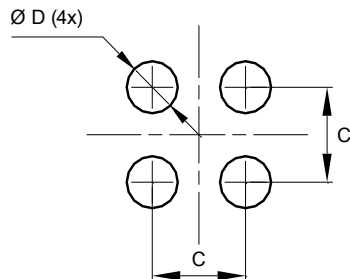


X1-WLB0808-4			
Dim	Min	Max	Typ
A	0.3320	0.4180	0.3750
A1	0.1350	0.1650	0.1500
A2	0.1750	0.2250	0.2000
A3	0.0220	0.0280	0.0250
b	0.1971	0.2409	0.2190
D	0.7900	0.8300	0.8100
E	0.7900	0.8300	0.8100
e	0.400 BSC		
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

### X1-WLB0808-4



Dimensions	Value (in mm)
C	0.4000
D	0.2190

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