



The 74HCS164 is an 8-bit serial-in/parallel-out shift register. The device features two serial data inputs (DSA and DSB), eight parallel data outputs (Q0 to Q7). Data is entered serially through DSA or DSB and either input can be used as an active HIGH enable for data entry through the other input. Data is shifted on the LOW-to-HIGH transitions of the clock (CP) input. A LOW on the master reset input ($\overline{\text{MR}}$) clears the register and forces all outputs LOW, independently of other inputs. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

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Ordering

Applications

- Serial-to-parallel data conversion
- Remote control holding register
- Output expansion
- LED matrix control
- 7-segment display control
- 8-bit data storage

Type number	V _{CC} (V)	Logic switching levels	Output drive capability (mA)	t _{pd} (ns)	f _{max} (MHz)	Nr of bits	Power dissipation considerations	T _{amb} (°C)	Package name
74HCS164PW	2.0 - 6.0	CMOS	± 7.8	7	97	8	low	-40~125	TSSOP14

Type number	Orderable part number, (Ordering code (12NC))	Status	Marking	Package	Package information	Reflow-/Wave soldering	Packing
74HCS164PW	74HCS164PWJ (935691501118)	Active	HCS164	 TSSOP14 (SOT402-1).	SOT402-1	SSOP-TSSOP-VSO-WAVE	SOT402-1_118

Type number	Orderable part number	Chemical content	RoHS	RHF-indicator
74HCS164PW	74HCS164PWJ	74HCS164PW		

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- HC(T)
- TSSOP
- HCS







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