







7.0 x 5.0 x 1.7 mm LCC Ceramic Package

## **Features**

- Quartz crystal controlled Precision Square Wave Oscillator
- PECL Differential Output
- Enable/Disable Function on pad 1
- Low Jitter
- 3.3V nominal Supply Voltage
- 25-220 MHz Frequency Range

## **Applications**

PON

Driving A/Ds, D/As, FPGAs Fibre Channel Ethernet, GbE, SynchE Medical Storage Area Networking COTS Telecom

Electrical Characteristics					
Parameter	Min	Тур	Max	Unit	Condition
Frequency Range <sup>2</sup>	25	-	220	MHz	Consult factory for other options
Frequency Stability $^2$ $\pm 20 = 20^*$ , $\pm 25 = 44$ , $\pm 50 = 45$	±20		±50	ppm	Includes supply voltage change, load changes, aging for 1 year at 25°C ± 2°C, shock, vibration and temperatures. *limited frequencies, see page 2
Operating Temperature Range <sup>2</sup>	-10 -20 -40	-	+70 +70 +85	°C	Standard range Extended range C option Extended range E option
Supply Voltage <sup>1, 2</sup> V <sub>CC</sub>	2.97	3.30	3.63	V	
Supply Current I <sub>CC</sub>	-	-	60	mA	
Output Waveform		Pl	ECL		
Output High Level V <sub>OH</sub>	2.275	2.350	2.420	V	Referenced to Ground
Output Low Level V <sub>OL</sub>	1.490	1.600	1.680	٧	Referenced to Ground
Output T <sub>RISE</sub> and T <sub>FALL</sub>	-	-	0.5	ns	Vth is 20% and 80% of output swing
Start Up Time	-	-	10	ms	Time for output to reach specified frequency
Duty Cycle	45	-	55	%	At output crossing point
V <sub>DISABLE</sub> VIL	-	-	0.3Vcc	V	Defended the survived
V <sub>ENABLE</sub> VIH	0.9Vcc	-		V	Referenced to ground
Enable Time	-	-	2	ms	Time for output to reach a logic high state
Disable Time	-	-	200	ns	Time for output to reach a high Z state
Enable/Disable Internal Pull-up	30	70	150	ΚΩ	Pin 1 open or high
Output Leakage $V_{OUT} = V_{CC}$ $V_{OUT} = 0V$	- -10	-	+10 -	μΑ	Pad 1 low, device disabled
Standby Current	-	-	15	μΑ	
	-	0.1	-		12 kHz to 20 MHz from the output frequency at 156.25 MHz
Jitter	-	0.8	-	ps	10 Hz to 1 MHz from the output frequency at 156.25 MHz
Phase Noise 10 Hz 100 Hz 1 kHz 10 kHz 100 kHz 1 MHz 20MHz	-	-75 -104 -127 -141 -153 -155 -156	-	dBc/Hz	At 25°C, 156.25 MHz
Storage Temperature Range	-55	-	+125	°C	

<sup>2</sup> Specified by part number

Product information is current as of publication date. The product conforms to specifications per the terms of the Pletronics standard warranty. Jan 24, 2024 Rev. G Production processing does not necessarily include testing of all parameters.

Notes: Specifications with Pad 1 E/D open circuit

1 Place an appropriate power supply bypass capacitor next to device for correct operation



Part N	umber					
Series Model	Frequency Stability		Operating Temperature Range	Supply Voltage V <sub>cc</sub>	Frequency in MHz	Optional T&R Packaging code
PE77	45	J	E	V	- 125.0M	-XX
	45 = ± 50 ppm (STD) 44 = ± 25 ppm 20* = ± 20 ppm		Blank = -10 to +70°C (STD) C = -20 to +70°C E = -40 to +85°C	<b>V</b> = 3.3V ± 10%	25 - 220 MHz	T250 = 250 per Reel T500 = 500 per Reel T1K = 1000 per Reel (Std)

<sup>\*</sup> Contact PLE sales for limited frequencies. Full frequency range available which excludes aging.

## **Device Marking**

PLE PE77 FFF.FF M • YMDxx PE7xYWWxx FFF.FF M • PLExx PLE = Pletronics
FFF.FF = Frequency in MHz

YMD or YWW = Date Code, All other marking is internal codes

Note: Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

### Codes for Date Code YMD (Year Month Day)

Code	3	4		5	6	7	Code	. 4	١.	В	С	D	Е	F		G	Н	J	K	L	М
Year	2023	2024	1 20	)25	2026	2027	Montl	h JA	N F	EB	MAR	APR	MAY	′ JUI	١,	JUL	AUG	SEP	OCT	NOV	DEC
				•				•		•			•		•			•	•	•	•
Code	1	2	3	4	5	6	7	8	9	Α	В	С	D	E	F	G	•				
Day	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	3				
Code	Н	J	K	L	М	N	Р	R	Т	U	V	w	Х	Υ	Z						
Day	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31						

### **Package Labeling**

P/N Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

RoHS Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

### Pletronics Inc. certifies this device is in accordance with the RoHS and REACH directives.

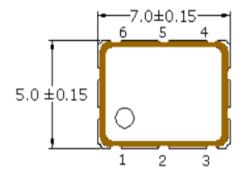
Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.16 grams

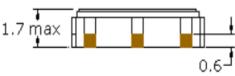
Moisture Sensitivity Level: 1 As defined in J-STD-020D

Second Level Interconnect code: e4



## **Mechanical Dimensions**





# 0.6 - 1.4 typ - 0.6 ref typ - 0.1 ref typ - 1.1 typ

Dimensions in mm

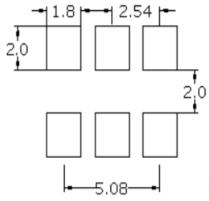
1.138 typ-

## Pad Connections

Pad	Function
1	Enable/Disable
2	No Connect
3	Ground/Lid
4	Output
5	Output-N
6	νω

Pad 1 Output  VIH/Open Active				
	Pad 1	Output		
	Vɪн/Open	Active		
	VIĻ/Gnd	Disabled/Tristate		

## Solder pad layout



**Pad Layout** 

Disclaimer: Recommended layout shown. Adjust layout as needed for individual process requirements.

Contacts (pads): Gold (0.3 to 1.0  $\mu$ m) over Nickel (1.27 to 8.89  $\mu$ m)

For Optimum Jitter Performance, Pletronics recommends:

--5.08 typ--

- A ground plane under the device
- Do not route large transient signals (both current and voltage) under the device
- Do not place near a large magnetic field such as a high frequency switching power supply

2.6 typ

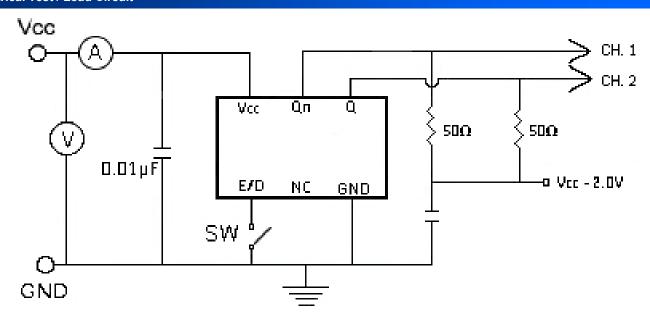
2.54 typ

Do not place near piezoelectric buzzers or mechanical fans

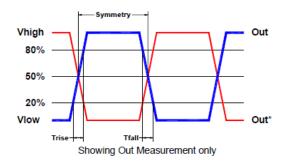
Production processing does not necessarily include testing of all parameters.



## **Electrical Test / Load Circuit**



## **Test Waveform**



## **Environmental / ESD Ratings**

Reliability: Environmental

Parameter	Condition
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	IPC J-STD-002
Thermal Cycle	MIL-STD-883 Method 1010, Condition B

## Thermal Characteristics:

The maximum die or junction temperature is 150°C

## **ESD Rating**

Model	Min. Voltage	Condition
Human Body Model	2000V	JESD22-A114
Machine Model	200V	JESD22-A115

### **Absolute Maximum Ratings**

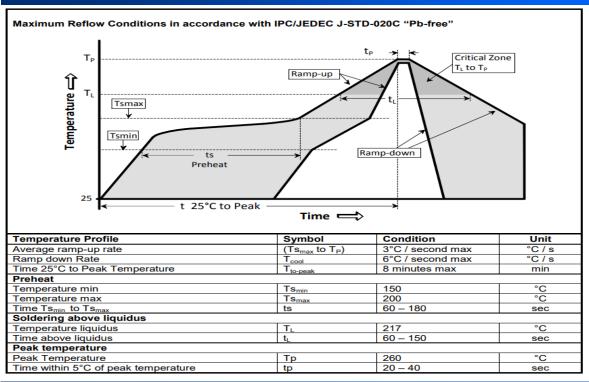
Parameter	Unit
V <sub>CC</sub> Supply Voltage	-0.3V to +4.0V
Vi Input Voltage	-0.3V to V <sub>CC</sub> + 0.3V
Vo Output Voltage	-0.3V to V <sub>CC</sub> + 0.3V

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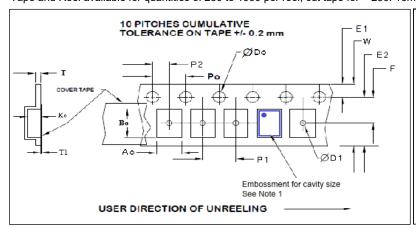
## **Reflow Cycle**



The part may be reflowed 2 times without degradation (typical for lead free processing).

## Tape and Reel

Tape and Reel available for quantities of 250 to 1000 per reel, cut tape for < 250. 16mm tape, 8mm pitch.



A B	

	Tape Variable Dimensions Table 2									
Tape Size	E2 typ	F	P1	W max	Ao	Во	Ko			
16mm	14.25	7.5 ±0.05	8.0 ± 0.1	16.3	5.56±0.1	7.85±0.1	2.0±0.1			

Dimensions i	in mm Drawing Not to scale
Note 1: Embossed cavity to con	form to EIA- 481-B

Tape Constant Dimensions Table 1										
Tape Size	Tape Do D1 typ E1 Po P2 T T1 Max max									
16mm	1.5 +0.1 -0.0	1.5	1.75 ±0.1	4.0 ±0.1	2.0 ±0.1	0.3	0.1			

Reel Dimensions (may vary) Table 3						
	А		В		С	D
Reel Size	Inches	mm	Inches	mm	mm	mm
7	7.0	180	2.50	60	13.0	Tape size +0.4
13	13.0	330	4	100	+0.5 -0.2	+2.0 -0.0

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Contacting Pletronics Inc.

Pletronics, Inc. 19013 36th Ave. West Lynnwood, WA 98036-5761 U.S.A.

Tel: 425.776.1880 Fax: 425.776.2760

email: ple-sales@pletronics.com URL: www.pletronics.com