

NCV74204V1GEVB: 3.3 V / 50 mA LDO Evaluation Board

The NCV74204V1GEVB is a Reference Design for LIN transceiver with integrated low drop voltage regulator. The reference design can be interfaced via IO pins to an external microcontroller or simple switch board. The boards are intended to give designers easy, quick and convenient means for evaluation of NCV7420 devices. The design incorporates the lowest bill of material and small board size.



Evaluation/Development Tool Information				
Product	Status	Compliance	Short Description	Parts Used
NCV74204V1GEVB	Active	Pb-free	3.3 V / 50 mA LDO Evaluation Board	NCV7420D24G , NCV7420D24R2G
				Contact Local Sales Office

Technical Documents			
Type	Document Title	Document ID/Size	Rev
Eval Board: Manual	NCV7420 LIN Transceiver with Voltage Regulator Evaluation Board User's Manual	EVBUM2044/D - 466.0 KB	0
Eval Board: BOM	NCV74204V1GEVB Bill of Materials ROHS Compliant	NCV74204V1GEVB_BOM_ROHS.PDF - 100.0 KB	0
Eval Board: Gerber	NCV74204V1GEVB Gerber Layout Files (Zip Format)	NCV74204V1GEVB_GERBER.ZIP - 117.0 KB	0
Eval Board: Schematic	NCV74204V1GEVB Schematic	NCV74204V1GEVB_SCHEMATIC.PDF - 192.0 KB	0
Eval Board: Test Procedure	NCV74204V1GEVB Test Procedure	NCV74204V1GEVB_TEST_PROCEDURE.PDF - 405.0 KB	0

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