

## Product Summary (@T<sub>A</sub> = +25°C)

V <sub>RRM</sub> (V)	I <sub>o</sub> (A)	V <sub>F</sub> max (V)	I <sub>R</sub> max (μA)
40	3	0.53	30

## Description

Packaged in the compact thermally efficient POWERDI-123 package, the SBRT3M40P1 provides very low reverse leakage and excellent V<sub>F</sub> stability at high temperatures. It is ideally suited to use as a rectifier diode in MR16 bridge rectifier applications.

## Applications

- Bridge Diodes
- Blocking Diodes
- Reverse Protection Diodes

## Features and Benefits

- Reduced ultra-low forward voltage drop (V<sub>F</sub>); better efficiency and cooler operation.
- Reduced high temperature reverse leakage; Increased reliability against thermal runaway failure in high temperature operation.
- <1.1mm package profile – ideal for thin applications.
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: PowerDI123
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ③
- Polarity: Cathode Band
- Weight: 0.01 grams (Approximate)



Top View



Device symbol

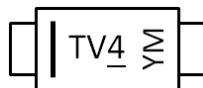
## Ordering Information (Note 4)

Part Number	Case	Packaging
SBRT3M40P1-7	PowerDI-123	3,000/Tape & Reel

Notes:

- EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



TV4 = Product Type Marking Code

YM = Date Code Marking

Y = Year (ex: A = 2013)

M = Month (ex: 9 = September)

### Date Code Key

Year	2013	2014	2015	2016	2017	2018	2019	2020
Code	A	B	C	D	E	F	G	H

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
 For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$		
Working Peak Reverse Voltage	$V_{RWM}$	40	V
DC Blocking Voltage	$V_{RM}$		
Average Rectified Output Current	$I_O$	3	A
Non-Repetitive Peak Forward Surge Current 8.3ms	$I_{FSM}$		
Single Half Sine-Wave Superimposed on Rated Load		70	A

**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	$R_{\theta JA}$	78	°C/W
Typical Thermal Resistance Junction to Case (Note 5)	$R_{\theta JC}$	16	°C/W
Operating and Storage Temperature Range	$T_J, T_{STG}$	-65 to +150	°C

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop (Note 6)	$V_F$	—	0.37 0.29 0.46 0.41	0.42 — 0.53 —	V	$I_F = 1\text{A}, T_J = +25^\circ\text{C}$ $I_F = 1\text{A}, T_J = +125^\circ\text{C}$ $I_F = 3\text{A}, T_J = +25^\circ\text{C}$ $I_F = 3\text{A}, T_J = +125^\circ\text{C}$
Leakage Current (Note 6)	$I_R$	—	8 2.2	30 15	μA mA	$V_R = 40\text{V}, T_J = +25^\circ\text{C}$ $V_R = 40\text{V}, T_J = +125^\circ\text{C}$

Notes: 5. Device mounted on 1 inch FR4.  
 6. Short duration pulse test used to minimize self-heating effect.

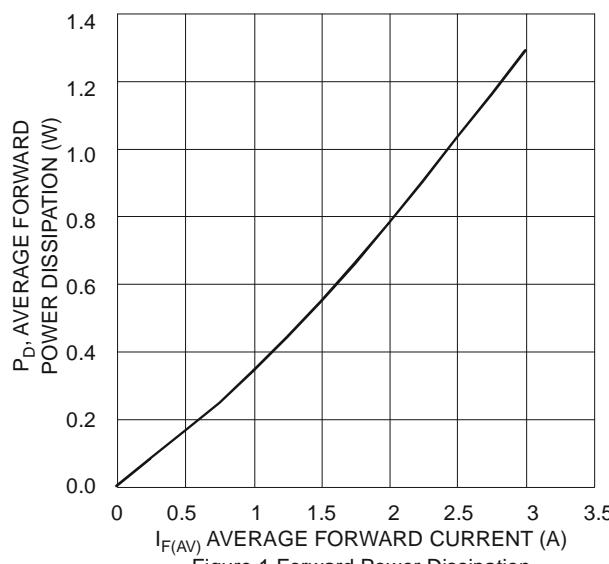


Figure 1 Forward Power Dissipation

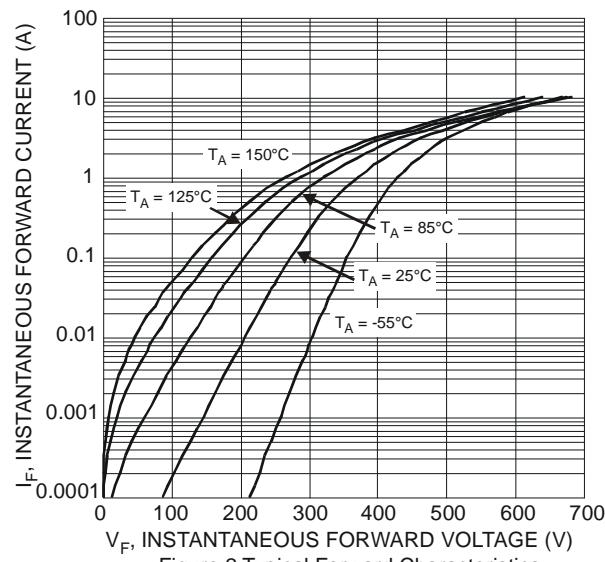


Figure 2 Typical Forward Characteristics

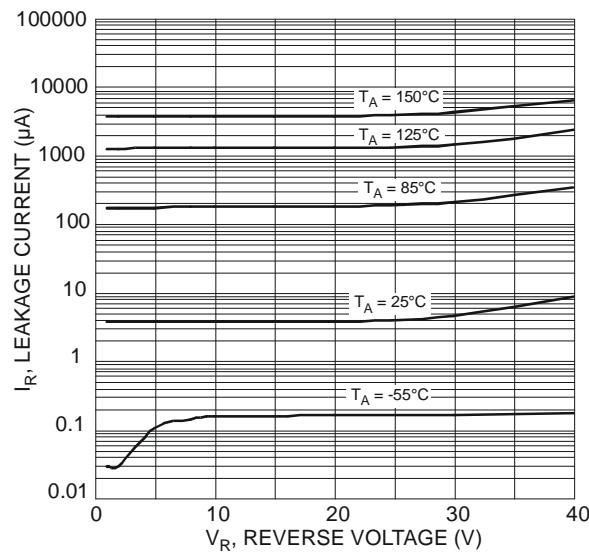


Figure 3 Typical Reverse Characteristics

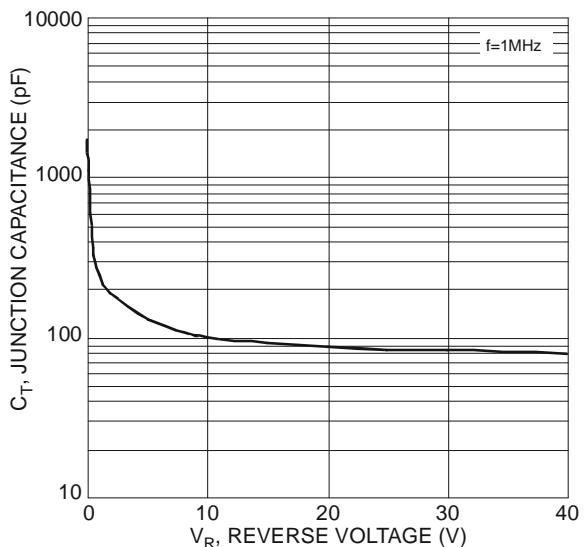


Figure 4 Typical Junction Capacitance

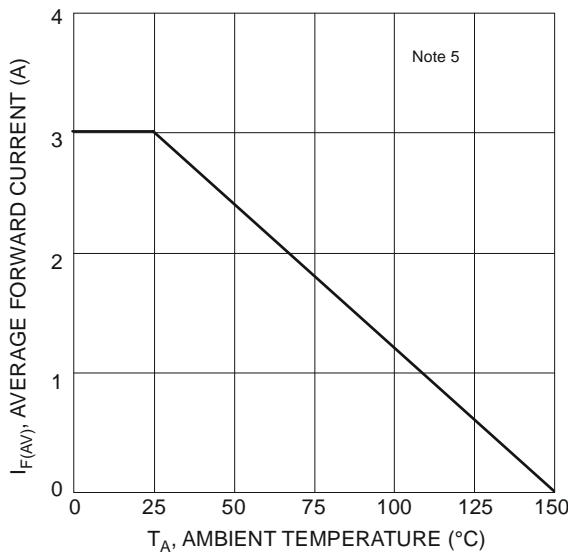


Figure 5 Forward Current Derating Curve

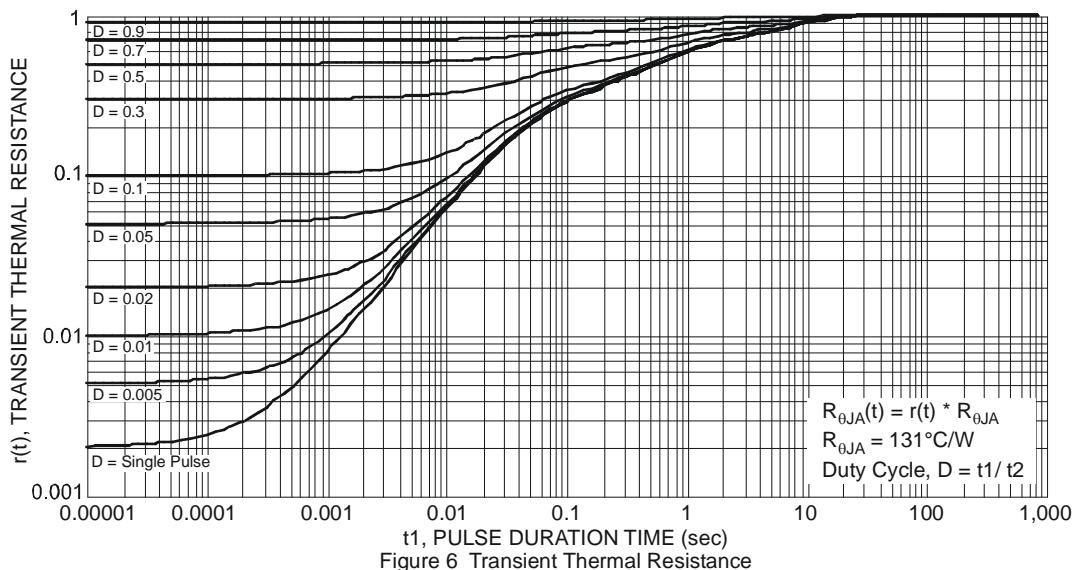
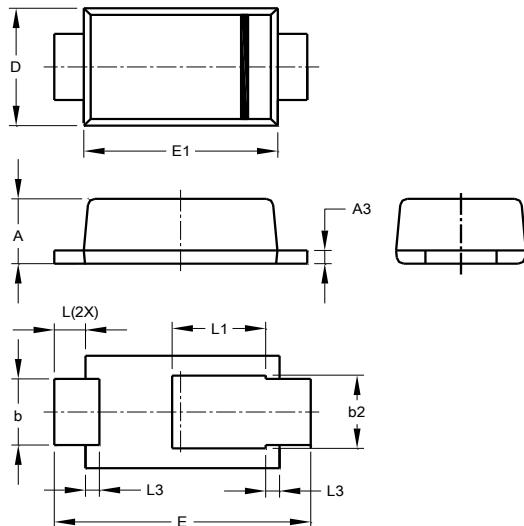


Figure 6 Transient Thermal Resistance

## Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

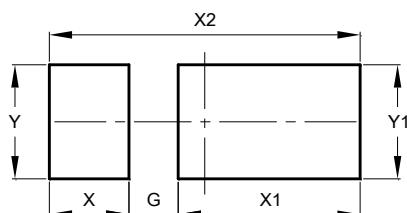


POWERDI®123			
Dim	Min	Max	Typ
<b>A</b>	0.93	1.00	0.98
<b>A3</b>	0.15	0.25	0.20
<b>b</b>	0.85	1.25	1.00
<b>b2</b>	1.025	1.125	1.10
<b>D</b>	1.63	1.93	1.78
<b>E</b>	3.50	3.90	3.70
<b>E1</b>	2.60	3.00	2.80
<b>L</b>	0.40	0.50	0.45
<b>L1</b>	1.25	1.40	1.35
<b>L3</b>	0.125	0.275	0.20

All Dimensions in mm

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
<b>G</b>	0.65
<b>X</b>	1.05
<b>X1</b>	2.40
<b>X2</b>	4.10
<b>Y</b>	1.50
<b>Y1</b>	1.50

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