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50 MHz Logic Probe

Model DP 52



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Tests TTL and CMOS Displays DC to 50 MHz Detects 10ns pulse width Overload protected

Specifications	model DP-52
INPUT CHARACTERISTICS	
Frequency Response	DC to 50 MHz
Minimum detectable pulse width	10nS
LOGIC THRESHOLD	
TTL Logic 1 threshold (Red LED)	3.0V + 0.25V
TTL Logic 0 threshold (Green LED)	0.75V + 0.25V
TTL CMOS 1 threshold (Red LED)	40% Vcc + 5%
TTL CMOS 0 threshold (Green LED)	15% Vcc + 5%
PULSING	
Both Red and Green	
INPUT OVERLOAD PROTECTION	
+70 VAC/DC (15 sec.)	
INPUT IMPEDANCE	
120 kΩ	
GENERAL	
Power supply protection	+20V
Operating supply voltage	4 to 18V
Storage temperature	-4 ⁰ to 149 ⁰ F (-20 ⁰ to 65 ⁰ C) < 80% RH
Operating temperature	32 ⁰ to 122 ⁰ F (0-50 ⁰ C) < 80% RH
Dimensions (HxWxD)	1.6oz. (45g)
One Year Warranty	

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MSRP: \$47.00

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