

DATA SHEET

NE/SA556

Dual timer

Product data
Replaces NE/SA/SE556/NE556-1 of 1994 Aug 31
IC11

2001 Aug 03

Dual timer

NE/SA556

DESCRIPTION

Both the NE556 and SA556 Dual Monolithic timing circuits are highly stable controllers capable of producing accurate time delays or oscillation. The 556 is a dual 555. Timing is provided by an external resistor and capacitor for each timing function. The two timers operate independently of each other, sharing only V_{CC} and ground. The circuits may be triggered and reset on falling waveforms. The output structures may sink or source 200 mA.

FEATURES

- Timing from microseconds to hours
- Replaces two 555 timers
- Operates in both astable and monostable modes
- High output current
- Adjustable duty cycle
- TTL compatible
- Temperature stability of 0.005%/°C

APPLICATIONS

- Precision timing
- Sequential timing
- Pulse shaping
- Pulse generator
- Missing pulse detector
- Tone burst generator
- Pulse width modulation
- Time delay generator
- Frequency division
- Touch-Tone® encoder
- Industrial controls
- Pulse position modulation
- Appliance timing
- Traffic light control

ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
14-Pin Plastic Small Outline (SO) Package	0 to +70°C	NE556D	SOT108-1
14-Pin Plastic Dual In-Line Package (DIP)	0 to +70°C	NE556N	SOT27-1
14-Pin Plastic Dual In-Line Package (DIP)	-40°C to +85°C	SA556N	SOT27-1

PIN CONFIGURATION

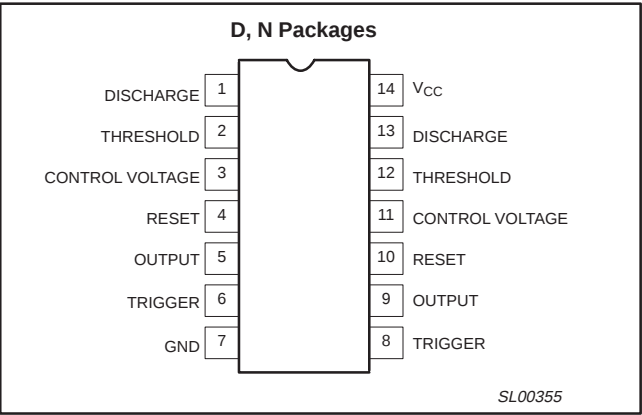


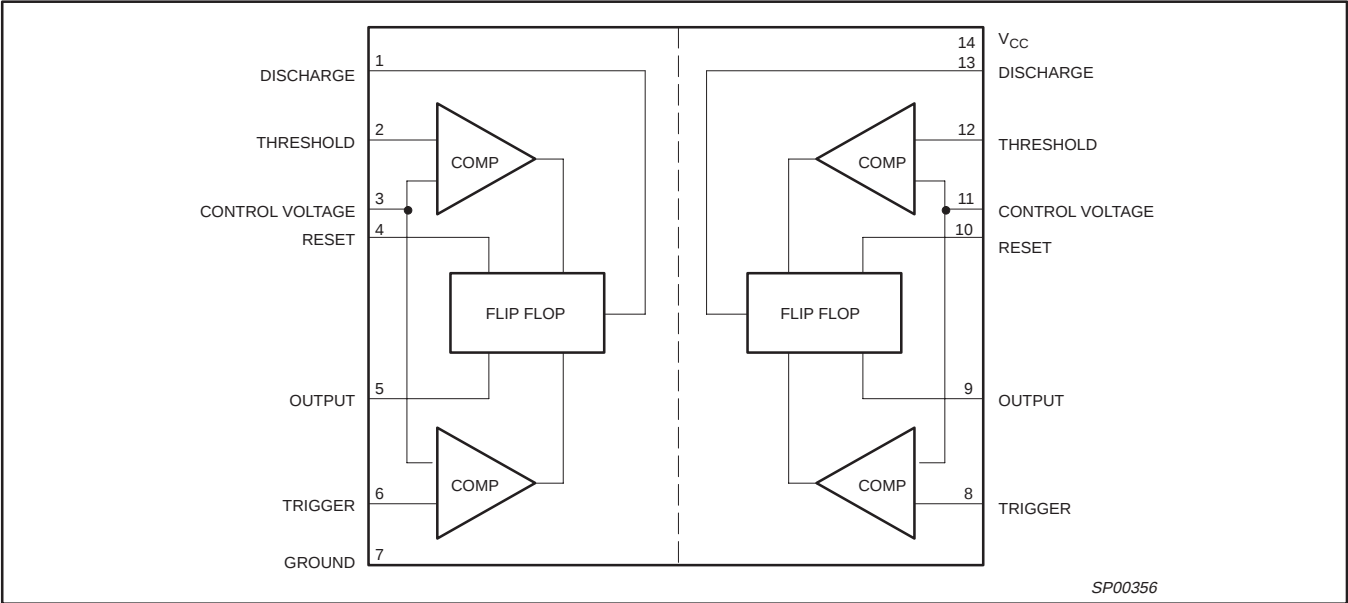
Figure 1. Pin Configuration

® Touch-Tone is a registered trademark of AT&T.

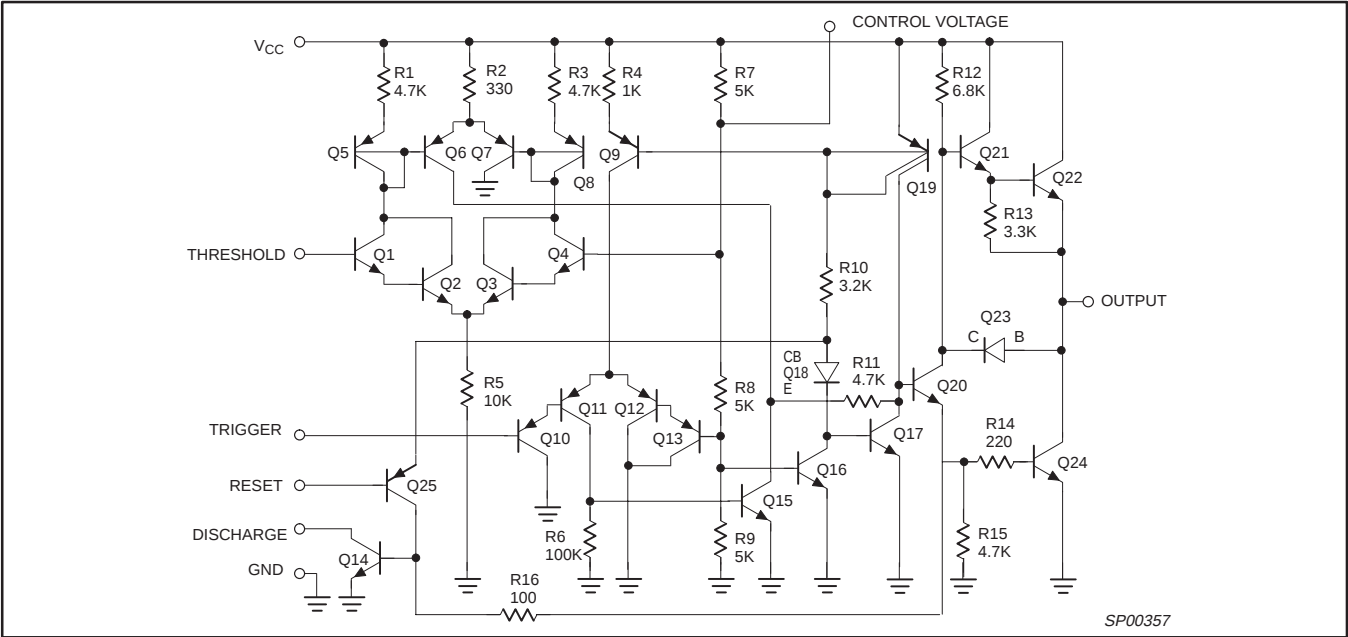
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BLOCK DIAGRAM



EQUIVALENT SCHEMATIC (Shown for one circuit only)



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ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V _{CC}	Supply voltage	+16	V
P _D	Maximum allowable power dissipation ¹	800	mW
T _{amb}	Operating temperature range		
	NE556	0 to +70	°C
	SA556	-40 to +85	°C
T _{stg}	Storage temperature range	-65 to +150	°C
T _{SOLD}	Lead soldering temperature (10 sec max)	+230	°C

NOTE:

- The junction temperature must be kept below 125 °C for the D package and below 150 °C for the N package. At ambient temperatures above 25 °C, where this limit would be exceeded, the Maximum Allowable Power Dissipation must be derated by the following:

D package 115 °C/W

N package 80 °C/W

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ELECTRICAL CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$, $V_{CC} = +5\text{ V}$ to $+15\text{ V}$, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	Min	Typ	Max	UNIT
V_{CC}	Supply voltage		4.5		16	V
I_{CC}	Supply current (low state) ¹	$V_{CC} = 5\text{ V}$, $R_L = \infty$ $V_{CC} = 15\text{ V}$, $R_L = \infty$		6 20	12 30	mA mA
t_M $\Delta t_M/\Delta T$ $\Delta t_M/\Delta V_S$	Timing error (monostable) Initial accuracy ² Drift with temperature Drift with supply voltage	$R_A = 2\text{ k}\Omega$ to $100\text{ k}\Omega$ $C = 0.1\text{ }\mu\text{F}$ $T = 1.1\text{ }^{\circ}\text{C}$		0.75 50 0.1	3.0 150 0.5	% ppm/ $^{\circ}\text{C}$ %/V
t_A $\Delta t_A/\Delta T$ $\Delta t_A/\Delta V_S$	Timing error (astable) Initial accuracy ² Drift with temperature Drift with supply voltage	$R_A, R_B = 1\text{ k}\Omega$ to $100\text{ k}\Omega$ $C = 0.1\text{ }\mu\text{F}$ $V_{CC} = 15\text{ V}$		5 400 0.3	13 500 1	% ppm/ $^{\circ}\text{C}$ %/V
V_C	Control voltage level	$V_{CC} = 15\text{ V}$ $V_{CC} = 5\text{ V}$	9.0 2.6	10.0 3.33	11.0 4.0	V
V_{TH}	Threshold voltage	$V_{CC} = 15\text{ V}$ $V_{CC} = 5\text{ V}$	8.8 2.4	10.0 3.33	11.2 4.2	V V
I_{TH}	Threshold current ³	$V_{CC} = 15\text{ V}$, $V_{TH} = 10.5\text{ V}$		30	250	nA
V_{TRIG}	Trigger voltage	$V_{CC} = 15\text{ V}$ $V_{CC} = 5\text{ V}$	4.5 1.1	5.0 1.67	5.6 2.2	V V
I_{TRIG}	Trigger current	$V_{TRIG} = 0\text{ V}$		0.5	2.0	μA
V_{RESET}	Reset voltage ⁵		0.4	0.7	1.0	V
	Reset current	$V_{RESET} = 0.4\text{ V}$	0.4	0.1	0.6	mA
I_{RESET}	Reset current	$V_{RESET} = 0\text{ V}$		0.4	1.5	mA
V_{OL}	Output voltage (low)	$V_{CC} = 15\text{ V}$ $I_{SINK} = 10\text{ mA}$ $I_{SINK} = 50\text{ mA}$ $I_{SINK} = 100\text{ mA}$ $I_{SINK} = 200\text{ mA}$		0.1 0.4 2.0 2.5	0.25 0.75 3.2	V
		$V_{CC} = 5\text{ V}$ $I_{SINK} = 8\text{ mA}$ $I_{SINK} = 5\text{ mA}$		0.25 0.15	0.3 0.25	V
V_{OH}	Output voltage (high)	$V_{CC} = 15\text{ V}$ $I_{SOURCE} = 200\text{ mA}$ $I_{SOURCE} = 100\text{ mA}$	12.75	12.5 13.3		V
		$V_{CC} = 5\text{ V}$ $I_{SOURCE} = 100\text{ mA}$	2.75	3.3		
t_R	Rise time of output			100	300	ns
t_F	Fall time of output			100	300	ns
	Discharge leakage current			20	100	nA
	Matching characteristics ⁴ Initial accuracy ² Drift with temperature Drift with supply voltage			1.0 ± 10 0.2	2.0 0.5	% ppm/ $^{\circ}\text{C}$ %/V

NOTES:

- Supply current when output is high is typically 1.0 mA less.
- Tested at $V_{CC} = 5\text{ V}$ and $V_{CC} = 15\text{ V}$.
- This will determine maximum value of $R_A + R_B$. For 15 V operation, the max total $R = 10\text{ M}\Omega$, and for 5 V operation, the maximum total $R = 3.4\text{ M}\Omega$.
- Matching characteristics refer to the difference between performance characteristics for each timer section in the monostable mode.
- Specified with trigger input high. In order to guarantee reset the voltage at reset pin must be less than or equal to 0.4 V. To disable reset function, the voltage at reset pin has to be greater than 1 V.
- Time measured from a positive-going input pulse from 0 to 0.4 V_{CC} into the threshold to the drop from high to low of the output. Trigger is tied to threshold.

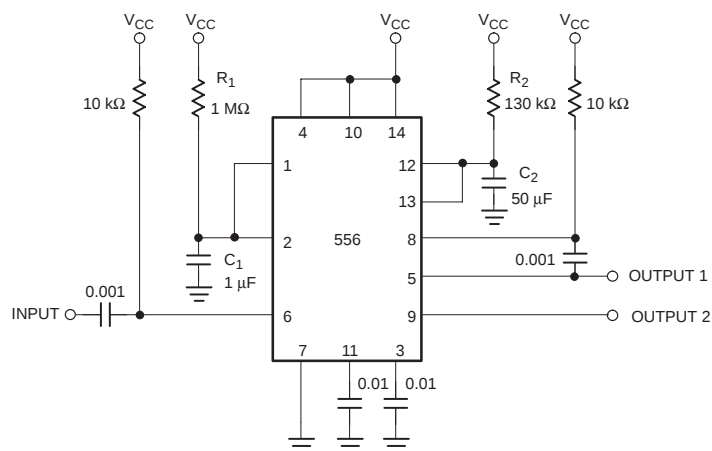
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TYPICAL APPLICATIONS

One feature of the dual timer is that by utilizing both halves it is possible to obtain sequential timing. By connecting the output of the first half to the input of the second half via a $0.001\ \mu\text{F}$ coupling capacitor sequential timing may be obtained. Delay t_1 is determined by the first half and t_2 by the second half delay.

The first half of the timer is started by momentarily connecting Pin 6 to ground. When it is timed out (determined by $1.1R_1C_1$) the second half begins. Its duration is determined by $1.1R_2C_2$.



SP00358

Figure 4. Sequential Timer

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TYPICAL PERFORMANCE CHARACTERISTICS

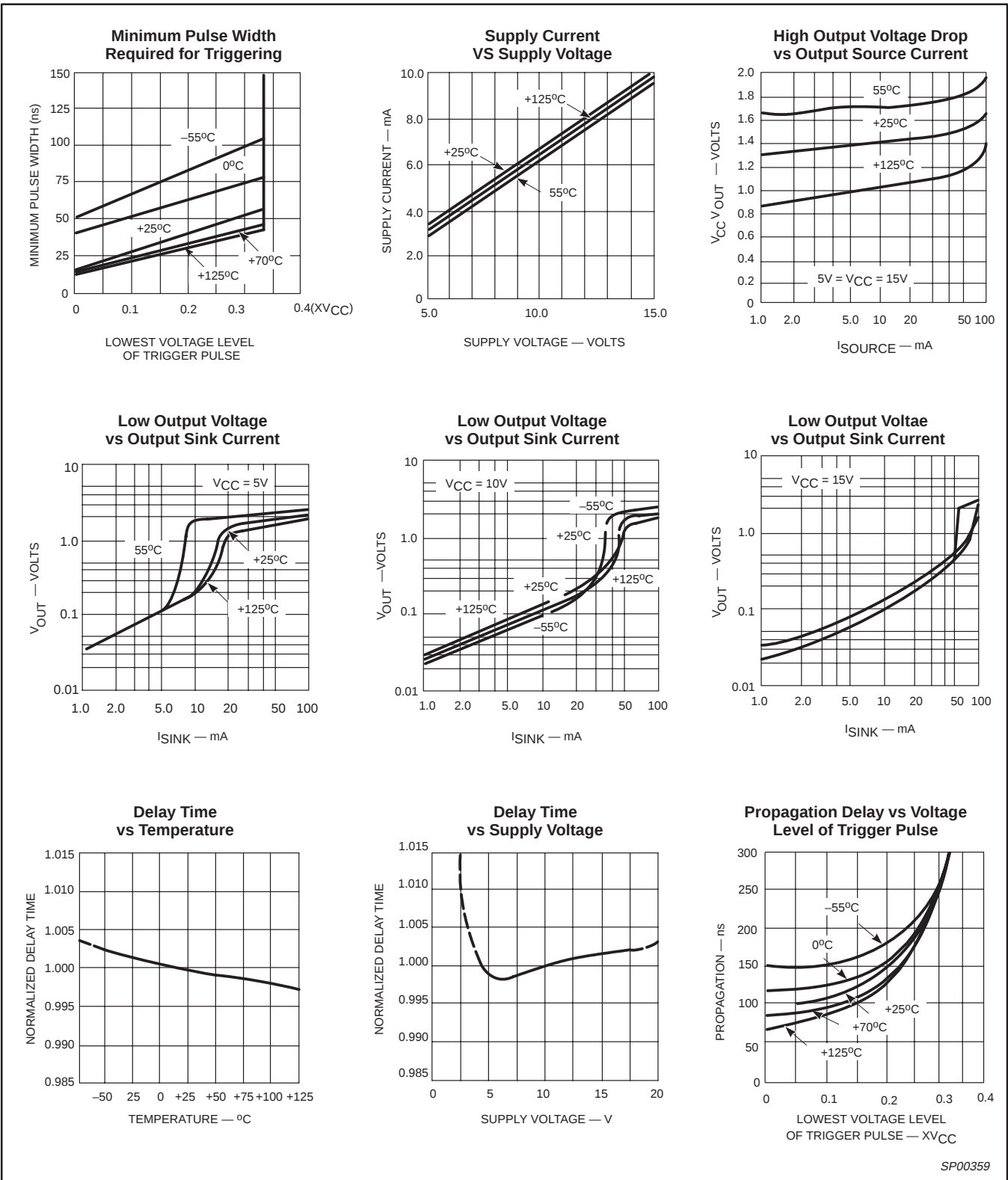


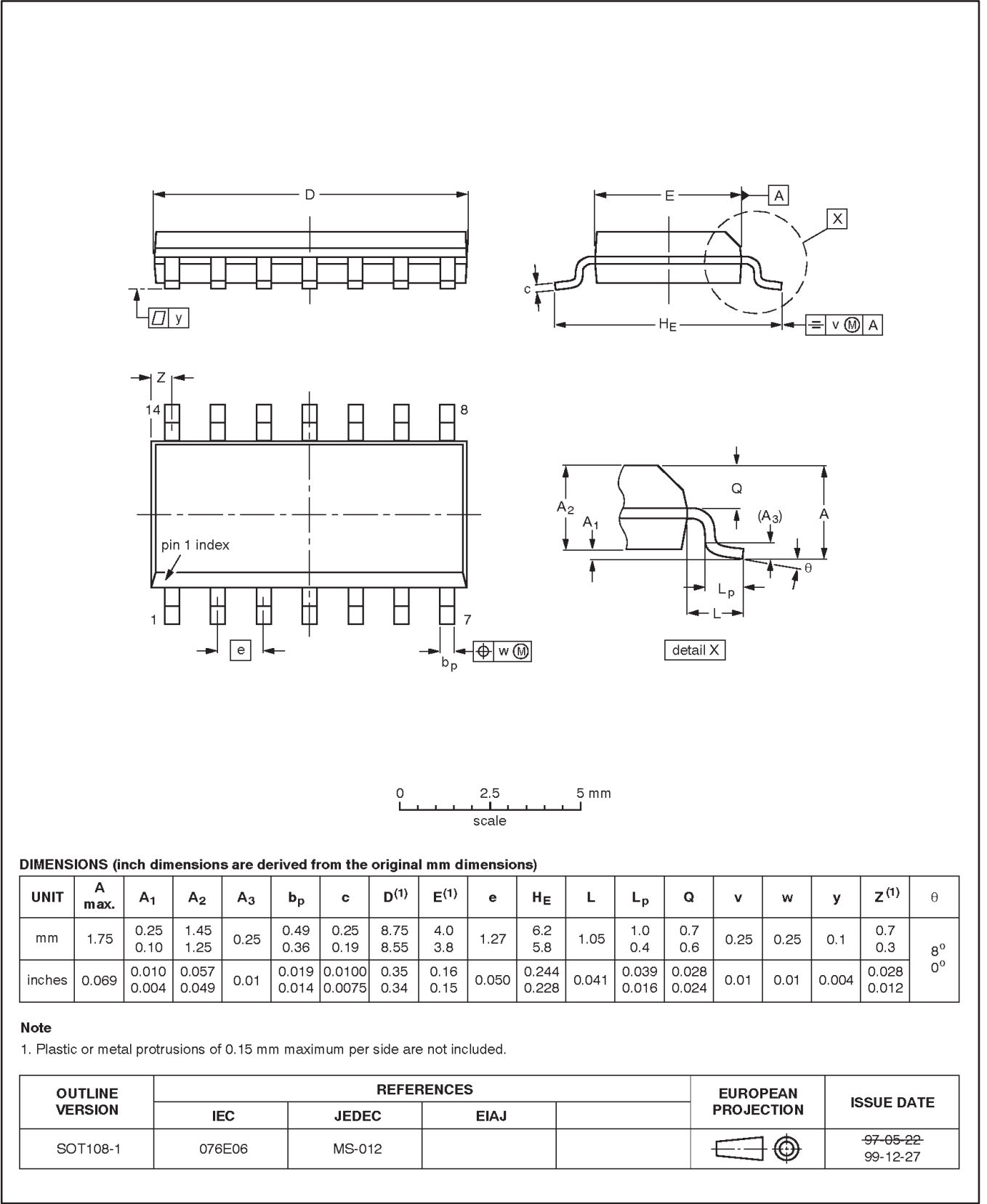
Figure 5. Typical Performance Characteristics

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SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1

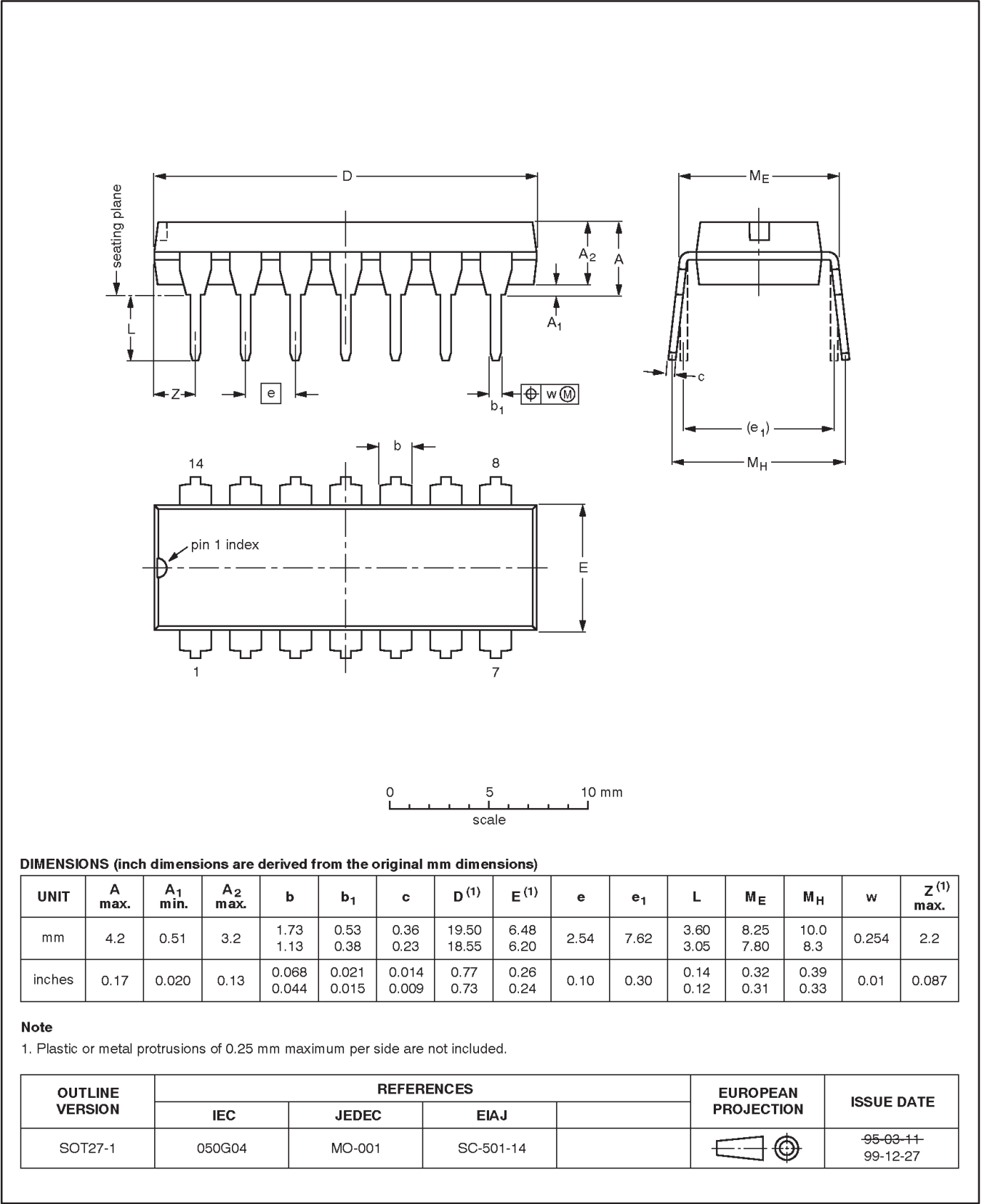


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DIP14: plastic dual in-line package; 14 leads (300 mil)

SOT27-1



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Data sheet status

Data sheet status ^[1]	Product status ^[2]	Definitions
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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[1] Please consult the most recently issued data sheet before initiating or completing a design.

[2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL <http://www.semiconductors.philips.com>.

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Contact information

For additional information please visit
<http://www.semiconductors.philips.com>. Fax: +31 40 27 24825

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sales.addresses@www.semiconductors.philips.com.

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