





#### 150V NPN LED DRIVING TRANSISTOR IN SOT89

#### **Features**

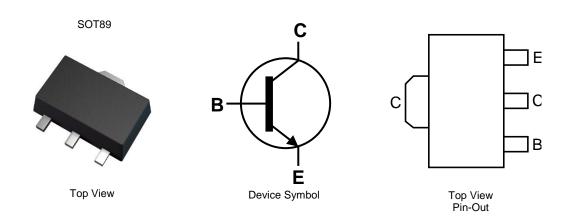
- BV<sub>CEO</sub> > 150V
- I<sub>C</sub> = 1A High Continuous Current
- $h_{FE} > 100$  for  $I_C = 150$ mA,  $V_{CE} = 0.25$ V
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

#### **Mechanical Data**

- Case: SOT89
- Case Material: Molded Plastic. "Green" Molding Compound;
   UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.055 grams (Approximate)

#### **Applications**

LED TV Backlight



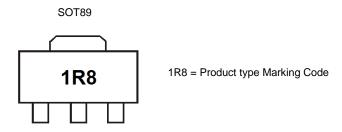
#### Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTN4004ZTA	AEC-Q101	1R8	7	12	1,000 units
ZXTN4004ZQTA	Automotive	1R8	7	12	1,000 units

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_compliance\_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**





# Absolute Maximum Ratings (@TA = +25°C unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	200	V
Collector-Emitter Voltage	$V_{\sf CEO}$	150	V
Emitter-Base Voltage	$V_{EBO}$	7	V
Continuous Collector Current	Ic	1	Α
Peak Pulse Current	Ісм	3	Α
Base Current	Ι <sub>Β</sub>	500	mA

# Thermal Characteristics ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
	(Note 6)		1		
Power Dissipation	(Note 7)	P <sub>D</sub>	1.5	W	
	(Note 8)		2.0		
	(Note 6)		125		
Thermal Resistance, Junction to Ambient Air	(Note 7)	$R_{ heta JA}$	83	°C/W	
	(Note 8)		60		
Thermal Resistance, Junction to Lead (Note 9)		$R_{ heta JL}$	13	°C/W	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

# ESD Ratings (Note 10)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes:

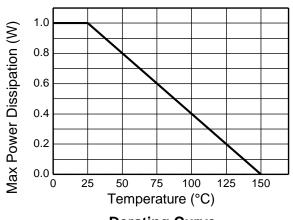
- 6. For a device mounted with the exposed collector pad on 15mm x 15mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
  7. Same as Note 6, except the device is mounted on 25mm x 25mm 1oz copper.
  8. Same as Note 6, except the device is mounted on 50mm x 50mm 1oz copper.
  9. Thereal exists 6, effort instance is related to 10 mm x 50mm 1oz copper.

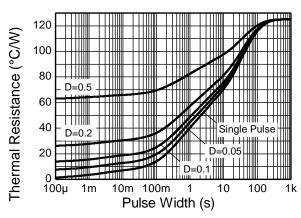
- Thermal resistance from junction to solder-point (on the exposed collector pad).
   Refer to JEDEC specification JESD22-A114 and JESD22-A115.





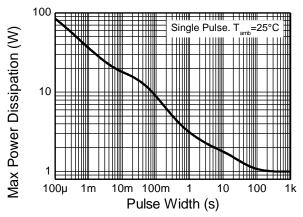
# **Thermal Characteristics and Derating Information**



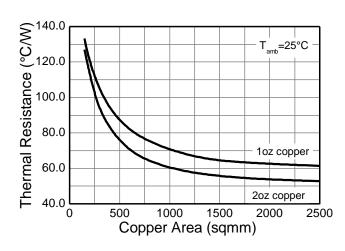


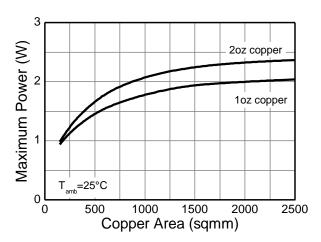
## **Derating Curve**

**Transient Thermal Impedance** 



### **Pulse Power Dissipation**





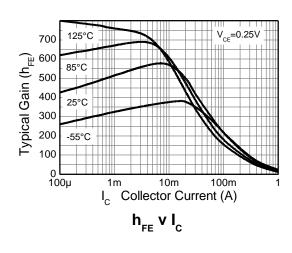


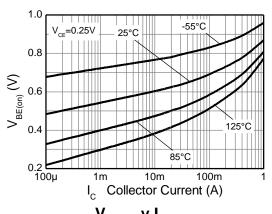
## Electrical Characteristics (@TA = +25°C unless otherwise specified.)

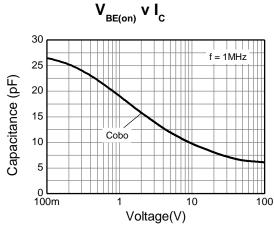
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Emitter Breakdown Voltage (Note 11)	BV <sub>CEO</sub>	150	175	-	V	$I_C = 10mA$
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	200	310	-	V	$I_C = 100\mu A$
Collector Cut-Off Current	I <sub>CBO</sub>	-	<1	50	nA	V <sub>CB</sub> = 150V
Collector Cut-Off Current	I <sub>CES</sub>	-	<1	50	nA	V <sub>CE</sub> = 150V
Emitter Cut-Off Current	I <sub>EBO</sub>	-	<1	50	nA	V <sub>EB</sub> = 7V
		200	-	-		$I_C = 30mA, V_{CE} = 5V$
Static Forward Current Transfer Ratio (Note 11)	h <sub>FE</sub>	60	-	-	-	$I_C = 85 \text{mA}, V_{CE} = 0.20 \text{V}$
		100	-	-		$I_C = 150 \text{mA}, V_{CE} = 0.25 \text{V}$
Base-Emitter Turn-On Voltage (Note 11)	$V_{BE(on)}$	-	0.71	0.95	V	$I_C = 150 \text{mA}, V_{CE} = 0.25 \text{V}$
Collector-Emitter Saturation Voltage (Note 11)	V <sub>CE(sat)</sub>	-	-	0.25	V	$I_C = 100 \text{mA}, I_B = 5 \text{mA}$
Delay Time	t <sub>(d)</sub>	-	512	-	ns	
Rise Time	t <sub>(r)</sub>	-	426	-	ns	$V_{CC} = 120V, I_C = 150mA,$
Storage Time	t <sub>(s)</sub>	-	3413	-	ns	$-I_{B2} = 1.5 \text{mA}, V_{CE}(ON) = 0.25 \text{V}$
Fall Time	t <sub>(f)</sub>	-	321	=	ns	
Storage Time	t <sub>(s)</sub>	-	65	-	ns	$V_{CC} = 120V, I_C = 150mA,$
Fall Time	t <sub>(f)</sub>	-	294	-	ns	$-I_{B2} = 1.5 \text{mA}, V_{CE(ON)} = 4 \text{V}$

Note: 11. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.

### Electrical Characteristics (@T<sub>A</sub> = +25°C unless otherwise specified.)





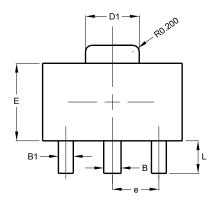


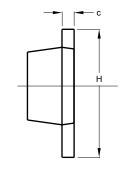
Capacitance v Voltage

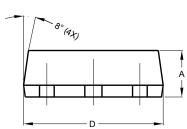


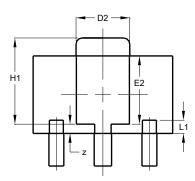
# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.





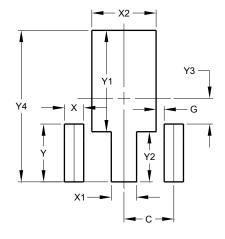




SOT89					
Dim	Min	Max	Тур		
Α	1.40	1.60	1.50		
В	0.50	0.62	0.56		
B1	0.42	0.54	0.48		
С	0.35	0.43	0.38		
D	4.40	4.60	4.50		
D1	1.62	1.83	1.733		
D2	1.61	1.81	1.71		
Е	2.40	2.60	2.50		
E2	2.05	2.35	2.20		
e	1	-	1.50		
Н	3.95	4.25	4.10		
H1	2.63	2.93	2.78		
L	0.90	1.20	1.05		
L1	0.327	0.527	0.427		
Z	0.20	0.40	0.30		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value
Dilliensions	(in mm)
С	1.500
G	0.244
Х	0.580
X1	0.760
X2	1.933
Υ	1.730
Y1	3.030
Y2	1.500
Y3	0.770
Y4	4.530

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.





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