

MOSFET – N-Channel, SUPERFET® II, FRFET®

650 V, 24 A, 150 mΩ

FCPF150N65F

Description

SUPERFET II MOSFET is onsemi’s brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. SUPERFET II FRFET MOSFET combines a faster and more rugged intrinsic body diode performance with fast switching, aimed at achieving better reliability and efficiency especially in resonant switching applications. SUPERFET II FRFET is very suitable for the switching power applications such as server/telecom power, Solar inverter, FPD TV power, computing power, lighting and industrial power applications.

Features

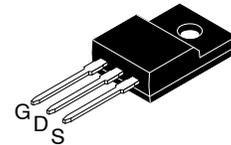
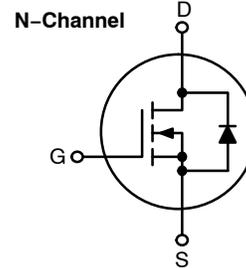
- 700 V @ $T_J = 150^\circ\text{C}$
- Typ. $R_{DS(on)} = 133\text{ m}\Omega$
- Ultra Low Gate Charge (Typ. $Q_g = 72\text{ nC}$)
- Low Effective Output Capacitance (Typ. $C_{oss(eff.)} = 361\text{ pF}$)
- 100% Avalanche Tested
- RoHS Compliant

Applications

- Telecom / Server Power Supplies
- Solar Inverters
- Computing Power Supplies
- FPD TV Power / Lighting

V_{DS}	$R_{DS(on)}\text{ MAX}$	$I_D\text{ MAX}$
650 V	150 mΩ @ 10 V	24 A*

*Drain current limited by maximum junction temperature.



TO-220 Fullpack, 3-Lead
/ TO-220F-3SG
CASE 221AT

MARKING DIAGRAM



FCPF150N65F = Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FCPF150N65F	TO-220F	1000 Units / Tube

FCPF150N65F

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DSS}	Drain to Source Voltage	650	V
V_{GSS}	Gate to Source Voltage	-DC	± 20
		-AC ($f > 1\text{ Hz}$)	± 30
I_D	Drain Current	- Continuous ($T_C = 25^\circ\text{C}$)	24*
		- Continuous ($T_C = 100^\circ\text{C}$)	14.9*
I_{DM}	Drain Current	- Pulsed (Note 1)	72*
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	663	mJ
I_{AR}	Avalanche Current (Note 1)	4.7	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	2.98	mJ
dv/dt	MOSFET dv/dt	100	V/ns
	Peak Diode Recovery dv/dt (Note 3)	50	
P_D	Power Dissipation	($T_C = 25^\circ\text{C}$)	39
		-Derate above 25°C	0.31
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds	300	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

*Drain current limited by maximum junction temperature.

1. Repetitive rating; pulse-width limited by maximum junction temperature.
2. $I_{AS} = 4.7\text{ A}$, $R_G = 25\ \Omega$, starting $T_J = 25^\circ\text{C}$.
3. $I_{SD} \leq 12\text{ A}$, $di/dt \leq 200\text{ A}/\mu\text{s}$, $V_{DD} \leq 380\text{ V}$, starting $T_J = 25^\circ\text{C}$.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.2	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} = 0 V, I _D = 10 mA, T _J = 25°C	650	–	–	V
		V _{GS} = 0 V, I _D = 10 mA, T _J = 150°C	700	–	–	
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25°C	–	0.72	–	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	–	–	10	μA
		V _{DS} = 520 V, V _{GS} = 0 V, T _C = 125°C	–	86	–	
I _{GSS}	Gate to Body Leakage Current	V _{GS} = ±20 V, V _{DS} = 0 V	–	–	±100	nA

ON CHARACTERISTICS

V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 2.4 mA	3	–	5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 12 A	–	133	150	mΩ
g _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 12 A	–	22	–	S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{DS} = 100 V, V _{GS} = 0 V, f = 1 MHz	–	2810	3737	pF
C _{oss}	Output Capacitance		–	91	121	pF
C _{rss}	Reverse Transfer Capacitance		–	0.77	–	pF
C _{oss}	Output Capacitance	V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz	–	54	–	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	–	361	–	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 380 V, I _D = 12 A, V _{GS} = 10 V (Note 4)	–	72	94	nC
Q _{gs}	Gate to Source Gate Charge		–	15	–	nC
Q _{gd}	Gate to Drain "Miller" Charge		–	31	–	nC
ESR	Equivalent Series Resistance	f = 1 MHz	–	0.69	–	Ω

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	V _{DD} = 380 V, I _D = 12 A, V _{GS} = 10 V, R _G = 4.7 Ω (Note 4)	–	28	66	ns
t _r	Turn-On Rise Time		–	15	40	ns
t _{d(off)}	Turn-Off Delay Time		–	73	156	ns
t _f	Turn-Off Fall Time		–	6	22	ns

DRAIN-SOURCE DIODE CHARACTERISTICS

I _S	Maximum Continuous Drain to Source Diode Forward Current	–	–	24	A	
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current	–	–	72	A	
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 12 A	–	–	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 12 A, dI _F /dt = 100 A/μs	–	123	–	ns
Q _{rr}	Reverse Recovery Charge		–	597	–	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature.

TYPICAL PERFORMANCE CHARACTERISTICS

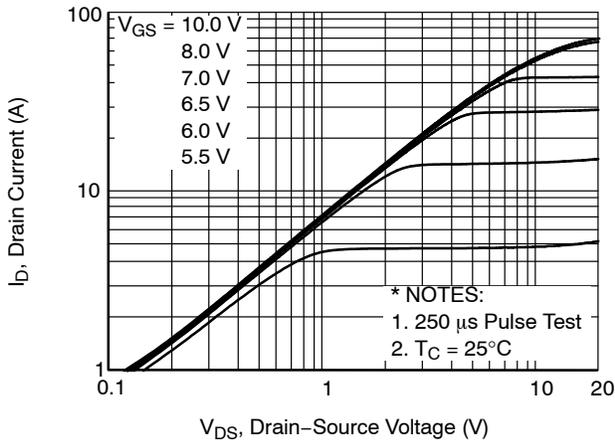


Figure 1. On-Region Characteristics

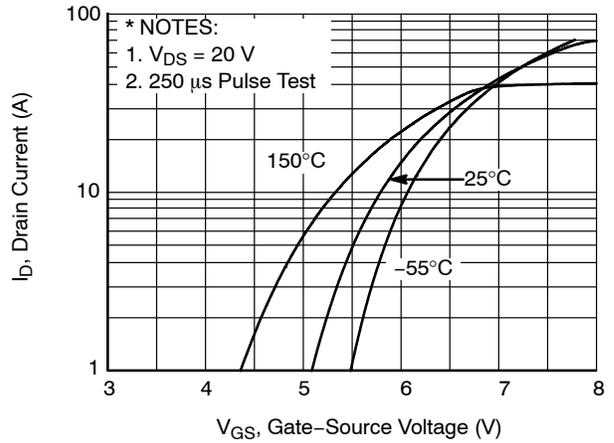


Figure 2. Transfer Characteristics

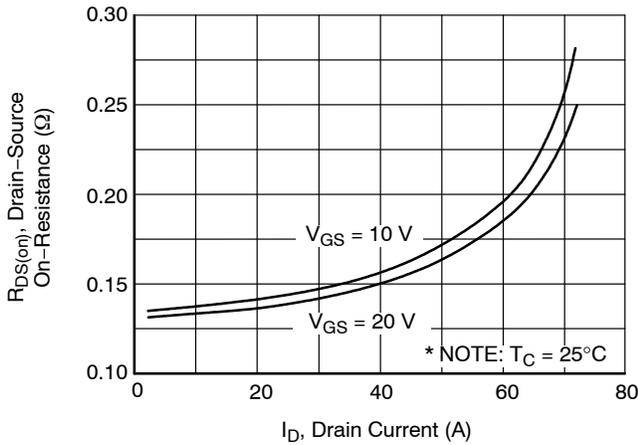


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

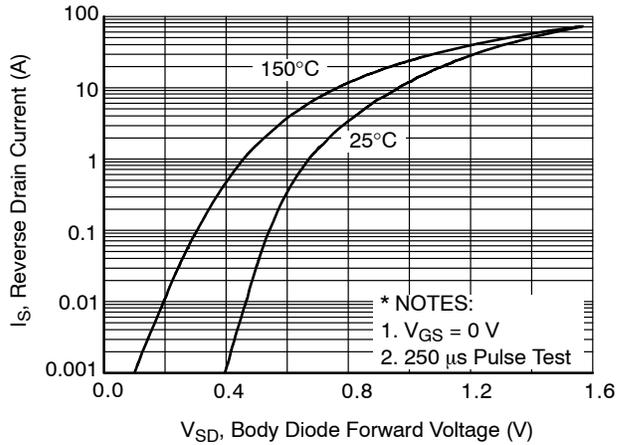


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

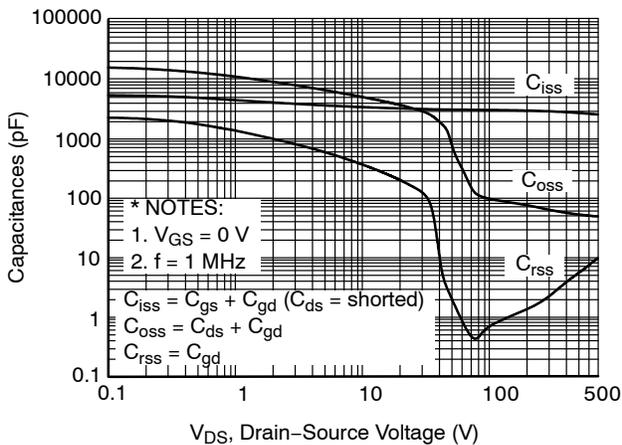


Figure 5. Capacitance Characteristics

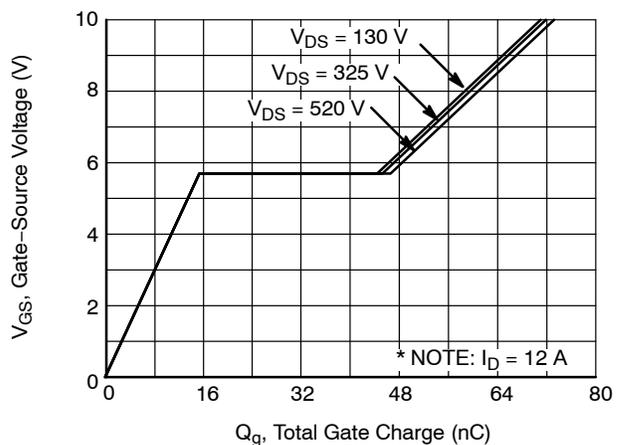


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

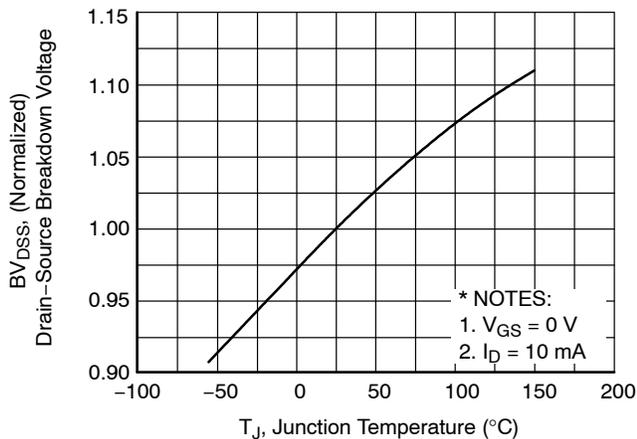


Figure 7. Breakdown Voltage Variation vs. Temperature

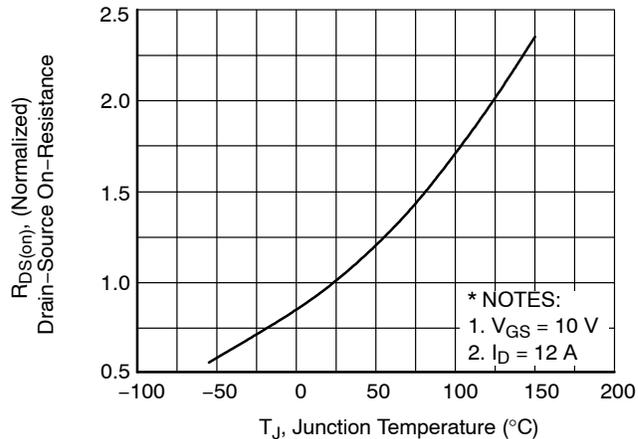


Figure 8. On-Resistance Variation vs. Temperature

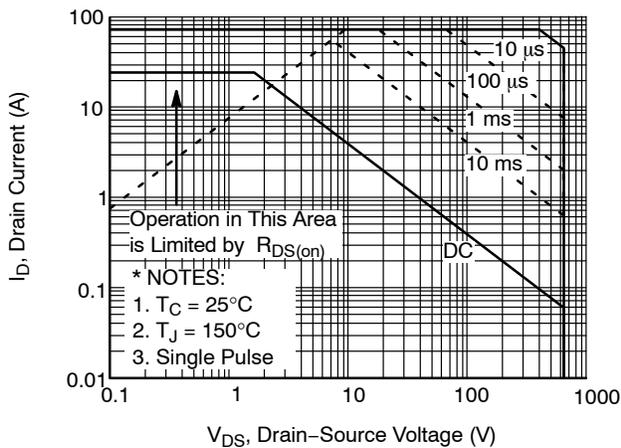


Figure 9. Maximum Safe Operating Area

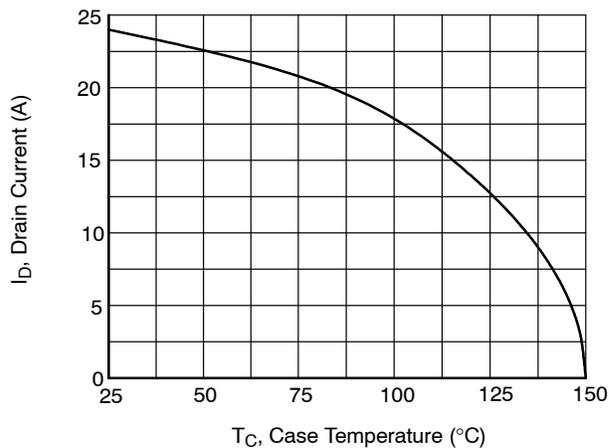


Figure 10. Maximum Drain Current vs. Case Temperature

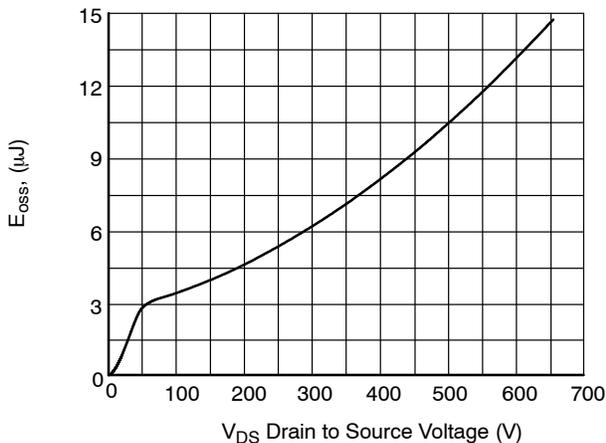


Figure 11. E_{OSS} vs. Drain to Source Voltage

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TYPICAL PERFORMANCE CHARACTERISTICS (continued)

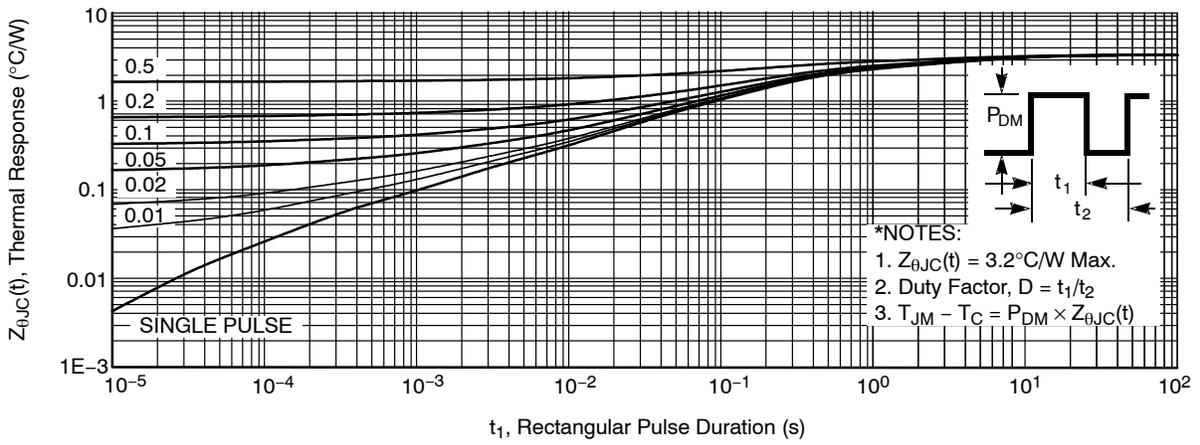


Figure 12. Transient Thermal Response Curve

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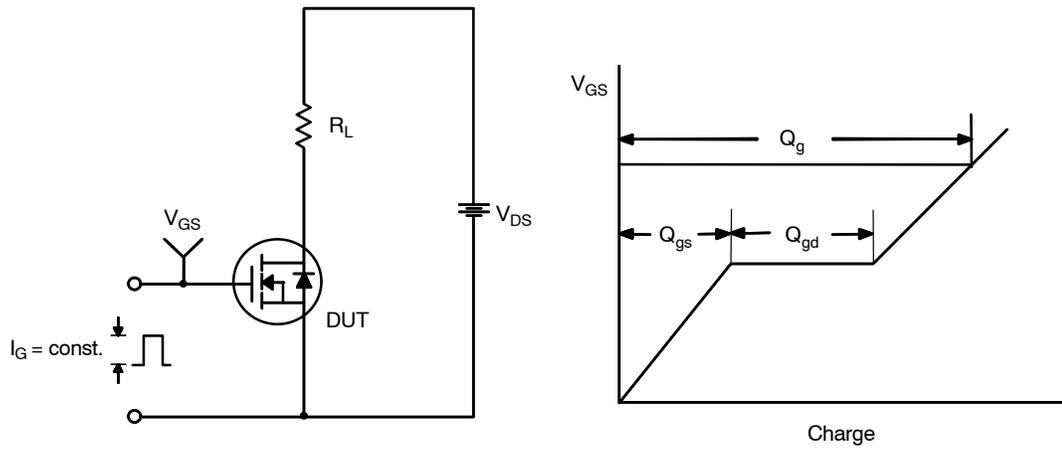


Figure 13. Gate Charge Test Circuit & Waveform

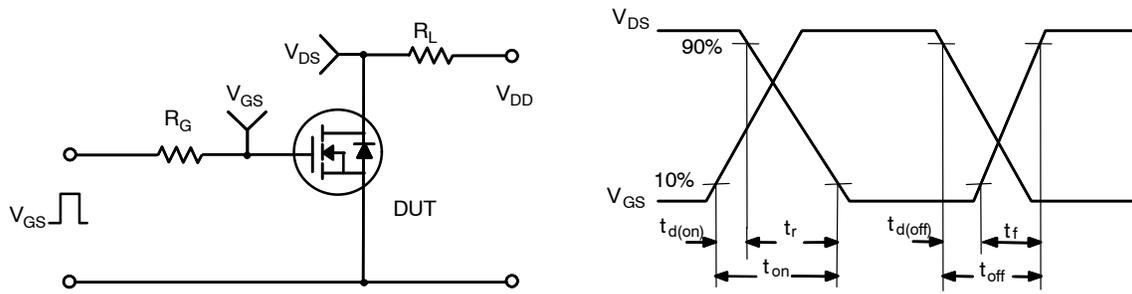


Figure 14. Resistive Switching Test Circuit & Waveforms

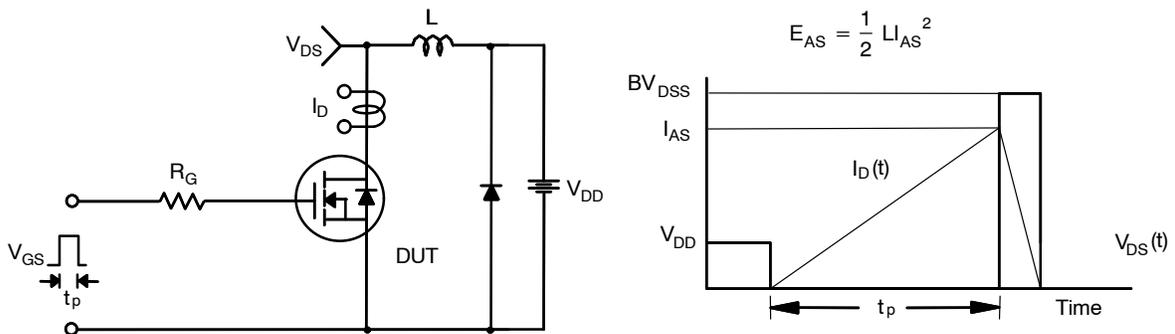


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

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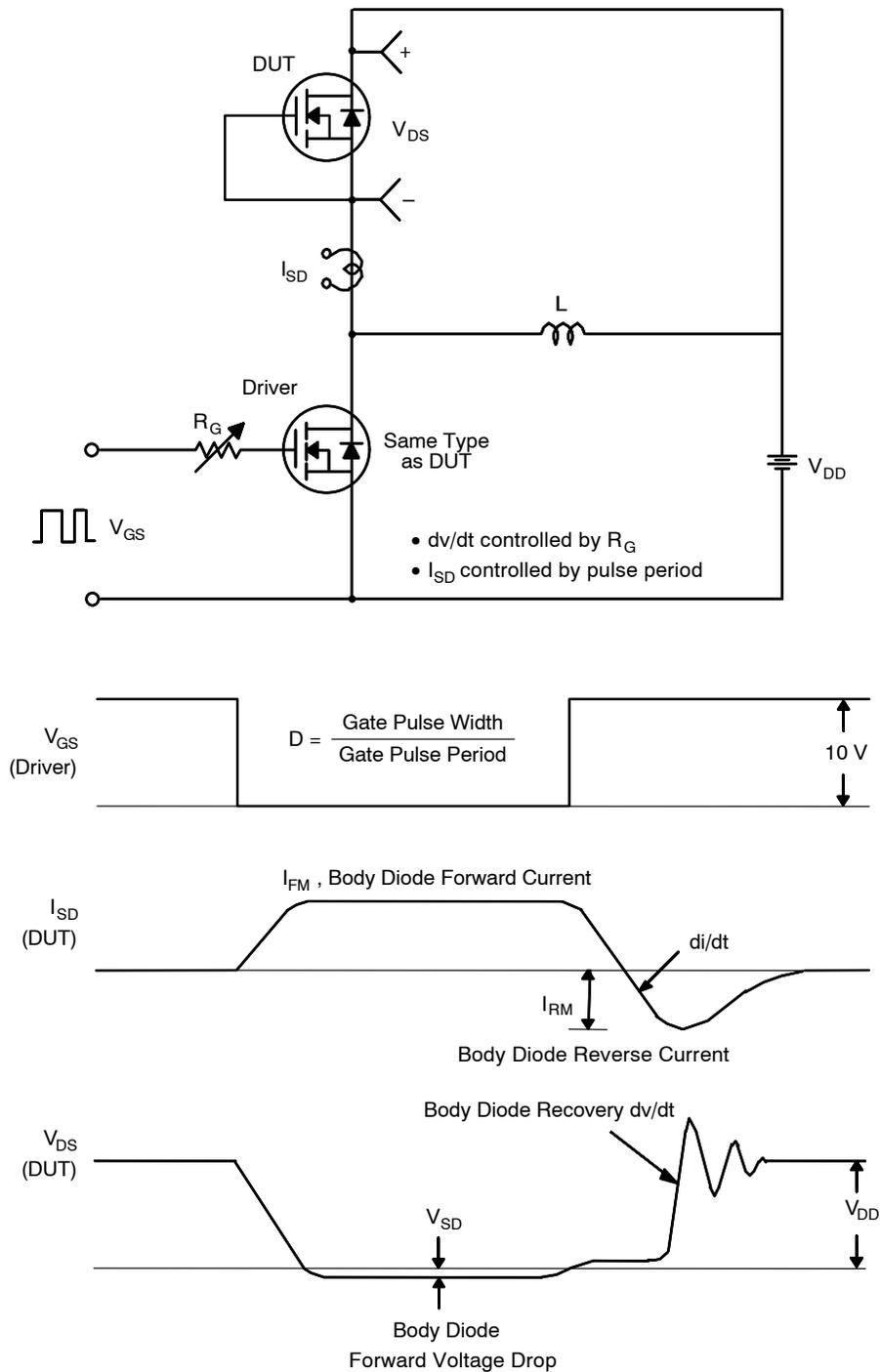
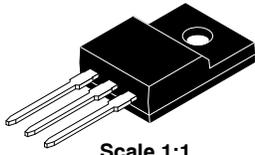


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

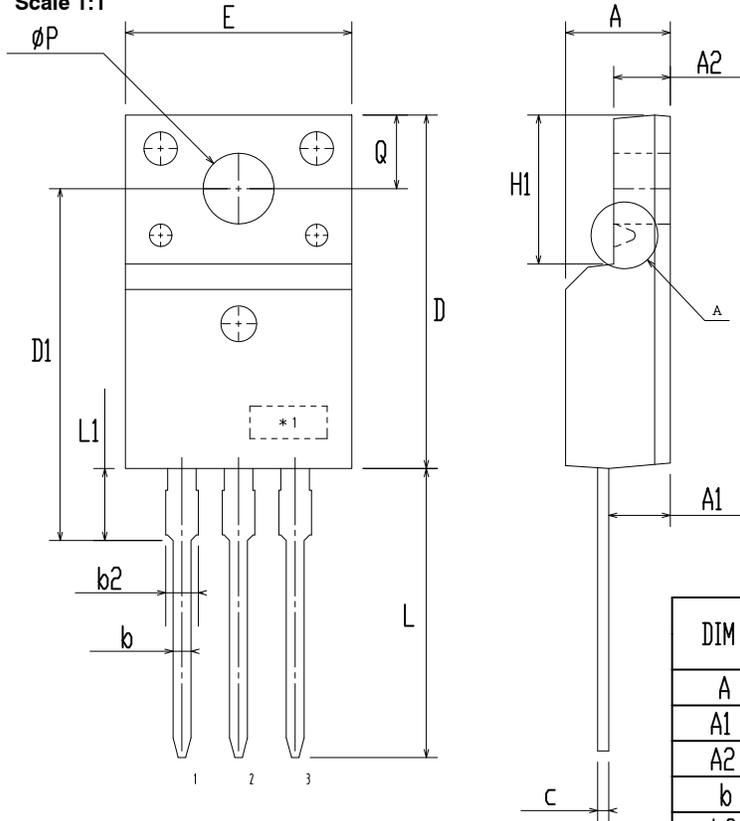
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CASE 221AT
ISSUE B

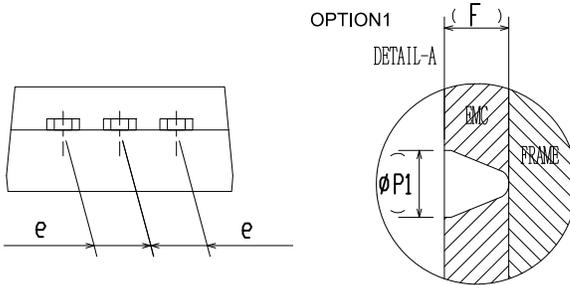
DATE 19 JAN 2021



Scale 1:1



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	~	~	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
e	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
L	12.78	12.98	13.18
L1	3.03	3.23	3.43
Ø P	2.98	3.18	3.38
Ø P1	~	1.00	~
Q	3.20	3.30	3.40



NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCTIONS.
- C. OPTION 1 - WITH SUPPORT PIN HOLE
- OPTION 2 - NO SUPPORT PIN HOLE

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