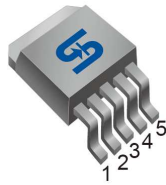




TO-220-5L



TO-263-5L
(D²PAK)



Pin Definition:

1. Enable
2. Input
3. Ground
4. Output
5. Adjust

General Description

TS29152 is using process with a PNP pass element for high current, high accuracy and low dropout voltage regulators. These regulator s feature 350mV(typ.) dropout voltages and very low ground current, these devices also find applications in lower current and low dropout critical systems, where their tiny dropout voltage and ground current values are important attributes. TS29152 is fully protected against over current faults, reversed input polarity, reversed lead insertion, over temperature operation, positive and negative transient voltage spikes, logic level enable control and error flag which signals whenever the output falls out of regulation.

On the TS29152, the enable pin may be tied to V_{IN} if it is not required for enable control.

Features

- Dropout voltage typically 0.6V @ $I_o=1.5A$
- Output Current up to 1.5A
- Low Ground Current
- Extremely Fast Transient Response
- +60V Transient Peak Voltage
- -20V Reverse Peak Voltage
- Zero Current Shutdown Mode
- Current Limit & Thermal Shutdown Protection

Ordering Information

| Part No. | Package | Packing |
|---------------|-----------|-------------------|
| TS29152CZ5 C0 | TO-220-5L | 50pcs / Tube |
| TS29152CM5 RN | TO-263-5L | 800pcs / 13" Reel |

Application

- PowerPC™ Power Supplies
- Battery Powered Equipment
- Consumer and Personal Electronics
- High Efficiency Linear Power Supplies
- High-efficiency Post Regulator for Switching Supply
- Low-Voltage microcontrollers and Digital Logic

Absolute Maximum Rating (Note 1)

| Parameter | Symbol | Limit | Unit |
|--------------------------------------|--------------------|--------------------|------|
| Supply Voltage | V_{IN} | -20V ~ +60 | V |
| Operation Input Voltage | V_{IN} (operate) | 26 | V |
| Power Dissipation (Note 4) | P_D | Internally Limited | W |
| Operating Junction Temperature Range | T_J | -40 ~ +125 | °C |
| Storage Temperature Range | T_{STG} | -65 ~ +150 | °C |
| Lead Soldering Temperature (260°C) | | 5 | S |

Thermal Performance

| Condition | Package type | Typ | Unit |
|---|--------------|-----|------|
| Thermal Resistance Junction to Ambient | TO-220-5L | 80 | °C/W |
| | TO-263-5L | 85 | |

Electrical Characteristics

$V_{IN}=V_{OUT}+1V$, $V_{enable}=2.4V$, $I_L=10mA$, $C_o=10\mu F$, Adjustable versions are programmed to 5V output, $T_J=25^{\circ}C$ unless otherwise specified.

| Parameter | Conditions | Min | Typ | Max | Unit |
|--|---|----------|------------------|----------|------------|
| Output Voltage | | 0.990 Vo | V _{OUT} | 1.010 Vo | V |
| Output Voltage | 10mA ≤ I _L ≤ 1.5A, Vo+1V ≤ V _{IN} ≤ 26V | 0.980 Vo | | 1.020 Vo | |
| Input Supply Voltage | | -- | -- | 26 | V |
| Output Voltage Temperature Coefficient | | -- | 20 | 100 | ppm/ °C |
| Line Regulation | Vo+1V ≤ V _{IN} ≤ 26V | -- | 0.05 | 0.5 | % |
| Load Regulation | 10mA ≤ I _L ≤ 1.5A | -- | 0.2 | 1.0 | % |
| Dropout Voltage (Note 4) | I _L =100mA | -- | 80 | 200 | mV |
| | I _L =750mA | -- | 200 | -- | |
| | I _L =1.5A | -- | 350 | 600 | |
| Quiescent Current (Note 5) | I _L =750mA | -- | 8 | -- | mA |
| | I _L =1.5A | -- | 22 | -- | |
| Short Circuit Current (Note 6) | V _{OUT} =0 | -- | 2.1 | 3.5 | A |
| Output Noise, 10Hz to 100KHz, I _L =100mA | C _L =2.2uF | -- | 600 | -- | uVrms |
| | C _L =10uF | -- | 400 | -- | |
| | C _L =33uF | -- | 260 | -- | |
| Reference | | | | | |
| Reference Voltage | | 0.980 Vo | 1.24 | 1.020 Vo | V |
| Reference Voltage | 10mA ≤ I _L ≤ 1.5A, 2.3V ≤ V _{IN} ≤ 26V | 0.970 Vo | | 1.030 Vo | |
| Adjust Pin Bias Current | | -- | 40 | 80 | nA |
| Reference Voltage Temperature Coefficient | (Note 7) | -- | 20 | -- | ppm/ °C |
| Adjust Pin Bias Current Temperature Coefficient | | -- | 0.1 | -- | nA/ °C |
| Enable Input | | | | | |
| Input Logic Voltage | Low (OFF) | -- | -- | 0.8 | V |
| | High (ON) | 2.4 | -- | -- | |
| Enable Pin Input Current | V _{EN} =26V | -- | -- | 750 | uA |
| | V _{EN} =0.8V | -- | -- | 5 | |
| Regulator Output Current Shutdown | V _{EN} ≤ 0.8V, V _{IN} ≤ 26V, V _{OUT} =0 | -- | 10 | 500 | uA |

1.5A Ultra Low Dropout Voltage Regulator

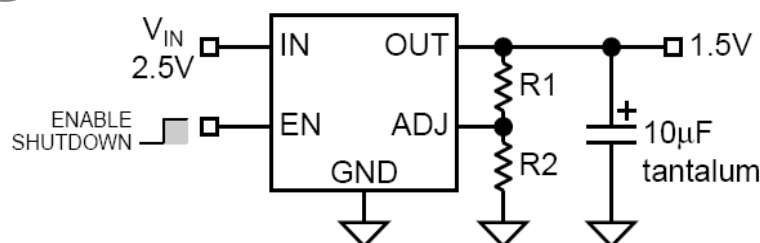
Electrical Specification (Continue)

- Note 1: Absolute Maximum Rating is limits beyond which damage to the device may occur. For guaranteed specifications and test conditions see the Electrical Characteristics.
- Note 2: Maximum positive supply voltage of 60V must be limited duration (<100mS) and duty cycle (<1%).
- Note 3: The maximum allowable power dissipation is a function of the maximum junction temperature, T_J , the junction to ambient thermal resistance, θ , and the ambient temperature, T_a . Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. The effective value of θ_{JA} can be reduced by using a heatsink.
- Note 4: Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential.
- Note 5: Ground pin current is the regulator quiescent current. The total current drawn from the source is the sum of the ground pin current and output load current.
- Note 6: Output current will decrease with increasing temperature, but it will be not dropped below 1.5A at the maximum specified temperature.
- Note 7: Thermal regulation is defined as the change in output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effects. Specification are for a 200mA load pulse at $V_{IN} = 20V$ (a 4W pulse) for $T = 10ms$
- Note 8: Comparator thresholds are expressed in terms of a voltage differential at the adjust terminal below the nominal reference voltage measured at 6V input. To express these thresholds in terms of output voltage change, multiply by the error amplifier gain $= V_{OUT}/V_{REF} = (R1+R2) / R2$. For example, at a programmed output voltage of 5V, the error output is guaranteed to go low when the output drops by $95mV \times 5V / 1.24V = 384mV$. Thresholds remain constant as a percent of V_{OUT} as V_{OUT} is varied, with the dropout warning occurring at typically 5% below nominal, 7.7% guaranteed.

Pin Description

| Pin Configuration | Function Description |
|-------------------|--|
| Enable | Enable (input): TTL/COMS compatible input. Logic high is enable; logic low or open is shutdown |
| Input | Unregulated input: +26V maximum supply |
| Ground | Ground: Ground pin and TAB/heatsink are internally connected. |
| Output | Regulator output |
| Adjust | Adjustment input: Feedback input. Connect to resistive voltage-divider network. |

Typical Application Circuit



Function Description

The TS29152 is high performance with low dropout voltage regulator suitable for moderate to high current and voltage regulator application. Its 350mA(typ) dropout voltage at full load and over temperature makes it especially valuable in battery power systems and as high efficiency noise filters in post regulator applications. Unlike normal NPN transistor design, where the base to emitter voltage drop and collector to emitter saturation voltage limit the minimum dropout voltage, dropout performance of the PNP output of these devices is limited only by low V_{CE} saturation voltage.

The TS29152 is fully protected from damage due to fault conditions. Linear current limiting is provided. Output current during overload conditions is constant. Thermal shutdown the device when the die temperature exceeds the maximum safe operating temperature. Transient protection allows device survival even when the input voltage spikes above and below nominal. The output structure of these regulators allows voltages in excess of the desired output voltage to be applied without reverse current flow.

Capacitor Requirement

The TS29152 requires an output capacitor to maintain stability and improve transient response is necessary. The value of this capacitor is dependent upon the output current, lower currents allow smaller capacitors. TS29152 regulators are stable with the 10uF minimum capacitor value at full load. Where the regulator is powered from a source with high AC impedance, a 0.1uF capacitor connected between input and ground is recommended. The capacitor should have good characteristics to above 250KHz. The capacitance values will be help to improved transient response, ripple rejection and output noise.

Minimum Load Current

The TS29152 is specified between finite loads. If the output current is too small leakage currents dominate and the output voltage rises. A 10mA minimum load current swamps any expected leakage current across the operating temperature range.

Thermal Characteristics

TS29152 linear regulators are simple to use, the most complicated design parameters to consider are thermal characteristics, thermal design requires the following application specification parameters:

- * Maximum ambient temperature, T_a
- * Output current, I_{OUT}
- * Output voltage, V_{OUT}
- * Input voltage, V_{IN}

We calculate the power dissipation of the regulator from these numbers and the device parameters from this data sheet. A heatsink may be required depending on the maximum power dissipation and maximum ambient temperature of the application. Under all possible operating conditions, the junction temperature must be within the range specified under absolute maximum ratings. To determine if the heatsink is required, the power dissipated by the regulator, P_D must be calculated.

The below formula shows the voltages and currents for calculating the P_D in the regulator:

$$I_{IN} = I_L / I_G$$

$$P_D = (V_{IN} - V_{OUT}) * I_L + (V_{IN}) * I_G$$

$$\begin{aligned} \text{Ex. } P_D &= (3.3V - 2.5V) * 0.5A + 3.3V * 11mA \\ &= 400mW + 36mW \\ &= 436mW \end{aligned}$$

Remark: I_L is output load current,
 I_G is ground current.
 V_{IN} is input voltage
 V_{OUT} is output voltage

Thermal Characteristics (Continue)

The next parameter which must be calculated is the maximum allowable temperature rise, $T_R(\max)$. this is calculated by the using to formula:

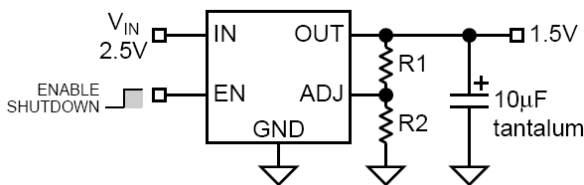
$$T_R(\max) = T_J(\max) - T_A(\max)$$

Where: $T_J(\max)$ is the maximum allowable junction temperature, which is 125°C for commercial grade parts.

$T_A(\max)$ is the maximum ambient temperature which will be encountered in the application. Using the calculated values for $T_R(\max)$ and P_D , the maximum allowable value for the junction to ambient thermal resistance, θ_{JA} , can now be found: $\theta_{JA} = T_R(\max) / P_D$

Adjustable Regulator Design

The adjustable regulator versions is allow to programming the output voltage anywhere between 1.25 and the 26V maximum operating rating of the family. Two resistors are used. Resistors can be quite large up to 1MΩ, because of the very high input impedance and low bias current of the sense comparator, the resistor values are calculated by:



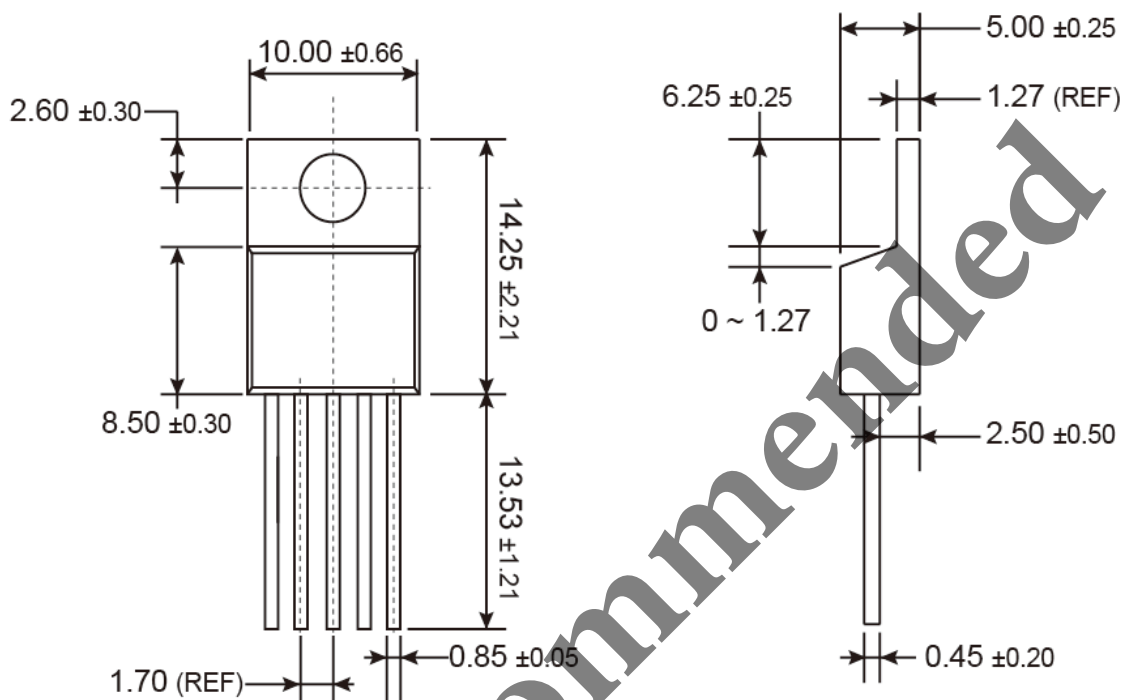
$$R1 = R2 * [(V_{OUT} / 1.24) - 1]$$

Where is V_{out} the desired output voltage. Above application circuit shows component definition. Applications with widely varying load currents may scale the resistors to draw the minimum load current required for proper operation.

Enable Input

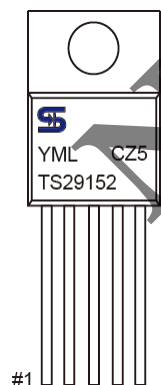
TS29152 versions feature an enable (EN) input that allows ON/OFF control of the device. Special design allows “zero” current drain when the device is disabled—only microamperes of leakage current flow. The EN input has TTL/CMOS compatible thresholds for simple interfacing with logic, or may be directly tied to $\leq 30V$. Enabling the regulator requires approximately 20uA of current.

TO-220-5L Mechanical Drawing



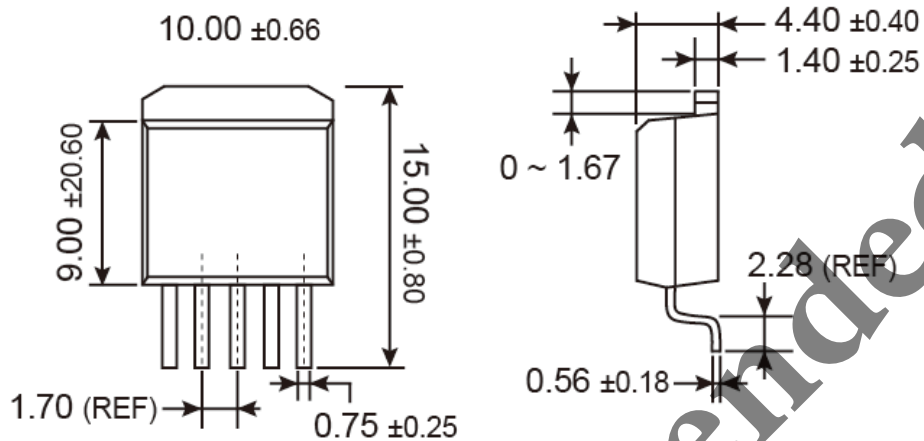
Unit: Millimeters

Marking Diagram



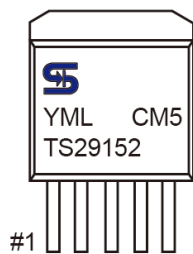
- Y** = Year Code
- M** = Month Code
- (A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)
- L** = Lot Code

TO-263-5L Mechanical Drawing



Unit: Millimeters

Marking Diagram



Y = Year Code
M = Month Code
 (A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)
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Not Recommended

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