# HITACHI

KAOHSIUNG HITACHI ELECTRONICS CO.,LTD P.O. BOX 26-27 2,13TH EAST ST. K.E.P.Z. KAOHSIUNG TAIWAN R.O.C. TEL:(07) 821-5811(7 LINE) FAX:(07) 821-5815

FOR MESSRS:	DATE: Nov.12,2010
I OK MESSINS.	<u>DATE : NOV.12,2010</u>

### CUSTOMER'S ACCEPTANCE SPECIFICATIONS

# TX16D11VM2CAA

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\*When product will be discontinued, customer will be informed by HITACHI with twelve months prior announcement.

ACCEPTED BY;		PROPOSED BY; Kenthen	
KAOUSII INC. LITACUI	Sh.	1 1	
KAOHSIUNG HITACHI	Sn.	7D04D0 0704 TV40D44VM000AA 7	. ,

7B64PS 2701-TX16D11VM2CAA-7

PAGE 1-1/1

ELECTRONICS CO.,LTD.

No.

## RECORD OF REVISION

DATE	SHEET No.				SUMM	ΔΡΥ				
Oct.22,'03	7B64PS 2703-	3. GENER	۸Τ⊏	ר א ח		ΛΙ <i>Σ</i> Ι				
OCt.22, 03	TX16D11VM2CAA-2					EOL	4 2	F°C II	E ma	
		Added: (10) Average life time 50kh at 25°C IL=5mA Added: (15) Viewing direction.								
	Page 3-1/2		, ,							
	7B64PS 2708-				CONNECTIO	N				
	TX16D11VM2CAA-2	Change	d CN	2 pi	n No.					
	Page 8-6/6									
			VSS		- GND f	or CF	L			
		2 \	√CFL		- Power	Supp	ly for	CFL		
	7B64PS 2709-	9. DIMENS	SION/	\I (	NITINE					
	TX16D11VM2CAA-2				definition of	f CNG	)			
		Change	u ine	piri	delimition o	I CINZ	<b></b>			
	Page 9-1/2			_	1					
					$  \rangle =   \rangle$	2				
			:		2	1				
	700400 0744	44.5.840111		2 0						
	7B64PS 2711-				RECAUTION					
	TX16D11VM2CAA-2	Adde	d : LC	M a	ssemble exp	olanat	ion.			
	Page 11-4/4									
Apr.12,'05	7B64PS 2706-	6.1 OPTIC	CAL (	CHA	RACTERISTI	CS C	F LC	D		
'	TX16D11VM2CAA-3	Revis	ed:							
	Page 6-1/3									
		I	ГЕМ		CONDITION	MIN.	TYP.	MAX	UNIT	NOTE
				$\theta \mathbf{x}$	$\phi$ =0°,K $\geq$ 5.0	-	(60)	-	deg	1~5
		Viewing Are	a	$\theta$ x'	$\phi = 180^{\circ}, K \ge 5.0$	-	(60)	-	deg	1~5
			-	$\theta$ y	φ=90°,K≥5.0	-	(45)	-	deg	1~5
				<i>θ</i> y'	<i>φ</i> =270°,K ≥ 5.0	-	(60)	-	deg	1~5
			Red	Х		-	(0.62)	-	-	
				У		-	(0.34)	-	-	
		Color Tone	Greer	y x		<u> </u>	(0.59)		_	
		(Primary		X	$\phi = 0^{\circ}$ , $\theta = 0^{\circ}$	_	(0.14)	_	-	
		Color)	Blue	у		-	(0.09)	-	-	
				×		-	(0.29)	-	-	
			White	У		-	(0.31)	-	-	
						Ĺ				
			EM		CONDITION	MIN.	TYP.	MAX	UNIT	NOTE
				θх	<i>φ</i> =0°,K≥5.0	-	70	-	deg	1~5
				$\theta x'$	φ=180°,K≥5.0	-	70	-	deg	1~5
		Viewing Are	a	$\theta$ y	<i>φ</i> =90°,K≥5.0	-	60	-	deg	1~5
				$\theta$ y'	<i>φ</i> =270°,K≥5.0	-	70	-	deg	1~5
			Red	х		0.57	0.62	0.67	-	
			rveu	у		0.29	0.34	0.39	-	
		Color Tone	Green	х		0.25	0.30	0.35	-	
		(Primary		У	$\phi = 0^{\circ},  \theta = 0^{\circ}$	0.54	0.59	0.64	-	
		Color)	Blue	Х	, .	0.09	0.14	0.19	-	
			-	у		0.04	0.09	0.14	-	
			White	X V		0.24	0.29 0.31	0.34	-	
			I	У		0.20	0.31	0.30	-	
	C LUTACLU T		C <sub>L</sub>							
	G HITACHI DATE	Nov.12,'10	Sh.	7B64	IPS 2702-TX1	6D11\	/M2CA	A-7 P	AGF	2-1/3
ELECTRON	ICS CO.,LTD.		No.	. 20-	5 2/52 //(	5511	207	'  <u>'</u>		, 5
	·									

# RECORD OF REVISION

DATE	SHEET No.			SUMMAF	 RY			
	7B64PS 2706-	6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT						
'	TX16D11VM2CAA-3							
	Page 6-3/3	TIEM TYP.						
		Brightness (200)						
			$\downarrow$					
		TIEM		TYP	_	7		
		Brightnes	SS	(280				
Aug.06.'05	7B64PS 2709-	9. DIMENSIONAL C	UTL	INE				
3 ,	TX16D11VM2CAA-4	Changed the len	gth	of CN2				
	Page 9-1/2~2/2		•		/ 5	<del>-</del> 0)		
		<del>&lt; (15)</del> >			$\leftarrow$ (;	50)	>	
				-			=	
		CN2	$\rightarrow$	•			CN2	
							<del>-</del>	
Mar.19,'07	7B64PS 2704-	4.2 ENVIRONMENT	AL A	ABSOLUT	E MA	XIMUN	1 RATINGS	
	TX16D11VM2CAA-5 Page 4-1/1	Revised						
	Fage 4-1/1		OP	ERATIN	STOF	RAGE		
		ITEM		G			COMMENT	
		I I LIVI					COMMENT	
			MIN	. MAX.	MIN.	MAX.		
				$\downarrow$				
			OPERATING			RAGE		
		ITEM	MIN		MIN.	MAX.	COMMENT	
		A h : t . T t	-20°(		-20°C	70°C	(Nata 0.00)	1
		Ambient Temperature	20 (	C   70 C	200	1700	(Note 2,3,6)	1
		Note 2 : Ta at -20°C	for	48h, at 0	60°C f	or 168	h.	
			$\downarrow$	,				
		Note 2 : For operatin	_					1
		For storage	con	dition Ta	at -20	0°C , 70	℃ < 100h	
May.13,'08	7B64PS 2705-	5.2.2 MECHANICAL	CHA	ARACTE	RISTIC	S		
	TX16D11VM2CAA-6	Changed :						
	Page 5-1/3	ITEM		SPECIFICA	ATION		NOTE	
		Pen Input Pressi	Iro	1.0N m	2 V	R0.8 nc	lyacetal pen	
		ren input riessi	ile	1.011 111	ал.	ixo.o pc	луасетат реп	
		17514		<b>ODE O</b> IEIO	. TION		NOTE	
		ITEM SPECIFICATION NOTE						
		Pen Input Pressure 1.2N max. R0.8, Polyacetal Pen						
	7B64PS 2709-	9. DIMENSIONAL OU	JTI IN	VF				
	TX16D11VM2CAA-6				chang	ed		
	Page 9-2/2	THE IOURDOI SIZE A	. ia pi		or iar ig			
	<u> </u>	<u> </u>						
KAOHSIUNG	G HITACHI	Nov 12 '10 Sh. 7864	20.00	700 TV40	7441/84		, DACE 3	<b>ン</b> /つ
ELECTRONI	ICS CO.,LTD.	Nov.12,'10 No. 7B64I	-5 Z	702-TX16[	אואדוע	2UAA-7	'   PAGE   2-2	<u> </u>
	, -	1 - 1					1	

# RECORD OF REVISION

DATE	SHEET No.	SUMMARY					
May.13,'08	7B64PS 2712-	12.1 LOT MARK					
	TX16D11VM2CAA-6 Page 12-1/1	Changed: 5 digits for production number					
	1 ago 12 1/1	C dinite for production grants					
		6 digits for production number					
		12.3 LOCATION OF LOT MARK Changed:					
		Lot No. & Caution Control No.  HITACHI TX16D11VM2CAA.  S0311-00005- hev. Make in Version Floor    Caution Rich Voltage					
		(26)					
		TX16D11VM2CAA, REV: 8041T. (5D). 123456. HITACHL MADE:IN:TAIWAN.					
		Added: 12.4 REVISION(Rev.) CONTROL					
		Rev No. ITEM					
		A CN1 JAE : FA5B040HP1R3000					
Nov.12,'10	7B64PS 2710- TX16D11VM2CAA-7 PAGE 10-5/5	10.3 APPEARANCE SPECIFICATION Changed: Blistering Puffiness 0.4mm max. → 0.6mm max.					
KAOHSIUN		Nov.12,'10 Sh. 7B64PS 2702-TX16D11VM2CAA-7 PAGE 2-3/3					
ELECTRON	ICS CO.,LTD.	No.   No.					

### 3.GENERAL DATA

(1) Part Name TX16D11VM2CAA

(2) Module Dimensions 173.0(W)mm x 70.0(H)mm x (8.6)max.(D)mm

(3) LCD Active Area 148.8(W)mm x 53.76(H)mm

(4) Dot Pitch 0.0775(W)mm x 3(R,G,B)(W) x 0.224(H)mm

(5) Resolution 640 x 3(R,G,B))(W) x 240(H) dots

(6) Color Pixel Arrangement R,G,B Vertical stripe

(7) LCD Type Transmissive Color TFT LCD (Normally White)

(8) Display Type Active Matrix

(9) Number of Colors 262k Colors (R,G,B 6bit parallel)

(10) Backlight Cold Cathode Fluorescent Tube (CFL) x 1

Average life time 50kh at 25°C IL=5mA

(11) Weight (140)g

(12) Interface 40pin (C-MOS)

(13) Power Supply Voltage 3.3V only (Include Timing Controller and Power Unit)

(14) Touch Panel Resistance Type

The surface is antiglare type.

(15) Viewing Direction 12 O'clock

KAOHSIUNG HITACHI ELECTRONICS CO.,LTD. DATE Nov.12,'10 Sh. No. 7B64PS 2703-TX16D11VM2CAA-7 PAGE 3-1/1

### 4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MA	VSS=0V				
ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply for Logic	VDD	-0.3	4.0	V	
Input Voltage	VI	-0.2	VDD+0.2		(Note 1)
Input Current	li	0	1	Α	
Static Electricity	VESD0	-	(±100)	V	(Note 2,3)
	VESD1	-	(±8)	kV	(Note 2,4)

Note 1: DTMG,DCLK,RD0~RD5,GD0~GD5,BD0~BD5.

Note 2 : 200pF-250 $\Omega$  25 $^{\circ}$ C - 70%RH

Note 3: Interface Pin Connector.

Note 4: The surface of metal bezel and LCD panel.

#### 4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		COMMENT	
I I E IVI	MIN.	MAX.	MIN.	MAX.	COMMENT	
Ambient Temperature	<b>-20</b> ℃	<b>70</b> ℃	<b>-20</b> ℃	<b>70</b> ℃	(Note 2,3,6)	
Humidity	(N	lote 1)	(Note 1)		Without condensation	
Vibration	-	4.9m/s <sup>2</sup> (0.5G)	ı	19.6m/s <sup>2</sup> (2G) (Note 5)	(Note 4)	
Shock	-	29.4m/s <sup>2</sup> (3G)	1	490m/s <sup>2</sup> (50G) (Note 5)	XYZ directions (Note 7,8)	
Corrosive Gas	Not A	cceptable	Not A	Acceptable		

Note 1 :  $Ta \le 40^{\circ}C$  :85%RH max.

Ta> $40^{\circ}$ C : Absolute humidity must be lower than the humidity of 85%RH at  $40^{\circ}$ C.

Note 2 : For operating condition Ta at  $-20^{\circ}$ C ,  $70^{\circ}$ C < 100h For storage condition Ta at  $-20^{\circ}$ C,  $70^{\circ}$ C < 100h

Note 3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4: 5Hz~100Hz(Except resonance frequency)

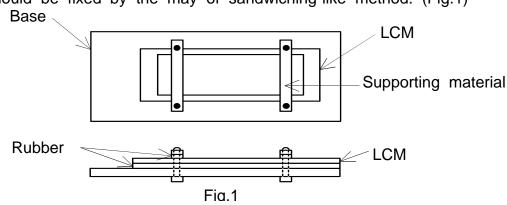
Note 5: This LCM will resume normal operation after finishing the test.

Note 6: The response time will be slower as low temperature.

Note 7: Pulse Width: 10ms

Note 8: The module has no mounting hole.

It should be fixed by the may of sandwiching-like method. (Fig.1)



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### 5. ELECTRICAL CHARACTERISTICS

### 5.1 ELECTRICAL CHARACTERISTICS OF LCD

Ta=25°C,VSS=0V

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	VDD	-	3.0	3.3	3.6	V
Input Voltage for Logic	VI	"H" level	2.0	1	VDD	V
(Note 1)	VI	"L" level	VSS	1	0.8	V
Power Supply Current	IDD for HVGA Display			94		
(Note 2)	Mode	VDD-VSS=3.3V	-	94	-	mA
	IDD for VGA Display	VDD-V33=3.3V		110	-	ША
	Mode					
Vsync Frequency	fV	-	52	60	68	Hz
	fH for HVGA Display		12.8	15.1	36.1	
Hsync Frequency	Mode		12.0	15.1	30.1	kHz
l isylic i requelley	fH for VGA Display		25.3	29.5	36.1	KI IZ
	Mode	-	25.5	29.5	30.1	
	fCLK for HVGA Display		8.7	10.7	26.7	
DCLK Frequency	Mode		0.7	10.7	20.7	MHz
DOLIN Troquericy	fCLK for VGA Display	_	17.2	20.9	26.7	IVII IZ
	Mode	_	11.2	20.9	20.7	

Note 1: DTMG,DCLK, RD0~RD5,GD0~GD5,BD0~BD5.

Note 2 : f V=60Hz, Ta=25 $^{\circ}$ C, Pattern used as display pattern : All Black.

Note 3: Need to make sure of flickering and rippling of display when setting the frame frequency in your set.

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### 5.2 ELECTRICAL CHARACTERISTICS OF TOUCH PANEL

#### 5.2.1 ELECTRICAL CHARACTERISTICS

ITEM		SPECIFICATION	NOTE
Resistance	X1-X2	<b>630~1610</b> Ω	
Between Terminal	Y1-Y2	<b>110~340</b> Ω	
Insulation Resistance	X-Y	<b>20M</b> $\Omega$ min.	Operating Voltage: 25V DC
Linearity	X	1.5% max.	(Note 1,2,3)
Linearity	Υ	1.5% max.	(Note 1,2,3)
Chattering		10ms max.	

### 5.2.2 MECHANICAL CHARACTERISTICS

ITEM	SPECIFICATION	NOTE
Pen Input Pressure	1.2N max.	R0.8 Polyacetal Pen
Surface Hardness	3H min.	JIS K5400

#### 5.2.3 OPTICAL CHARASTERISTICS

ITEM	SPECIFICATION	NOTE
Transparency	80% min.	

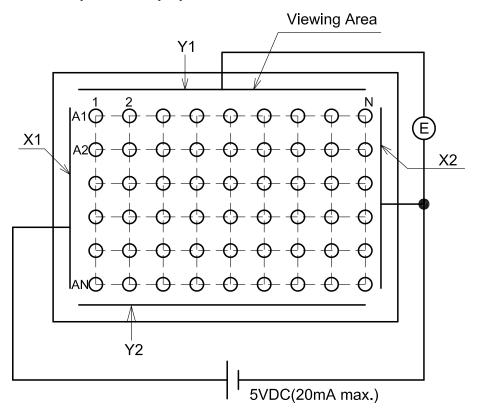
KAOHSIUNG HITACHI			Sh.			
ELECTRONICS CO.,LTD.	DATE	Nov.12,'10	No.	7B64PS 2705-TX16D11VM2CAA-7	PAGE	5-1/3

Note 1: Operating Voltage 5V DC.

Note 2: Test Condition.

(a) Y axis linearity testing method (with tip radius 0.8, polaycetal pen). Vx1-x2=5V, VOUT=Vy1.

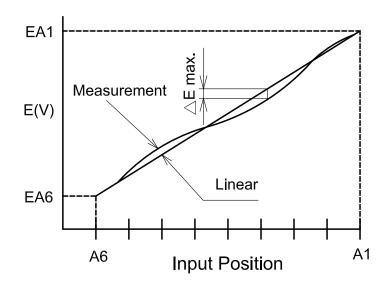
(b) X axis linearity method Vy1-y2=5V, VOUT=Vx1.



Note 3: Calculation

(a) Y axis linearity

Linearity=
$$\frac{\triangle E \text{ max.}}{EA1-EA10}$$
 x100(%)



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### 5.3 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Lamp Voltage	VL	-	(450)	-	Vrms	Ta=25°ℂ
Frequency	fL	-	(55)	-	kHz	
Lamp Current (1Lamp)(Note 7)	IL	2.0	5.0	7.0	mΑ	Ta=25°C
Starting Discharge Voltage	VS (Note 2)	1300	-	-	Vrms	Ta=0°ℂ

- Note 1 : Please design your lamp driving circuit (inverter) based on the above specifications, and inform HITACHI about it.
- Note 2 : Starting discharge voltage is increased when LCM is operating under low temperature.
  - Please check the characteristics of your inverter before applying to your set.
- Note 3 : Average life time of CFL will be decreased when LCM is operating under low temperature.
- Note 4: Under lower driving frequency of an inverter, a certain Backlight system (CFL & CFL reflection sheet) may generate a sound noise. Before designing the inverter, please consider the driving frequency and noise.
- Note 5: When IL is over 7.0mA, it may cause uneven contrast near CFL location, due to heat dispersion form CFL.
- Note 6: We recommend to equip protection circuit (To stop output) which works under abnormal operation to the inverter for CFL.

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### 6. OPTICAL CHARACTERISTICS

### 6.1 OPTICAL CHARACTERISTICS OF LCD

Ta=25°C (Backlight on)

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
		$\theta x$	$\phi$ =0°,K $\geq$ 5.0	-	70	-	deg	1~5
Violuina Aroa		$\theta \mathbf{x}'$	$\phi = 180^{\circ}, K \ge 5.0$	-	70	-	deg	1~5
Viewing Area		$\theta$ y	<i>φ</i> =90°,K≥5.0	-	60	-	deg	1~5
		$\theta$ y	<i>φ</i> =270°,K≥5.0	-	70	-	deg	1~5
Contrast Ratio		К	$\phi = 0^{\circ}, \theta = 0^{\circ}$	100	200	-	-	5
Response Time (rise+fall)		tr+tf	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	(45)	-	ms	6
Color Tone	х		0.57	0.62	0.67	-		
(Primary Color)	Red	у		0.29	0.34	0.39	-	
	Croon	х		0.25	0.30	0.35	-	
	Green	У	1 0° 0 0°	0.54	0.59	0.64	-	
	Dlug	х	$\phi = 0^{\circ},  \theta = 0^{\circ}$	0.09	0.14	0.19	-	
	Blue	у		0.04	0.09	0.14	-	
	White	х		0.24	0.29	0.34	-	
		у		0.26	0.31	0.36	-	

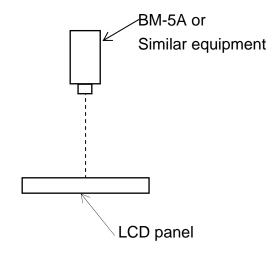
(Measurement condition : HITACHI standard)

(Note 3~6): See next page.

Note 1 : Driving Condition
Display Pattern : White Raster

ICFL Current: (5)mA

Note 2 : Measurement Condition (Transmitance)

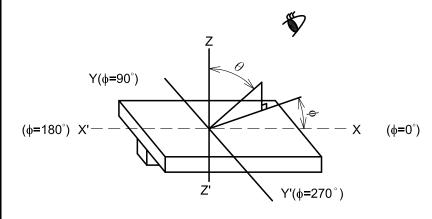


KAOHSIUNG HITACHI	DATE	Navido (40 Sh	7D04D0 0700 TV40D44VM00AA	7 0405	6.4/2
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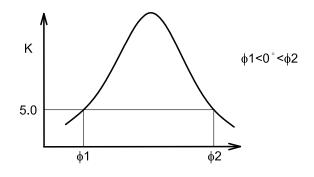
Note 3 : Definition of  $\theta$  and  $\phi$  (Normal) Viewing direction

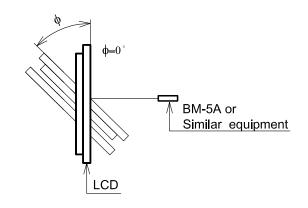
Note 5 : Definition of contrast "K"

K=\frac{\text{White Brightness}}{\text{Black Brightness}}



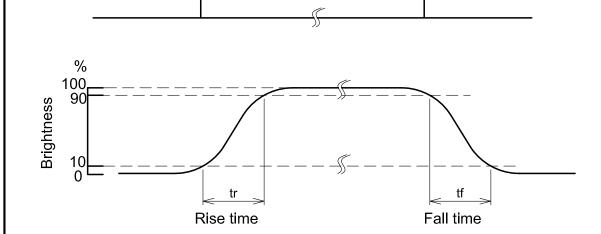
Note 4: Definition of Viewing angle φ1 and φ2





Note 6: Definition optical response time

Black



White

KAOHSIUNG HITACHI ELECTRONICS CO.,LTD.

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7B64PS 2706-TX16D11VM2CAA-7

**Black** 

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### 6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

ITEM	MIN.	TYP.	MAX.	UNIT	NOTE
Brightness	1	280	-	cd/m <sup>2</sup>	IL=(5)mA (Note 1,2)
Rise Time	1	(3)	-	Minute	IL=(5)mA Brightness 80%
Brightness Uniformity	1	-	±25	%	Under mentioned (Note 1,3,4)

(Measurement condition: HITACHI standard)

CFL: 0h operation, Ta=25°C

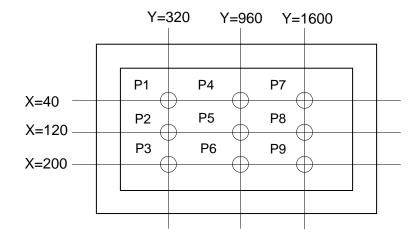
Display data should all be "ON"

Note 1: Measurement after 10 minutes from CFL operating.

Average value of 9 points (Note 3)

Note 2: Brightness control: 100%.

Note 3: Measurement of the following 9 places on the display.

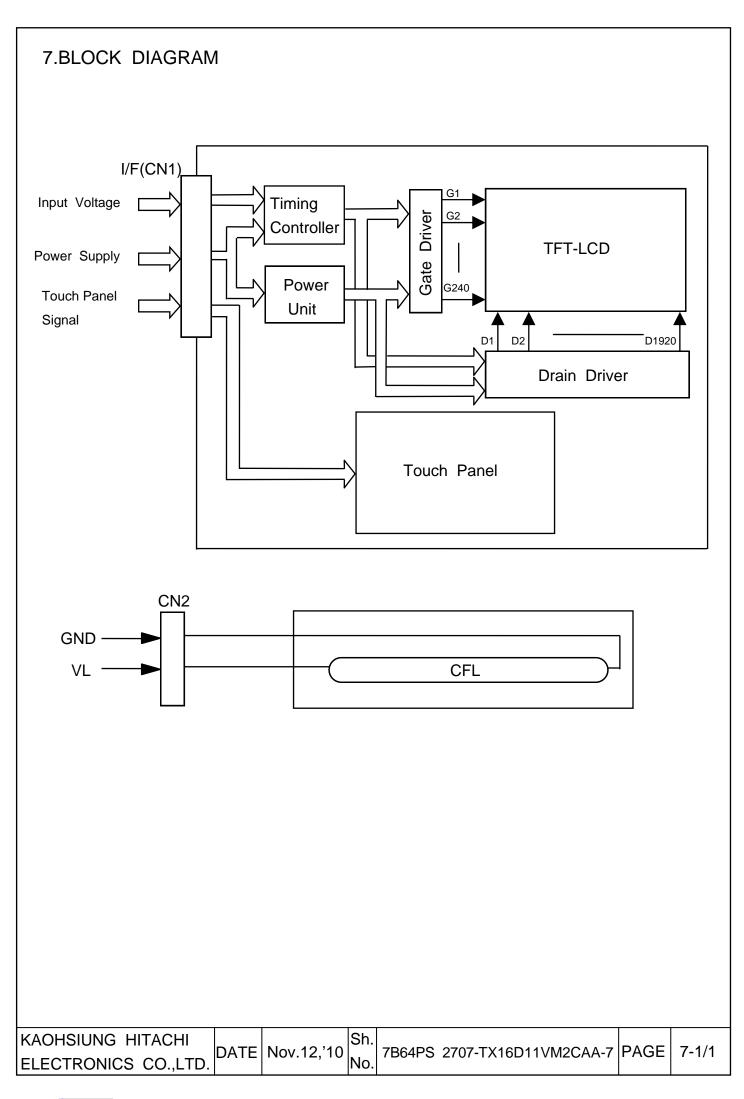


Note 4: Definition of the brightness tolerance.

Max. brightness or Min. brightness - Average brightness

Average brightness - X100

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ELECTRONICS CO.,LTD.	DATE	Nov.12,'10  N	No.	7B04PS 2700-1X10D11VM2CAA-7	PAGE	0-3/3



### 8.INTERFACE TIMING

### 8.1.1 INTERFACE TIMING FOR HVGA DISPLAY MODE

	ITEM	MIN.	TYP.	MAX.	UNIT	SYMBOL	REMARKS
DCLK	Cycle time	37.5	(94)	114.9		tclk	
	Low level Width	15	-	-		twcL	
	High level Width	15	-	-	ns	twch	
	Rise time	-	-	25		trclk	
	Fall time	-	-	25		tfCLK	
	Duty	0.45	0.5	0.55	-	D	D= tclkl/clk
Hsync	Set up time	5	-	-	nc	<b>t</b> sH	for DCLK
	Hold time	10	-	-	ns	tнн	IOI DOLK
	Cycle	679	(709)	739	tclk	thp	
	Valid width	4	5	5	ICLK	twн	
	Rise/Fall time	-	-	30	ns	Thr,thf	
Vsync	Set up	0	-	-	tclk	tsv	for Hsync
	Hold	2	-	-	ICLK	thv	ioi risyric
	Cycle	245	(251)	533	the	<b>t</b> vp	
	Valid width	2	2	2	LHP	twv	
	Rise/Fall time	-	-	50	ns	<b>t</b> ∨r, <b>t</b> ∨f	
DTMG	Set up time	5	-	-	ns	tsı	for DCLK
	Hold time	10	-	-	115	tнı	IOI DOLK
	Rise/Fall time	-	-	30	ns	Tır,tıf	
	Horizontal back porch	24	(37)	50	tclk	<b>t</b> HBP	
	Horizontal front porch	15	(32)	49	ICLK	<b>t</b> HFP	
	Vertical back porch	4	(7)	196	<b>t</b> HP	<b>t</b> vbp	
	Vertical front porch	1	(4)	97	INP	<b>t</b> VFP	
Data	Set up time	5	-	-	ns	tsd	for DCLK
	Hold time	10	-	-	113	<b>t</b> HD	IOI DOLK
	Rise/Fall time	-	-	25	ns	T <sub>Dr</sub> ,t <sub>Df</sub>	

Note: Vsync Cycle No. should be set to odd.

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### 8.1.2 INTERFACE TIMING FOR VGA DISPLAY MODE

	ITEM	MIN.	TYP.	MAX.	UNIT	SYMBOL	REMARKS	
DCLK	Cycle time	37.4	(47.8)	58.1		tclk		
	Low level Width	15	-	-		twcL		
	High level Width	15	-	-	ns	twch		
	Rise time	-	-	25		trclk		
	Fall time	-	-	25		tfCLK		
	Duty	0.45	0.5	0.55	-	D	D= tclkl/clk	
Hsync	Set up time	5	-	-	nc	tsн	for DCLK	
	Hold time	10	-	-	ns	tнн	IOI DOLK	
	Cycle	679	(709)	739	tclk	thp		
	Valid width	4	5	5	ICLK	twн		
	Rise/Fall time	-	-	30	ns	Thr,thf		
Vsync	Set up	0	tour	tsv	for House			
	Hold	2	-	-	tclk	thv	for Hsync	
	Cycle	485	(491)	533	<b>t</b> HP	<b>t</b> vp		
	Valid width	2	2	2	LHP	twv		
	Rise/Fall time	-	-	50	ns	<b>t</b> ∨r, <b>t</b> ∨f		
DTMG	Set up time	5	-	-	ne	tsı	for DCLK	
	Hold time	10	-	-	ns	tнı	101 DOLK	
	Rise/Fall time	-	-	30	ns	Tır,tıf		
	Horizontal back porch	24	(37)	50	tclk	<b>t</b> HBP		
	Horizontal front porch	15	(32)	49	ICLK	<b>t</b> HFP		
	Vertical back porch	4	(7)	28	tHP	<b>t</b> VBP		
	Vertical front porch	1	(4)	25	LHP	<b>t</b> VFP		
Data	Set up time	5	-	-	ns	tsp	for DCLK	
	Hold time	10	-	-	113	thd	IUI DCLK	
	Rise/Fall time	-	-	25	ns	Tor,tof		

Note: Vsync Cycle No. should be set to odd.

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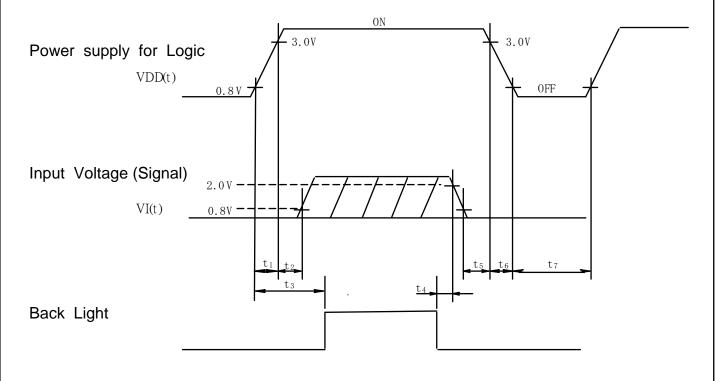
# 8.2 TIMING CHART (Data is latched negative edge trigger of DCLK) $t_{Hf}$ , $t_{Vf}$ $t_{Hr}$ , $t_{Vr}$ t<sub>If</sub>, t<sub>Df</sub> t<sub>Ir</sub>, t<sub>Dr</sub> VSYNC, HSYNC, DTMG, $V_{\mathrm{IH}}$ min R0~5, G0~5, B0~5 $V_{\rm IL}$ max. DCLK $t_{HD}$ Invalid Data Invalid Data DATA $t_{ m HI}$ DTMG DCLK $\mathbf{t}_{\underline{SH}}$ Hsync $t_{HV}$ Vsync $t_{HP}$ t<sub>WH</sub> Hsync $t_{HBP}$ $t_{HFP}$ DTMG $t_{VP}$ -sf Vsync $t_{VFP}$ DTMG

Note 1: DTMG is definition of the above timing for Hsync and Vsync.

Note 2: No matter when Hsync and Vsync is inputted ,this LCM can be drove only DTMG Signal. DTMG should be set to low level when it is not input valid data.

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### 8.3 POWER ON/OFF SEQUENCE



 $\begin{array}{c} \underline{\text{POWER ON}} \\ & t_1 \leqq 15\,\text{ms} \\ 0\,\text{ms} < t_2 \leqq 45\,\text{ms} \\ 0.\,1s \leqq t_3 \end{array}$ 

 $\begin{array}{c} \underline{\text{POWER OFF}} \\ 5\,\text{ms} & \leq t_4 \\ 0\,\text{ms} & \leq t_5 \leq 45\,\text{ms} \\ 0\,\text{ms} & \leq t_6 \leq 20\,\text{ms} \\ 0.\,\,4\text{s} & \leq t_7 \end{array}$ 

Note 1 :  $0V \le VI(t) \le VDD(t)$ 

VI(t) and VDD(t) is a surfeit of condition for power on/off.

Note 2 : Input Voltage(Signal) should not be set high impedance when power on.

					_
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### 8.4 RELATIONSHIP BETWEEN DISPLAYED COLOR AND INPUT DATA

	COLOR & GRAY	GRAY SCALE								DA	TA S	SIGN	IAL							
	SCALE	LEVELS	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	ВЗ	В4	B5
	Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	-	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Cyan	-	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Color	Red	-	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	-	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	-	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\uparrow$	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	$\uparrow$	$\downarrow$				$\downarrow$						$\downarrow$					$\downarrow$			
Neu	$\downarrow$	$\downarrow$		ā.	ā.	<u> </u>	-	_		_	-	$\downarrow$		-		_		-	-	
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	$\downarrow$	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\uparrow$	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Green	<b>↑</b>	$\downarrow$				$\downarrow$						$\downarrow$					$\downarrow$			
Green	$\downarrow$	$\downarrow$				$\downarrow$						$\downarrow$					$\downarrow$			
	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	<b>V</b>	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\uparrow$	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Blue	<b>↑</b>	$\downarrow$				$\downarrow$						$\downarrow$					$\downarrow$			
Diue	$\downarrow$	$\downarrow$				$\downarrow$						$\downarrow$					$\downarrow$			
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	<b>V</b>	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

				-
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#### 8.5 INTERNAL PIN CONNECTION

CN1 JAE: FA5B040HP1(Suitable FPC: t0.3±0.05mm, 0.5±0.05mm pitch)

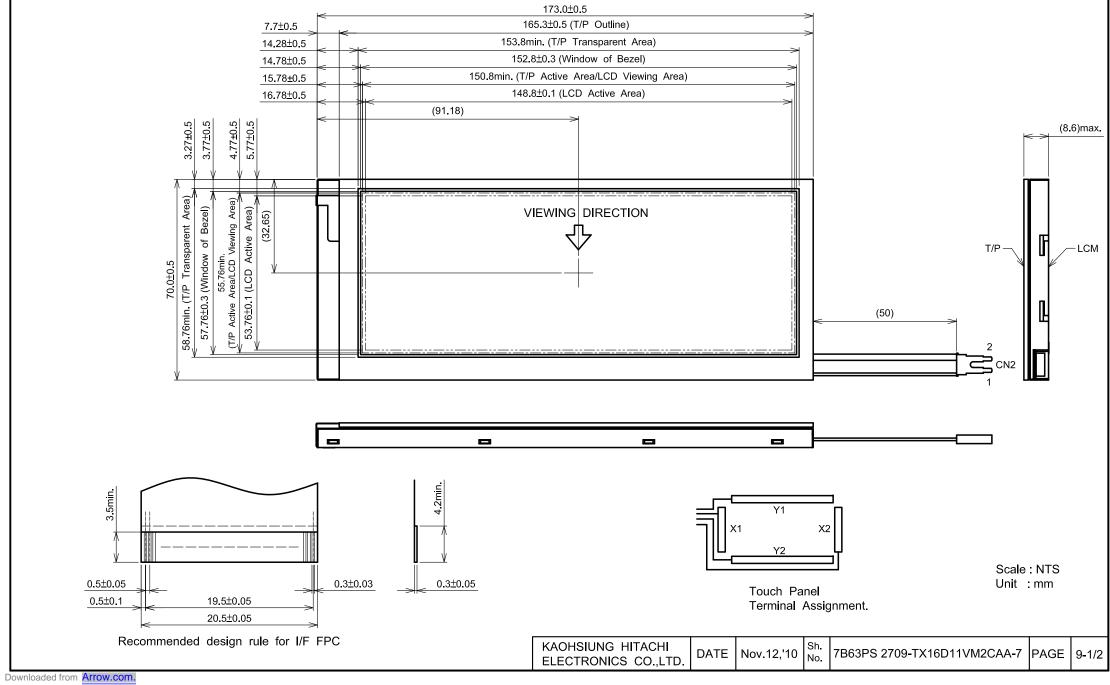
PIN No.	SIGNAL	FUNCTION
1	VDD	Power Supply for Logic
2	VDD	Power Supply for Logic
3	VDD	
4	VDD	Power Supply for Logic
5	NC	Power Supply for Logic
6	DTMG	No Connection
7	VSS	Timing Signal for Data GND
		Dot Clock
8	DCLK	
9	VSS	GND
10	NC	No Connection
11	VSS	GND
12	B5	
13	B4	_Blue Data
14	B3	
15	VSS	GND
16	B2	
17	B1	_Blue Data
18	B0	
19	VSS	GND
20	G5	
21	G4	Green Data
22	G3	
23	VSS	GND
24	G2	
25	G1	Green Data
26	G0	
27	VSS	GND
28	R5	
29	R4	Red Data
30	R3	
31	VSS	GND
32	R2	
33	R1	Red Data
34	R0	
35	Vcom	Common Voltage (Generated by LCM)
36	VSS	GND
37	X1	Analog Signal Touch Panel
38	Y1	Analog Signal Touch Panel
39	X2	Analog Signal Touch Panel
40	Y2	Analog Signal Touch Panel
		CD 00\/C 1 (Cuitable Connector: ICT CM00D DUC

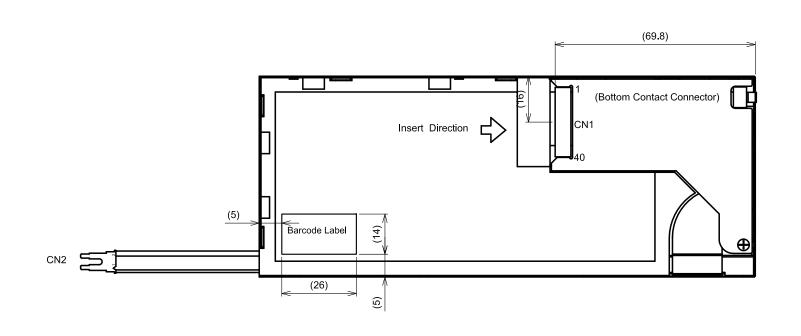
CN2 JST Housing : BHSR-02VS-1 (Suitable Connector : JST SM02B-BHSS-1)
Contact pin : SBHS-002T-P0.5

PIN No.	SIGNAL	LEVEL	FUNCTION
1	VSS	-	GND for CFL
2	VCFL	-	Power Supply for CFL

							i
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ELECTRONICS CO.,LTD.	DATE	140v.12, 10	No.	7604F3 2700-1X10D11VW2CAA-7 F	AGL	0-0/0	

### 9. DIMENSIONAL OUTLINE 9.1 DIMENDIONAL OUTLINE OF LCM





Scale : NTS Unit : mm

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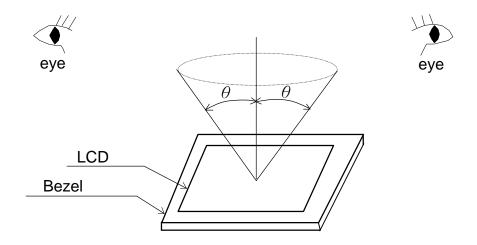
### 10. APPEARANCE STANDARD

#### 10.1 APPEARANCE INSPECTION CONDITION

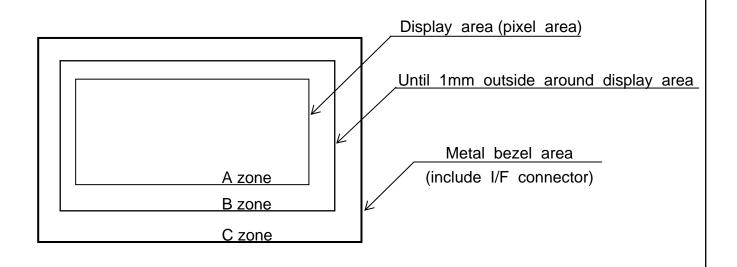
Visual inspection should be done under the following condition.

- (1) The inspection should be done in a dark room.(More than 1000(lx) and non-directive)
- (2) The distance between eyes of an inspector and the LCD module is 30cm.
- (3) The viewing zone is shown the figure.

The  $\theta$  is defined as  $\theta \le 45^{\circ}$  for LCM power off  $\theta \le 5^{\circ}$  for LCM power on



#### 10.2 DEFINITION OF ZONE



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ELECTRONICS CO.,LTD.	DATE	NOV. 12, 10	No.	7864PS	PAGE	10-1/5	l

### 10.3 APPEARANCE SPECIFICATION

### (1)LCD Appearance

\*) If the problem related to this section occurs about this item, the responsible persons of both party (Customer and HITACHI) will discuss the matter in detail.

No.	ITEM			CRITE	RIA			APPLIED ZONE	
	Scratches	Length L(mm)		Width W(mm)	nι	ximum umber eptable	Minimum space		
		Ignored		W≦0.02		nored	-	A,B	
		L≦40	0.0	02 <w≦0.04< td=""><td></td><td>10</td><td>-</td><td></td></w≦0.04<>		10	-		
		L≦20		W≦0.04		10	-		
	Dent	Distinguished (To be judged		is acceptable	ard)			А	
	Wrinkles in Polarizer	Same as abo	ove					Α	
	Bubbles	Averag D	e di (mm			Maximum accept			
		D	≦0.	2		Igno	red		
		0.2 <d< td=""><td></td><td></td><td></td><td>12</td><td>2</td><td>_ A  </td></d<>				12	2	_ A	
		0.3 <d< td=""><td>≦0.</td><td>5</td><td></td><td>3</td><td></td><td></td></d<>	≦0.	5		3			
		0.5 <d< td=""><td></td><td></td><td></td><td>nor</td><td>ne</td><td></td></d<>				nor	ne		
	Stains			Filamentous (	Line s	shape)			
	Foreign	Length		Width			ım number		
	Materials	L(mm)		W(mm)			eptable	A,B	
L	Daule Coat	L≦2.0		W≦0		Ig	nored		
	Dark Spot	L≦3.0		0.03 <w≤0< td=""><td></td><td></td><td>6</td><td></td></w≤0<>			6		
С		L≦2.5		0.05 <w≦0< td=""><td></td><td></td><td>1</td><td></td></w≦0<>			1		
				Round(Dot					
D		Average diame	eter	Maximum nur		Minim	um Space		
		D(mm) D<0.2		acceptable	е				
		0.2≦D<0.3		Ignored 10		1	 0 mm	A,B	
		0.2≦D < 0.4		5			0 mm	A,b	
		0.4≦D	•	none			-		
		The total num	her		nentoi	ıs + Round	d=10		
				easily are accep		io i reduit	<u>u— 10</u>		
	Color Tone			HITACHI STA		RD		Α	
•	Color Uniformity	Same as abo						Α	
	Dot Defect					Maximum accept			
		Sparkle mode		1 dot		4			
				2 dots		1			
			Tota	al (Note.(3)-(f))		5		Α	
		Black mode		1 dot		5		_	
				2 dots		2			
				al (Note.(3)-(f))		5			
		<u> </u>	Tota	al (Note.(3)-(f))		10	)		

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### (2) CFL BACKLIGHT APPEARANCE

No.	ITEM			APPLIED ZONE		
С	Dark Spots White Spots	Average diam D(mm)	eter	Maximum	number acceptable	
F	Foreign Materials	D≦0.4			ignored	A
L.	(Spot)	0.4 <d< td=""><td></td><td></td><td>none</td><td></td></d<>			none	
	Foreign Materials (Line)	Width W(mm)		ngth nm)	Maximum number acceptable	
Α		W≤0.2	L≦	L≦2.5	1	Α
С		VV <u>≦</u> 0.∠	2.5	<L	None	
K		0.2 <w< td=""><td></td><td>-</td><td>none</td><td></td></w<>		-	none	
L	Scratches	Width	Ler	ngth	Maximum number	
I		W(mm)	L(n	nm)	acceptable	
G	W≦0.1		,	-	ignored	_
H		0.1 < W ≦ 0.2	L≦	11.0	1	A
Т		U. I \ VV <u>≥</u> U.Z	11.0	) <l< td=""><td>None</td><td></td></l<>	None	
		0.2 <w< td=""><td></td><td>-</td><td>none</td><td></td></w<>		-	none	

KAOHSIUNG HITACHI	DATE	Nov 12 '10	Sh.	7DC4DC 0740 TV4CD44\/M0C4 A 7	DACE	10.2/5
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### (3) TOUCH PANEL APPEARANCE

ITEM	CRITERIA								
	W>0.1	-	None						
Scratch	0.10≧W>0.05	10 <l< td=""><td>None</td></l<>	None						
	0.05≧W -		ignored						
	W>0.10	-	Dust(Circular)						
Dust(Linear)	0.10≧W>0.05	3 <l< td=""><td colspan="3">None</td></l<>	None						
	0.05≧W	-	ignored						
	D>	0.3	None						
Dust(Circular)	0.3≧D	>0.25	Maximum 3pcs(Dust to Dust>20mm)						
	0.25	5>D	ignored						

Applied only in the active area. Scratches or dusts in the outside of the active area are acceptable unless the electrical characteristics are affected.

• Dirt

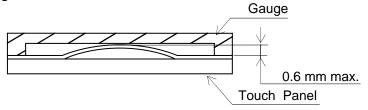
Acceptable if not noticeable on a black mat.

• Tip, crack (applicable to glass only)

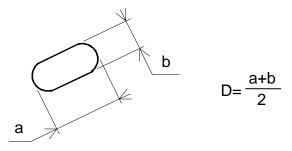
ITEM	CRITERIA									
	X X Z	Х	≦2							
Tip Corner		Υ	≦5	Not acceptable if the film is damaged						
		Z	≦1.1							
	X	Х	≦5							
Tip Side	z	Υ	≦3	Not acceptable if the film is damaged						
		Z	≦1.1							
Crack				None						
Other		<u> </u>	<b>Y</b> ≦1	Not acceptable if the electrical Characteristics is affected						

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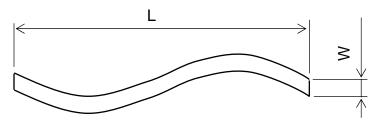
### Blistering Puffiness



Note 1: Definition of average diameter (D)



Note 2: Definition of length (L) and width (W)



Note 3: Definition of dot defect

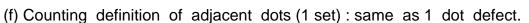
- (a) Dot Defect: Defect Area > 1/2 dot
- (b) Sparkle mode: Brightness of dot is more than 30% at Black raster.
- (c) Black mode: Brightness of dot is less than 70% at R.G.B raster.
- (d) 1 dot: Defect dot is isolated, not attached to other defect dot.
- (e) N dot: N defect dots are consecutive (fig.1).

  (N means the number of defect dots.)

( fig .1)												
R	G	В	R	G	В	R	G	В				
				Χ								

2 dots defect included defect dot "X" is defined as follows.

Adjacent dots to defect dot "X":



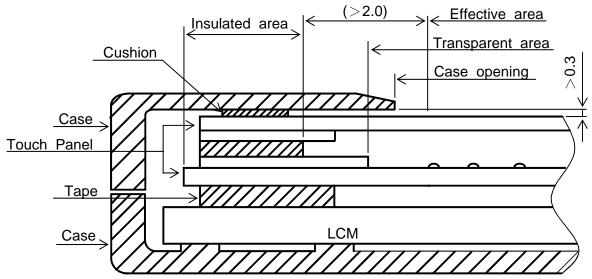
(g) Those wiped out easily are acceptable.

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#### 11. PRECAUTION IN DESIGN

#### 11.1 MOUNTING PRECAUTION

(1) When assembling the Touch Panel and you case, please refer to the figure below.



- (2) The clearance between the Touch Panel and case shall be designed so that the case edge never presses the input screen when it is deformed by heat or other causes.
- (3) The case shall be designed not to touch the tail portion (FPC for Touch Panel).
- (4) The boundary space between the effective area and the insulated area is unstable. Touching this area may effect the operation of the Touch Panel. The case must be designed so that it does not touch the boundary space.

#### 11.2 PRECAUTIONS AGAINST ELECTROSTATIC DISCHARGE

As this module contains C-MOS LSIs, it is not strong against electrostatic discharge. Make certain that the operator's body is connected to the ground through a list band, etc. And don't touch I/F pins directly.

#### 11.3 HANDLING PRECAUTIONS

- (1) Since the Touch Panel on the top, and the frame on the bottom tend to be easily damaged, they should be with full care so as not to get them touched, pushed or rubbed by a piece on glass, tweezers and anything else which are harder a pencil lead 2H.
- (2) As the adhesives used for adhering upper/lower polarizer's and frame are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol. The following are recommended for use:

normal hexane

Please contact with us when it is necessary for you to use chemicals other than the above.

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- (3) Lightly wipe to clean the dirty surface with absorbent cotton or other soft material like chamois, soaked in the recommended chemicals without scrubbing it hardly. Always wipe the surface horizontally or vertically. Never give a wipe in a circle. To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.
- (4) Immediately wipe off saliva or water drop attached on the display area because it may cause deformation or faded color.
- (5) Fogy dew deposited on the surface may cause a damage, stain or dirt to the polarizer.
  - When you need to take out the LCD module from some place at low temperature for test, etc.
  - It is required to be warmed them up to temperature higher than room temperature before taking them out.
- (6) Touching the display area or I/F pins with bare hands or contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched with bare hands.

  (Some cosmetics are detrimental to polarizer's.)
- (7) In general, the glass is fragile so that, especially on its periphery, tends to be cracked or chipped in handling. Please not give the LCD module sharp shocks by falling, etc.
- (8) Maximum pressure to the surface must be less than 1.96×10<sup>4</sup> Pa.

  And if the pressure area is less than 1cm<sup>2</sup>, maximum pressure must be less than 1.96N.
- (9) Since the metal width is narrow on these locations (see page 9-1/2), please careful with handling.
- (10) Top sheets shall be cleaned gently using a soft cloth such as those used for glasses.

Hard wiping accumulated dust will leave scars on the surface even using a cloth.

#### 11.4 OPERATION PRECAUTION

- (1) Using a LCM module beyond its maximum ratings may result in its permanent destruction.
  - LCM module's should usually be used under recommended operating conditions shown in chapter 4.
  - Exceeding any of these conditions may adversely affect its reliability.
- (2) Response time will be extremely delayed at lower temperature than the specified operating temperature range and on the other hand LCD's shows dark blue at higher temperature.
  - However those phenomena do not main defects of the LCD module. Those phenomena will disappear in the specified operating temperature range.
- (3) If the display area is pushed hard during operation, some display patterns will be abnormally display.

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- (4) A slight dew depositing on terminals may cause electrochemical reaction which leads to terminal open circuit. Please operate the LCD module under the relative condition of 40°C 85%RH.
- (5) Resistance range: Your controller shall be set up to allow the resistance range of Touch Panel specified in our CAS.
- (6) Pointed position of Touch Panel may shift owing to a change in resistance of Touch Panel depending on the operation condition. To compensate this shift, the set shall be given a calibration function.
- (7) Input shall be made with a stylus pen (poly acetal, R0.8). Chances are very high that use of a metal piece including a ball point pen or sharp edge will impair accuracy.
- (8) The Touch Panel is an auxiliary input device. The system shall be designed to have other input device.

### 11.5 STORAGE

In case of storing LCD module for a long period of time (for instance, for years) for the purpose of replacement use, the following precautions necessary.

- (1) Store the LCD modules in a dark place; do not expose them to sunlight or ultraviolet rays.
- (2) Keep the temperature between  $10^{\circ}$ C and  $35^{\circ}$ C at normal humidity.
- (3) Store the LCD modules in the container which is used for shipping from us.
- (4) No articles shall be left on the surface over an extended period of time.

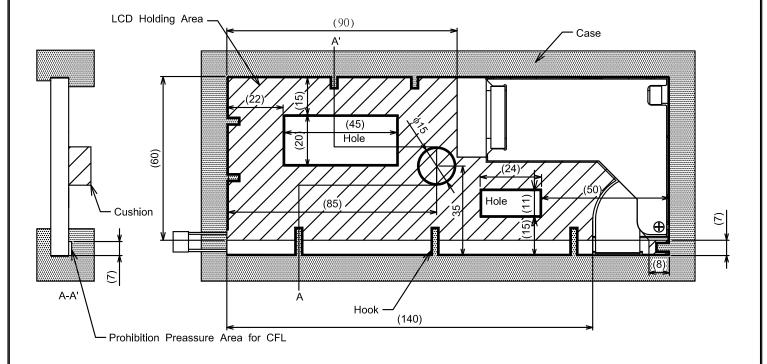
#### 11.6 SAFETY

Wear finger cots or gloves whenever handling or assembling a Touch Panel its glass edges are sharp.

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### 11.7 MOUNTING PRECAUTION

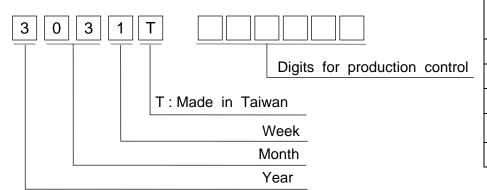
- (1) When assembling the LCM Module, please refer to the below.
- (2) The use of cushion is recommended in order to protect the module from shock.
- (3) To pervent the module cover from being pressed, the distance between the case and cushion, should be shorter than 1.0mm.



### 12. DESIGNATION OF LOT MARK

### 12.1 LOT MARK

Lot mark is consisted of 5 digits for production lot and 6 digits for production control.



Year	Figure in
	lot mark
2010	0
2011	1
2012	2
2013	3
2014	4

Month	Figure in	Month	Figure in
Month	lot mark	Month	lot mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Figure in
(day in calendar)	lot mark
1~ 7	1
8~14	2
15~21	3
22~28	4
29~31	5

#### 12.2 SERIAL No.

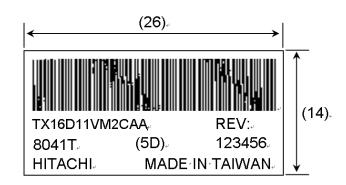
Serial No. is consisted of 5 digits number (00001~99999).

### 12.3 LOCATION OF LOT MARK

Label is bring attached on the back side of module.

### 12.4 REVISION(Rev.) CONTROL

Rev No.	ITEM
Α	CN1 JAE: FA5B040HP1R3000



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### 13. PRECAUTION FOR USE

- (1) A limit sample should be provided by the both parities on an occasion when the both parties agree to its necessity.
  Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- (2) On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
  - (1) When a question is arisen in the specifications.
  - (2) When a new problem is arisen which is not specified in this specifications.
  - (3) When an inspection specifications change or operating condition change by customer is reported to HITACHI, and some problem is arisen in the specification due to the change.
  - (4) When a new problem is arisen at the customer's operating set for sample evaluation.
- (3) Regarding the treatment for maintenance and repairing, both parties will discuss it in six months later after latest delivery of this product.

The precaution that should be observed when handling LCM have been explained above.

If any points are unclear or if you have any requests, please contact with HITACHI.

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