



Power Management IC For Bay Trail -M/-D/-I Platform - BD9596MWV

BD9596MWV (Jarrell Cove) is a Power Management Integrated Circuit (PMIC) designed specifically for use on Bay Trail-M/-D/-I platform for in-vehicle infotainment (IVI) systems, industrial control systems.

Request for Documents

VIDEO CONTACT US FAQ

\* This product is a STANDARD grade product and not recommend for on-vehicle devices.

PART NUMBER	STATUS	PACKAGE	UNIT QUANTITY	MINIMUM PACKAGE QUANTITY	PACKING TYPE	CONSTITUTION MATERIALS LIST	ROHS
BD9596MWV-E2	Active	UQFN88MV0100	1000	1000	Taping	<a href="#">Inquiry</a>	<a href="#">Yes</a>

TECHNICAL DATA

PART EXPLANATION

For ICs

SPECIFICATIONS:	FEATURES:
<div>GRADE</div> <div>Vin1(Min.)[V]</div> <div>Vin1(Max.)[V]</div> <div>SW frequency(Max.)[MHz]</div> <div>Operating Temperature (Min.)[°C]</div> <div>Operating Temperature (Max.)[°C]</div> <div>STANDARD</div> <div>3.5</div> <div>5.5</div> <div>1.0</div> <div>-40</div> <div>95</div>	<div>⌘Complicated Power up / down sequence and state control are embedded.</div> <div>⌘Fewer external components count / compact size.</div> <div>⌘Controlling power regulation for each state (S4/S5, S3 and S0) results in a low power consumption.</div> <div>⌘Built-in reference clock (RTCCLK): 32.768kHz±5%.</div> <div>⌘Power control logic with processor interface and event detection.</div> <div>⌘SVID interface for control and register access.</div> <div>⌘Error mode indicator for debugging.</div> <div>⌘Built-in UVLO, SCP, OVP and TSD protection.</div>

APPLICATIONS:	PIN CONFIGURATION:
N/A	N/A

SUPPORTING LINKS:	
N/A	

RECENT CLICKS:

Other New/Updated Products Relating to Power Management

PART NUMBER	Product Series	Datasheet	Distribution Inventory
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NEW PRODUCTS:

New Product List

