



WPH4003

N-Channel Power MOSFET 1700V, 3A, 10.5Ω, TO-3PF-3L

ON Semiconductor®

http://onsemi.com

Features

- ON-resistance RDS (on) = 8.2Ω (typ.)
- Input Capacitance Ciss=850pF (typ.)
- 10V drive

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		1700	V
Gate-to-Source Voltage	V _{GSS}		±30	V
Drain Current (DC)	I _{DC} ^{*1}	Limited only maximum temperature T _{ch} =150°C	3	A
	I _{Dpack} ^{*2}	T _c =25°C (Our ideal heat dissipation condition) ^{*3}	2.5	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	6	A
Allowable Power Dissipation	PD		3.0	W
		T _c =25°C	55	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C
Avalanche Energy (Single Pulse) ^{*4}	E _A		49	mJ
Avalanche Current ^{*5}	I _{AV}		3	A

Note : ^{*1} Shows chip capability

*2 Package limited

*3 Our condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

*4 V_{DD}=50V, L=10mH, I_{AV}=3A (Fig.1)

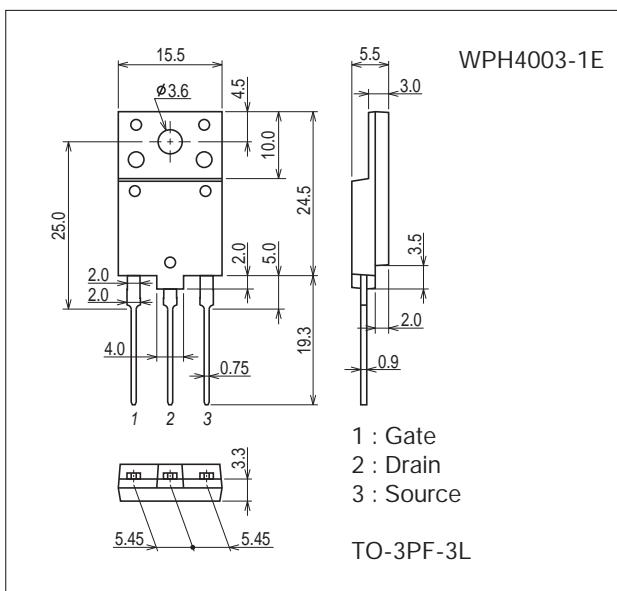
*5 L≤10mH, single pulse

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Package Dimensions

unit : mm (typ)

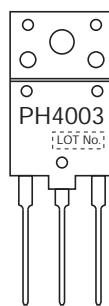
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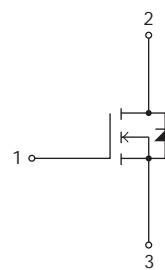
Product & Package Information

- Package : TO-3PF-3L
- JEITA, JEDEC : SC-96
- Minimum Packing Quantity : 30 pcs./magazine

Marking



Electrical Connection



Electrical Characteristics at $T_a=25^\circ C$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V(BR)_{DSS}$	$I_D=10\text{mA}, V_{GS}=0\text{V}$	1700			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=1360\text{V}, V_{GS}=0\text{V}$			1	mA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30\text{V}, V_{DS}=0\text{V}$			± 100	nA
Cutoff Voltage	$V_{GS(\text{off})}$	$V_{DS}=10\text{V}, I_D=1\text{mA}$	2		4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=20\text{V}, I_D=1.5\text{A}$	1.2	2.4		S
Static Drain-to-Source On-State Resistance	$R_{DS(\text{on})}$	$I_D=1.5\text{A}, V_{GS}=10\text{V}$		8.2	10.5	Ω
Input Capacitance	C_{iss}	$V_{DS}=30\text{V}, f=1\text{MHz}$		850		pF
Output Capacitance	C_{oss}			90		pF
Reverse Transfer Capacitance	C_{rss}			27		pF
Turn-ON Delay Time	$t_{d(\text{on})}$	See Fig.2		19		ns
Rise Time	t_r			21		ns
Turn-OFF Delay Time	$t_{d(\text{off})}$			200		ns
Fall Time	t_f			55		ns
Total Gate Charge	Q_g	$V_{DS}=200\text{V}, V_{GS}=10\text{V}, I_D=3\text{A}$		48		nC
Gate-to-Source Charge	Q_{gs}			6		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			22		nC
Diode Forward Voltage	V_{SD}	$I_S=3\text{A}, V_{GS}=0\text{V}$		0.8	1.5	V
Reverse Recovery Time	t_{rr}	See Fig.3		410		ns
Reverse Recovery Charge	Q_{rr}		$I_S=3\text{A}, V_{GS}=0\text{V}, dI/dt=100\text{A}/\mu\text{s}$	3000		nC

Fig.1 Unclamped Inductive Switching Test Circuit

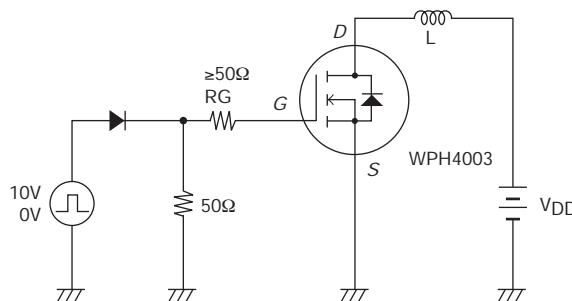


Fig.2 Switching Time Test Circuit

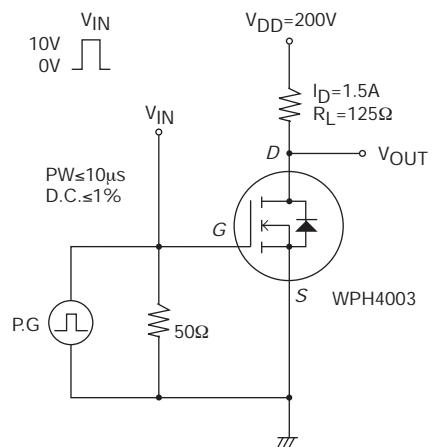
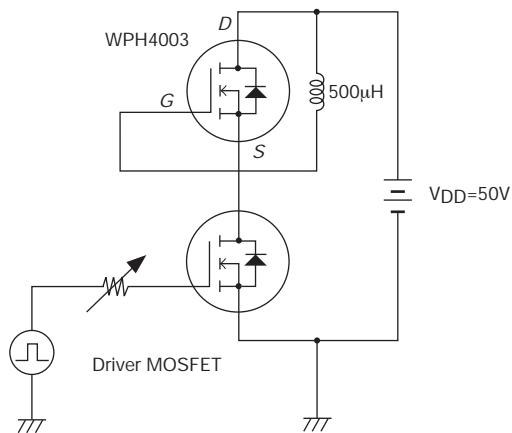
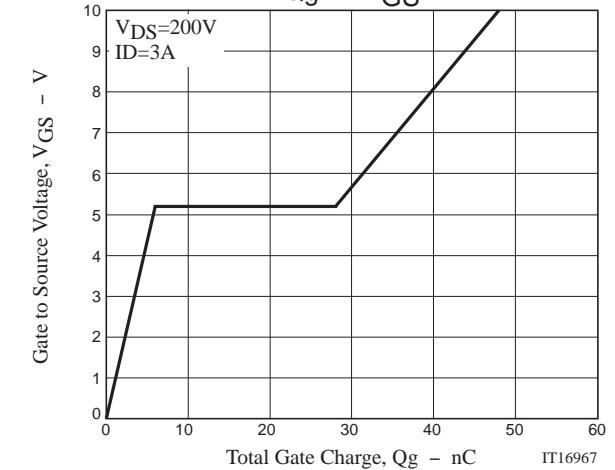
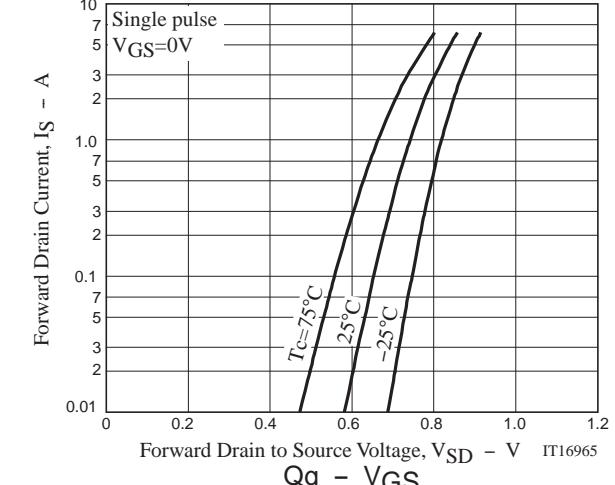
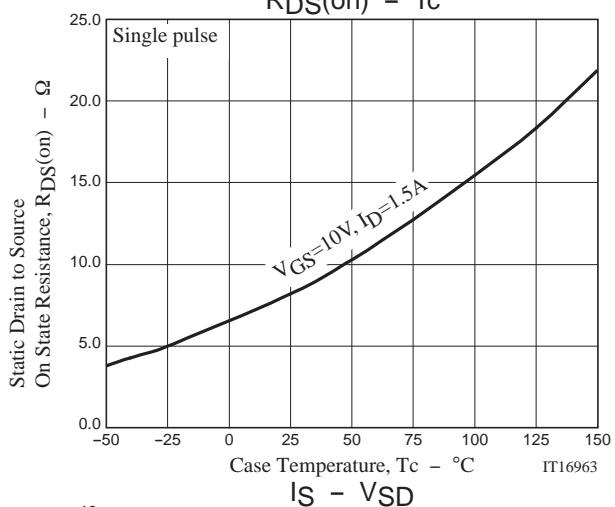
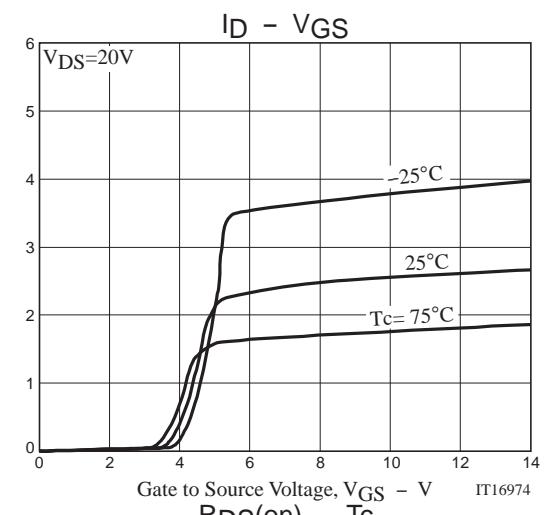
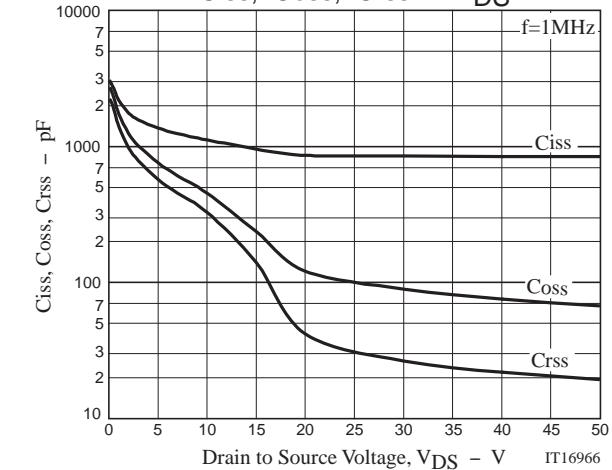
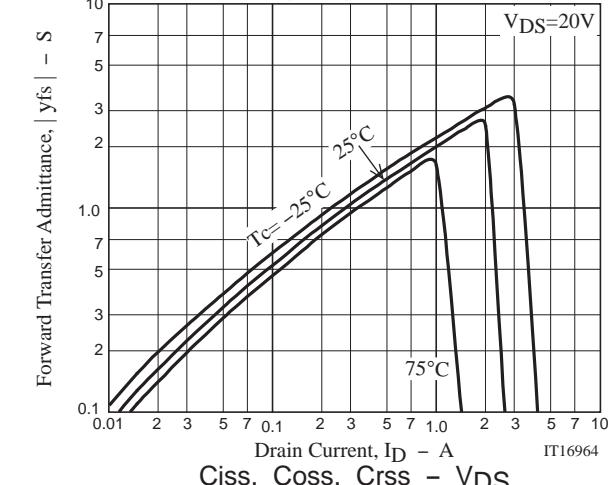
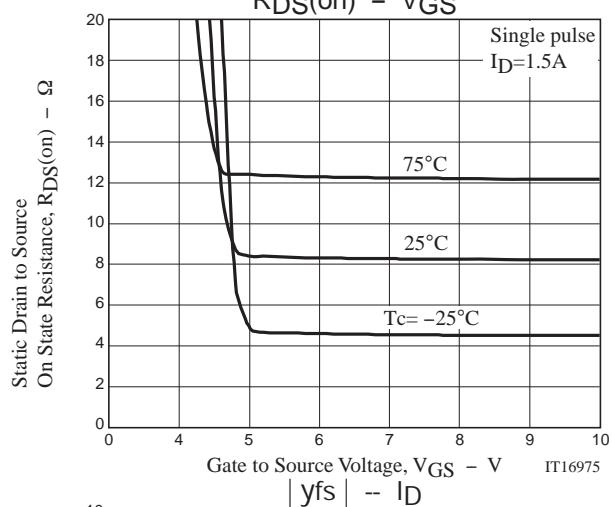
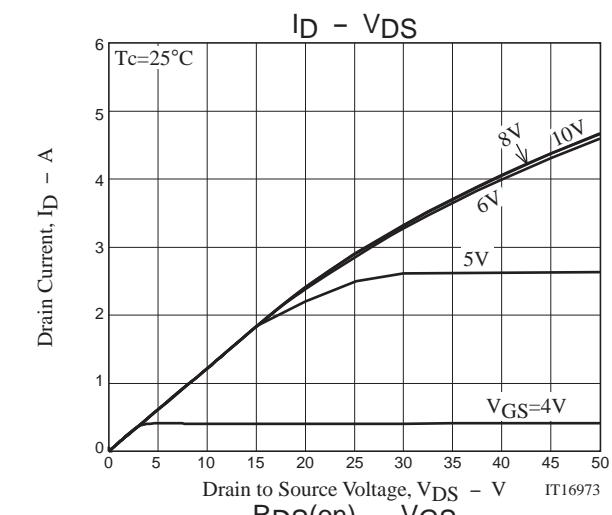


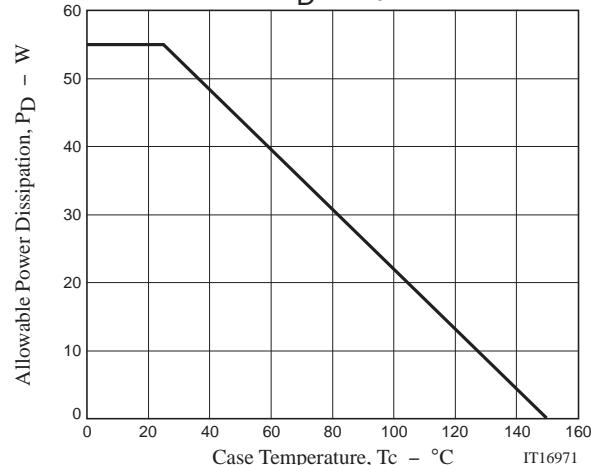
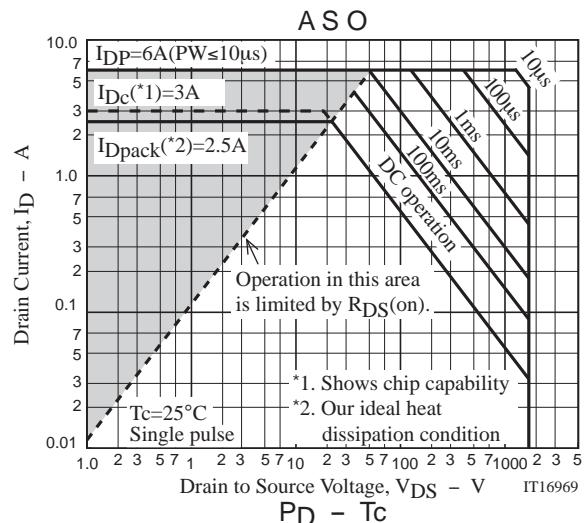
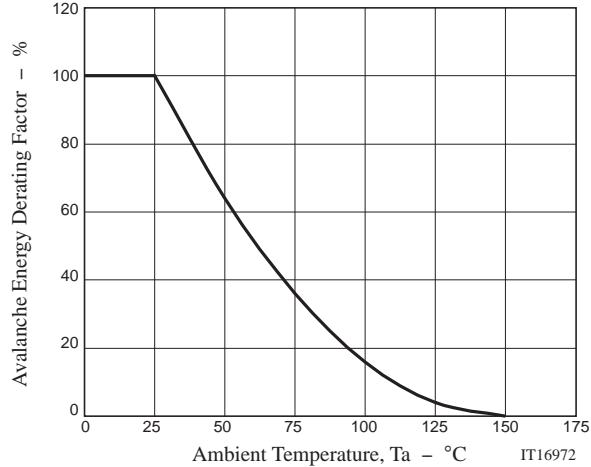
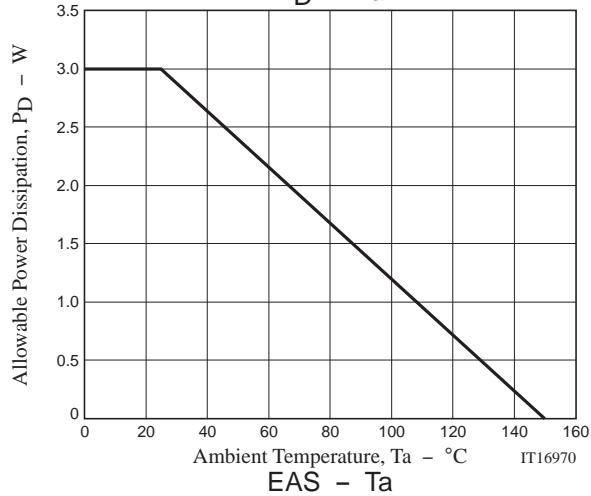
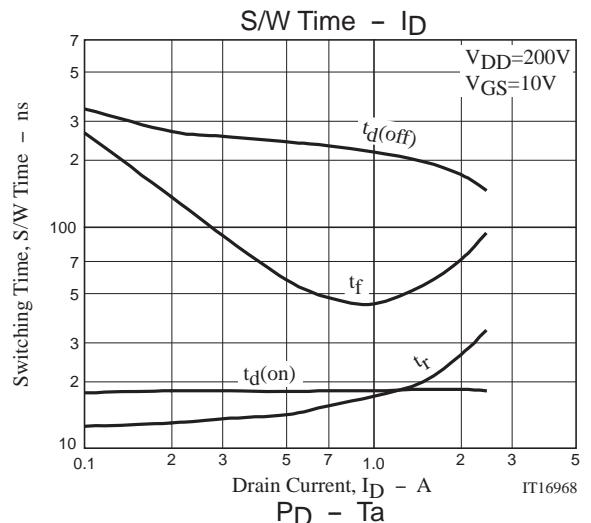
Fig.3 Reverse Recovery Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
WPH4003-1E	TO-3PF-3L	30pcs./magazine	Pb Free





Magazine Specification

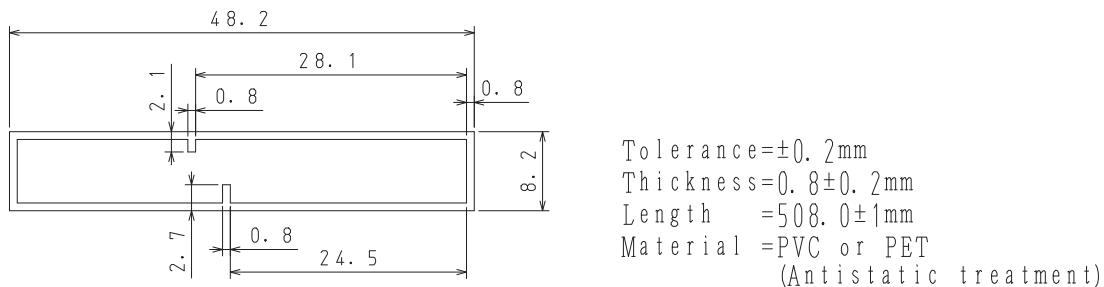
WPH4003-1E

1. Packing Format

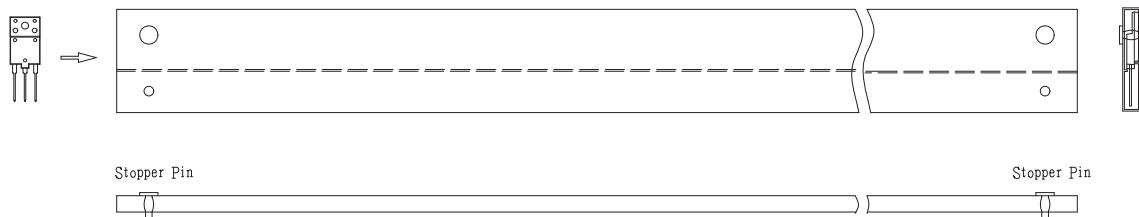
Package Name	Maximum Number of devices contained (pcs)			Packing format	
	Magazine	Inner box	Outer box	Inner BOX	Outer BOX
TO-3PF-3L	30	360	1440	SPD-0V0001 12 magazines contained Dimensions:mm (external) 568×150×55	SPD-LV0010 4 inner boxes contained Dimensions:mm (external) 590x225x178

2. Magazine dimensions

(unit:mm)



3. Storage method to magazine

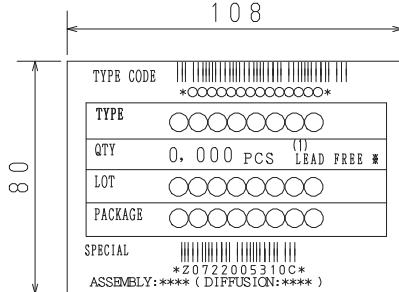


4. Inner box label (unit: mm)

Stopper Pin

5. Outer box label (unit:mm)

It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.



NOTE (1)

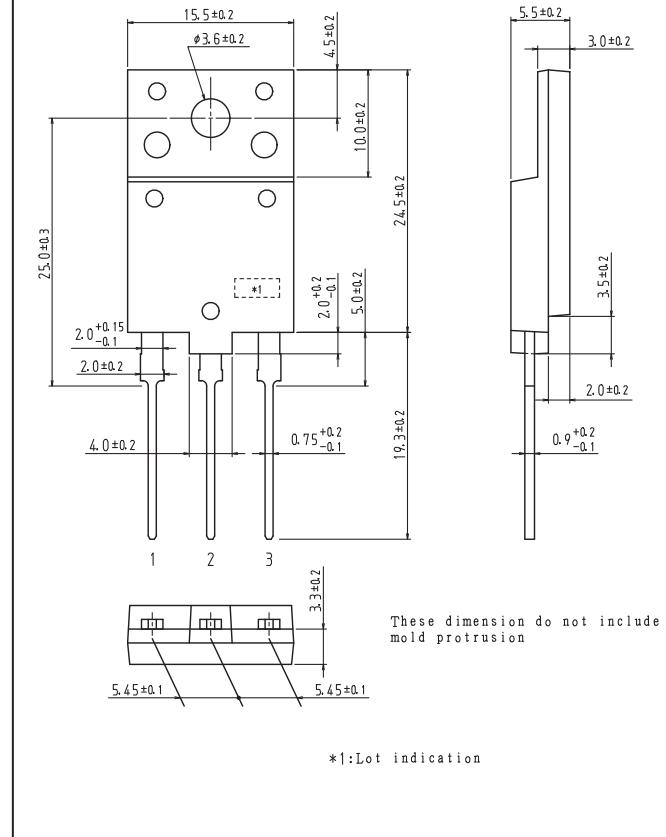
The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A

Outline Drawing

WPH4003-1E

Mass (g)	Unit
5.5 * For reference	mm



Note on usage : Since the WPH4003 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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