

C3D10065I

3rd Generation 650 V, 10 A Silicon Carbide Schottky

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Package Type: Isolated TO-220-2
Marking: C3D10065I

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Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Electrically Isolated Package (2.5kV)

Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters
- AC/DC Converters

Maximum Ratings ($T_c = 25^\circ\text{C}$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes
Repetitive Peak Reverse Voltage	V_{RRM}	650	V		
DC Blocking Voltage	V_{DC}	650			
Continuous Forward Current	I_F	19	A	$T_c = 25^\circ\text{C}$	Fig. 3
		10		$T_c = 125^\circ\text{C}$	
		9		$T_c = 135^\circ\text{C}$	
Repetitive Peak Forward Surge Current	I_{FRM}	39		$T_c = 25^\circ\text{C}, t_p = 10 \text{ ms}$, Half Sine Wave	
		26.5		$T_c = 110^\circ\text{C}, t_p = 10 \text{ ms}$, Half Sine Wave	
Non-Repetitive Forward Surge Current	I_{FSM}	90		$T_c = 25^\circ\text{C}, t_p = 10 \text{ ms}$, Half Sine Wave	Fig. 8
		70		$T_c = 110^\circ\text{C}, t_p = 10 \text{ ms}$, Half Sine Wave	
Non-Repetitive Peak Forward Surge Current	$I_{F,Max}$	860		$T_c = 25^\circ\text{C}, t_p = 10 \mu\text{s}$, Pulse	
		680		$T_c = 110^\circ\text{C}, t_p = 10 \mu\text{s}$, Pulse	
Power Dissipation	P_{tot}	60	W	$T_c = 25^\circ\text{C}$	Fig. 4
		26		$T_c = 110^\circ\text{C}$	
Diode dV/dt ruggedness	dV/dt	200	V/ns	$V_R = 0-650\text{V}$	
i^2t value	$\int i^2 dt$	40.5	A^2s	$T_c = 25^\circ\text{C}, t_p = 10 \text{ ms}$	
		24.5		$T_c = 110^\circ\text{C}, t_p = 10 \text{ ms}$	

Electrical Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Notes
Forward Voltage	V _F	1.5	1.8	V	I _F = 10 A, T _j = 25 °C	Fig. 1
		2.0	2.4		I _F = 10 A, T _j = 175 °C	
Reverse Current	I _R	12	60	μA	V _R = 650 V, T _j = 25 °C	Fig. 2
		24	220		V _R = 650 V, T _j = 175 °C	
Total Capacitive Charge	Q _C	24		nC	V _R = 400 V, T _j = 25 °C, I _F = 8 A	Fig. 5
Total Capacitance	C	460.5		pF	V _R = 0 V, T _j = 25 °C, f = 1 MHz	Fig. 6
		44			V _R = 200 V, T _j = 25 °C, f = 1 MHz	
		40			V _R = 400 V, T _j = 25 °C, f = 1 MHz	
Capacitance Stored Energy	E _C	3.6		μJ	V _R = 400 V	Fig. 7

Notes:
SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{θ, JC (TYP)}	2.5	°C / W	
Junction Temperature	T _j	-55 to +175	°C	
Case & Storage Temperature	T _c	-55 to +175		
TO-220 Mounting Torque	-	1	Nm	M3 Screw
		8.8	lbf-in	6-32 Screw

Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Notes
Human Body Model	HBM	Class 3B (≥ 8000 V)
Charge Device Model	CDM	Class C3 (≥ 1000 V)

Typical Performance

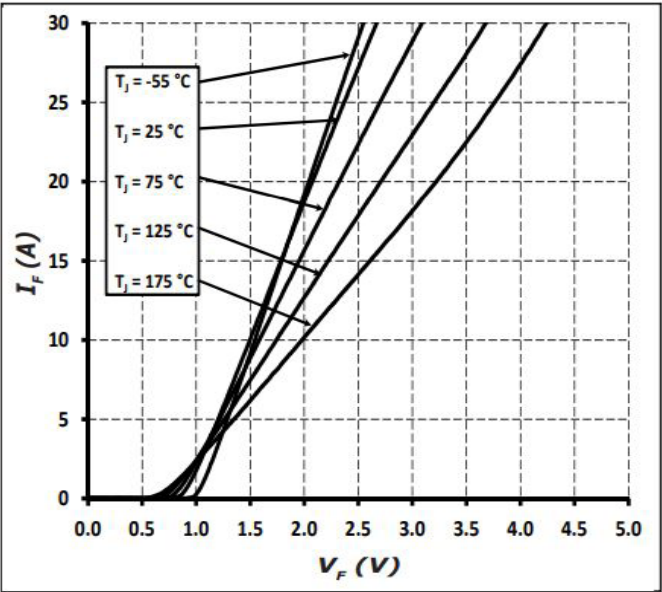


Figure 1
Forward Characteristics

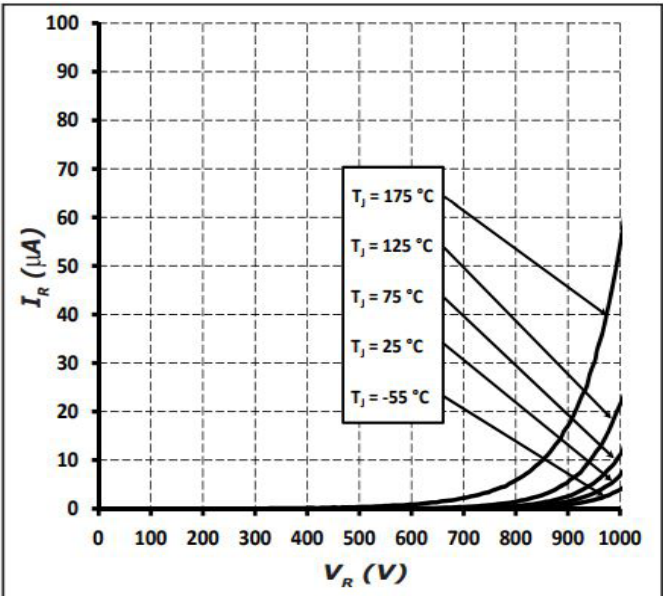


Figure 2
Reverse Characteristics

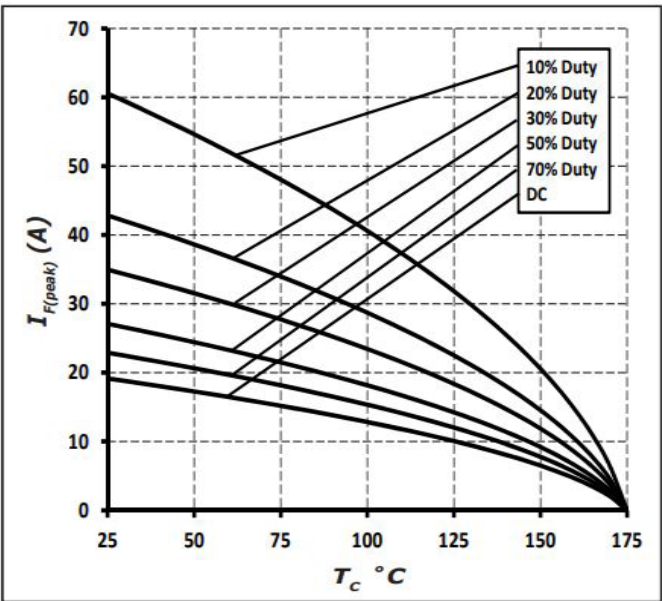


Figure 3
Current Derating

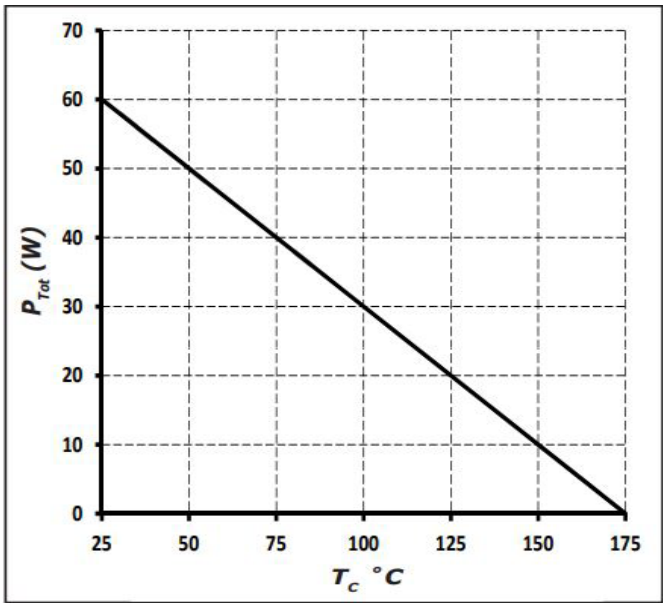


Figure 4
Power Derating

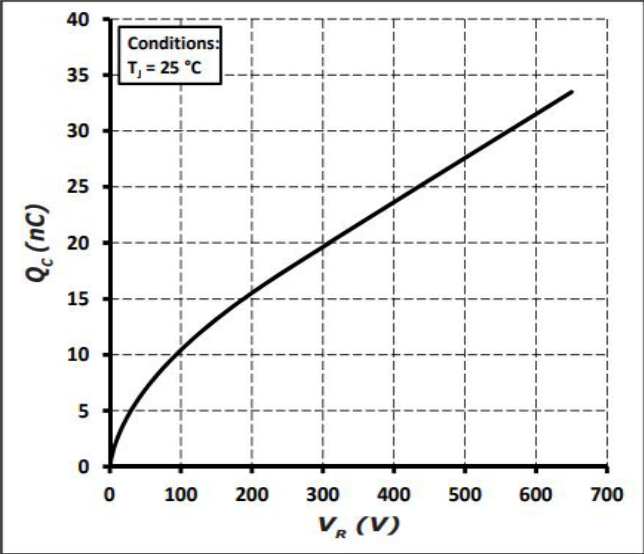


Figure 5
Total Capacitance vs. Reverse Voltage

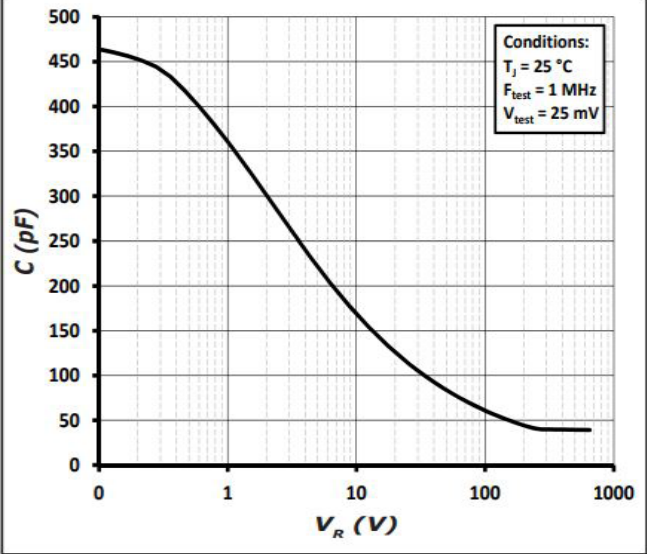


Figure 6
Capacitance vs. Reverse Voltage

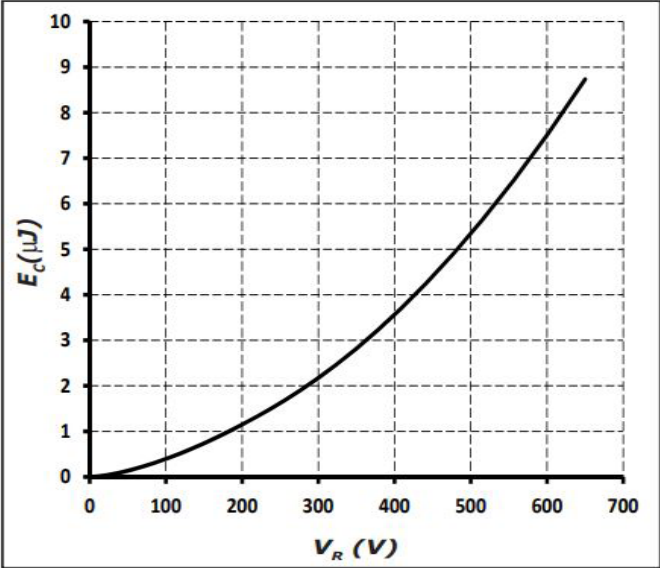


Figure 7
Capacitance Stored Energy

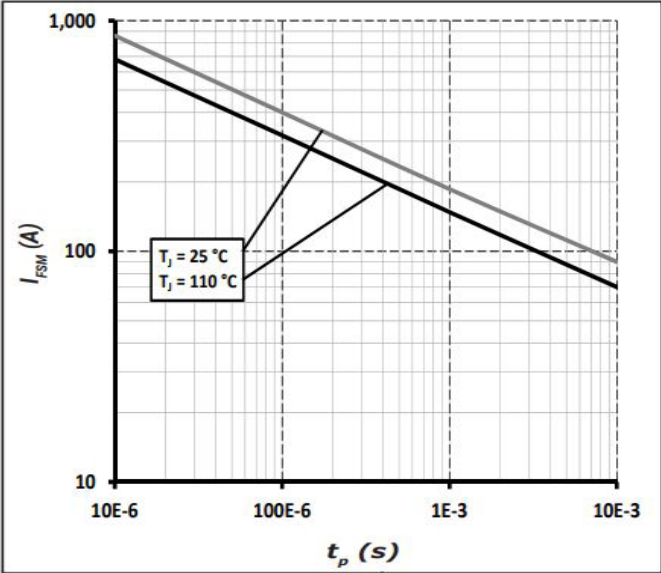


Figure 8
Non-Repetitive Peak Forward Surge Current
versus Pulse Duration (sinusoidal waveform)

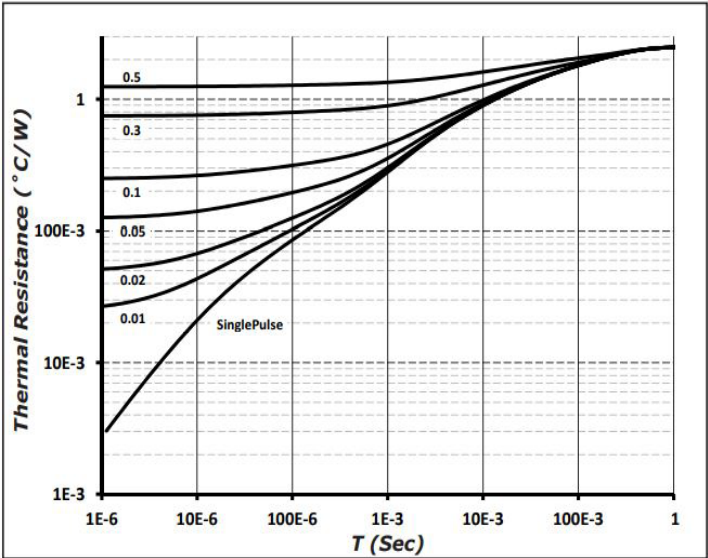
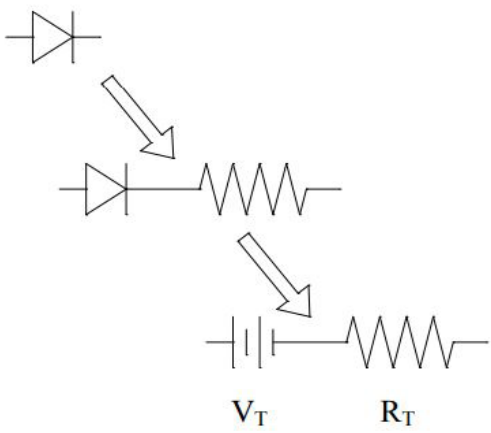


Figure 9
Transient Thermal Impedance

Diode Model



$$V_{fT} = V_T + I_f * R_T$$

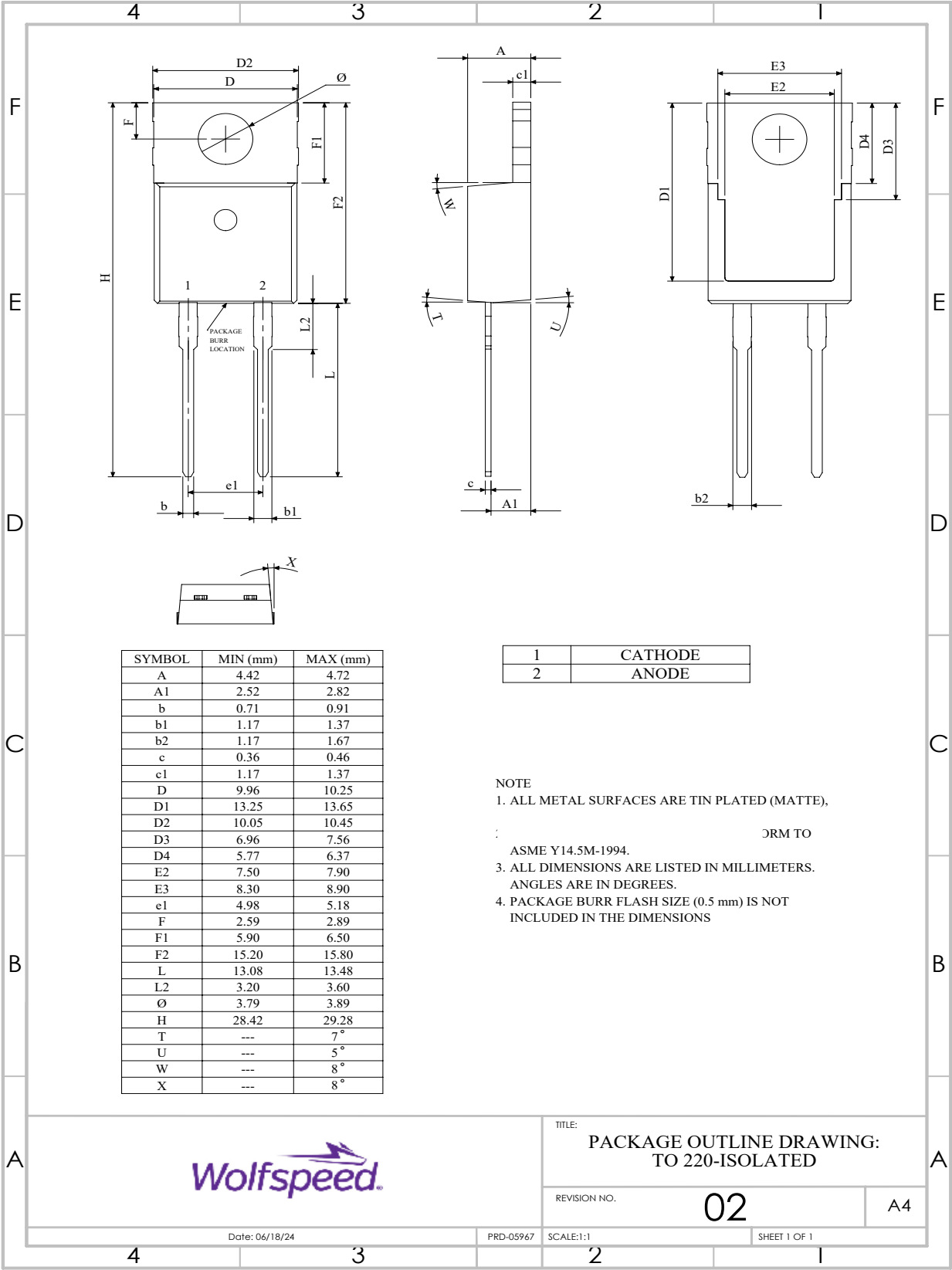
$$V_T = 0.96 + (T_j * -1.1 * 10^{-3})$$

$$R_T = 0.07 + (T_j * 7.4 * 10^{-4})$$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Package Dimensions & Pin-Out

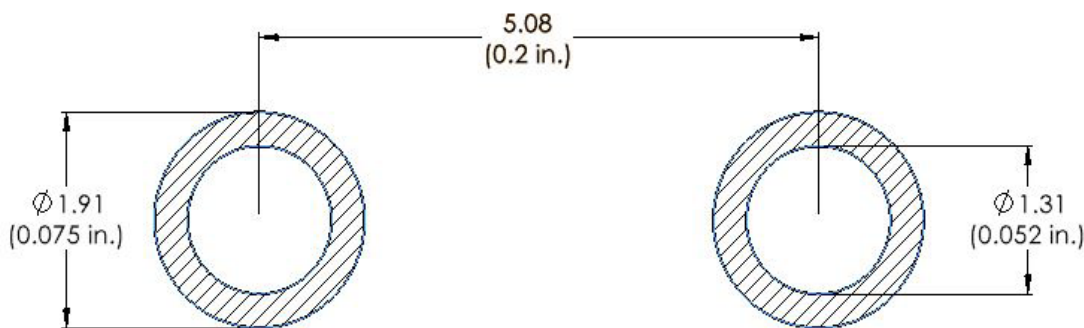
Package: TO-220-2





Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
C3D10065I	Tube



Revision History

Document Version	Date of Release	Description of Changes
E	January-2018	Initial Release
7	May-2023	Update Package Drawing Update Landing Pad
8	October - 2024	Legal Disclaimer, POD

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