

PDTB123YT

50 V, 500 mA PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

1 January 2023

Product data sheet

1. General description

PNP Resistor-Equipped Transistor (RET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTD123YT

2. Features and benefits

- 500 mA output current capability
- · Reduces pick and place costs
- · Built-in bias resistors
- ±10 % resistor ratio tolerance
- · Simplifies circuit design
- Reduces component count

3. Applications

- · Digital application in automotive and industrial segments
- Cost-saving alternative for BC807 series in digital applications
- · Control of IC inputs
- · Switching loads

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-50	V
Io	output current		-	-	-500	mA
R1	bias resistor 1 (input)	T _{amb} = 25 °C	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		4.1	4.55	5	

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	3	
2	GND	ground (emitter)		R1
3	0	output (collector)	SOT23	GND



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6. Ordering information

Table 3. Ordering information

Type number Package				
	Name	Description	Version	
PDTB123YT		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23	

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PDTB123YT	%7Y

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-50	V
V_{CEO}	collector-emitter voltage	open base		-	-50	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
VI	input voltage	positive		-	5	V
		negative		-	-20	V
Io	output current			-	-500	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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50 V, 500 mA PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -40 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
	current	V _{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = -50 V; I _B = 0 A; T _{amb} = 25 °C	-	-	-0.5	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$	-	-	-0.65	mA
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -50 mA; T _{amb} = 25 °C	70	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -50 \text{ mA}; I_B = -2.5 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	-	-300	mV
V _{I(off)}	off-state input voltage	V _{CE} = -5 V; I _C = -100 μA; T _{amb} = 25 °C	-0.4	-0.6	-1	V
V _{I(on)}	on-state input voltage	V_{CE} = -0.3 V; I_{C} = -20 mA; T_{amb} = 25 °C	-0.5	-1	-1.4	V
R1	bias resistor 1 (input)	T _{amb} = 25 °C	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		4.1	4.55	5	
C _c	collector capacitance	V_{CB} = -10 V; I_{E} = 0 A; i_{e} = 0 A; f = 100 MHz; T_{amb} = 25 °C	-	11	-	pF

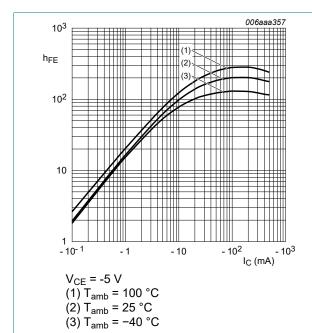


Fig. 1. DC current gain as a function of collector current; typical values

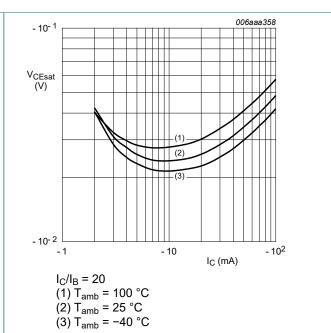


Fig. 2. Collector-emitter saturation voltage as a function of collector current; typical values

50 V, 500 mA PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

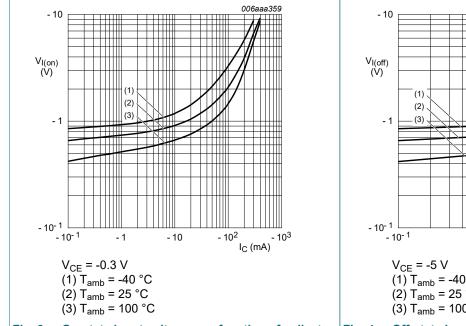
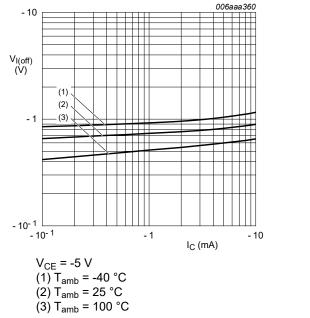


Fig. 3. On-state input voltage as a function of collector current; typical values



g. 4. Off-state input voltage as a function of collector current; typical values

50 V, 500 mA PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

11. Test information

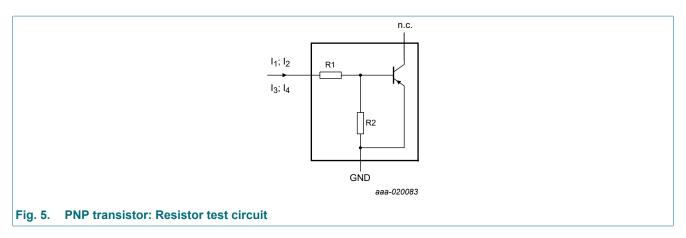
Resistor calculation

· Calculation of bias resistor 1 (R1)

$$R_{I} = \frac{V(I_{2}) - V(I_{1})}{I_{2} - I_{1}}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I4) - V(I3)}{R1 \cdot (I4 - I3)} - 1$$

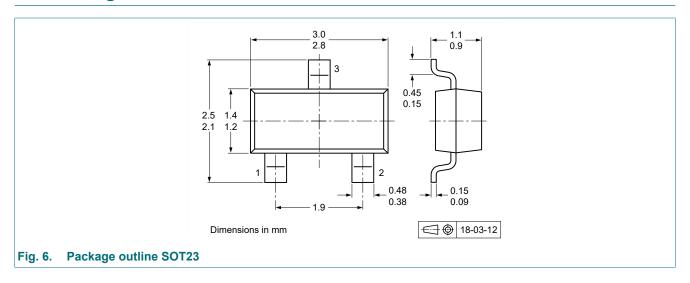


Resistor test conditions

Table 8. Resistor test conditions

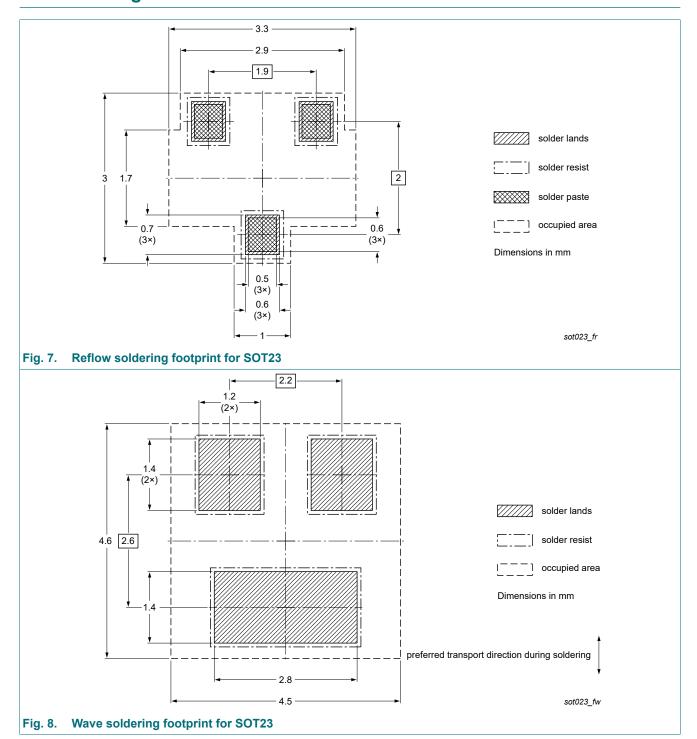
Type number	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I ₁	l ₂	l ₃	14
PDTB123YT	2.2	10	-0.7 mA	-0.8 mA	0.45 mA	0.55 mA

12. Package outline



50 V, 500 mA PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

13. Soldering



50 V, 500 mA PNP resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

14. Revision history

Table 9. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PDTB123YT v.5	20230101	Product data sheet	-	PDTB123YT v.4	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Product changed to non automotive. Please refer to the automotive product(s) with -Q. 				
PDTB123YT v.4	20201116	Product data sheet	-	PDTB123YT v.3	
PDTB123YT v.3	20100923	Product data sheet	-	PDTB123YT_SER v.2	
PDTB123YT_SER v.2	20091116	Product data sheet	-	PDTB123YT_SER v.1	
PDTB123YT_SER v.1	20050427	Product data sheet	-	-	

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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