





April 2013



- Pletronics' LV55D Series is a quartz crystal controlled precision square wave generator with an LVDS output.
- The package is designed for high density surface mount designs.
- Tape and Reel or cut tape packaging is available.
- 3.2 x 5 mm LCC Ceramic Package
- Enable/Disable Function on pad 1
- Disable function includes low standby power mode
- 3.3 V Supply
- Low Jitter

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.09 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020D.1

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{cc} Supply Voltage	-0.5V to +5.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V

Thermal Characteristics

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 45 to 65°C/Watt depending on the solder pads, ground plane and construction of the PCB.

ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

Product information is current as of publication date. The product conforms to specifications per the terms of the Pletronics standard warranty. Production processing does not necessarily include testing of all parameters.



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Part Number:

P81.0LG
• YMDxx

Marking Legend:

P = Pletronics LG = LVDS

81.0M = Frequency in MHz

YMD = Date of Manufacture (year and week, or year-month-day)

All other marking is internal factory codes

Codes for Date Code YMD

Code	0	1	2	3	4	Cod	e A	В	С	D	E	F	G	Н	J	K	L	M
Year	2010	2011	2012	2013	201	4 Mon	h JAN	I FEB	MAF	R APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
(Code		1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G
	Day		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
(Code		Н	J	K	L	M	N	Р	R	T	U	٧	W	X	Υ	Z	
	Day		17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm)

Font is Courier New

Bar code is 39-Full ASCII

(The tape and reel will be marked LV55D015-81.0M)

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

P/N: LV5545DV-100.0M

Customer P/N: 12345678

Qty: D/C 6MC

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=260C for 10s 2X Max

Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A



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Electrical Specification for 3.30V ±10% over the specified temperature range

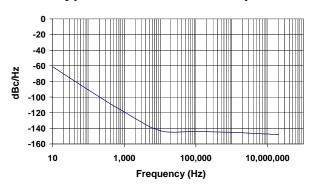
Item	Min	Тур	Max	Unit	Condition	
Frequency Accuracy	-50	1	+50	ppm	For all supply voltages, load changes, aging for 1 year, shock, vibration and temperatures	
Output Waveform		L١	/DS			
Output High Level		1.43	1.60	Volts	See load circuit R1 = 50 ohms	
Output Low Level	0.90	1.10		Volts		
Differential Output (V _{OD})	250	350	450	mVolts		
Output Offset Voltage (Vos)	-	1.25	-	Volts		
Output Symmetry	45	50	55	%	Referenced to 50% of amplitude or crossing point	
Output T _{RISE} and T _{FALL}	-	150	400	pS	Vth is 20% and 80% of waveform	
Jitter	-	-	0.6	pS	Measured from 12KHz to 20MHz from Fnominal	
	-	-	2.8	RMS	Measured from 10Hz to 1MHz from Fnominal	
Vcc Supply Current	-	16	27	mA	Includes current of properly terminated device	
Enable/Disable Internal Pull-up	50	-	-	Kohm	To Vcc (equivalent resistance)	
V disable	-	-	0.7	Volts	Referenced to Ground	
V enable	1.7	-	-	Volts	Referenced to Ground	
Output leakage V _{OUT} = V _{CC}	-10	-	+10	uA	Pad 1 low, device disabled	
V _{OUT} = 0V	-10	-	+10	uA		
Enable	-	-	3	mS	Time for output to reach a logic state	
Disable time	-	-	200	nS	Time for output to reach a high Z state	
Start up time	-	-	3	mS	Measured from the time Vcc = 3.0V	
Standby Current I _{CC}	-	-	10	uA	Pad 1 low, device disabled	
Operating Temperature Range	-40	-	+85	°C		
Storage Temperature Range	-55	-	125	°C		

Specifications with Pad 1 E/D open circuit

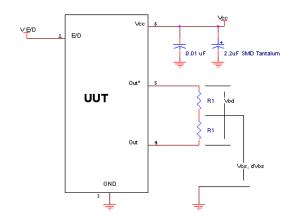


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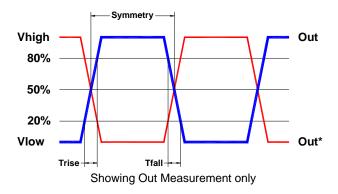
Typical Phase-Noise Response



Load Circuit



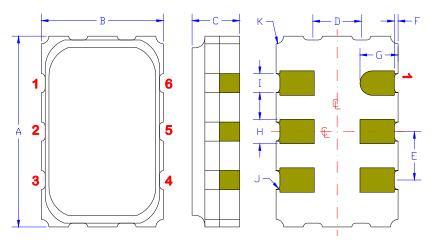
Test Waveform





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Mechanical:



Inches mm 0.197 ±0.006 5.00 ±0.15 В 0.125 ±0.006 3.20 ±0.15 С 0.053 max 1.35 max D^1 0.050 1.27 E^1 1.27 0.050 F^1 0.004 0.10 G^1 0.039 1.00 H^1 0.025 0.63 I¹ 0.020 0.50 J^1 0.004R 0.10R K^1 0.008R 0.20R

Contacts:

Gold 11.8 to 39.4 µinches (0.3 to 1.0 μ m) over

Nickel 50 to 350 μ inches (1.27 to 8.89 μ m)

¹ Typical dimensions

Not to Scale

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.30 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm CC}$ if the oscillator is to be always on.
2	No connect	There is no internal connection to this pad
3	Ground (GND)	
4	Output	The outputs must be terminated, 100 ohms between the outputs is the ideal
5	Output*	termination.
6	Supply Voltage (V _{CC})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

Layout and application information



Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable on both input pads

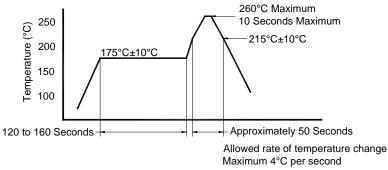
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



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Reflow Cycle (typical for lead free processing)



The part may be reflowed 3 times without degradation.

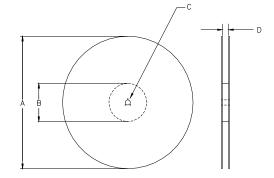
Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

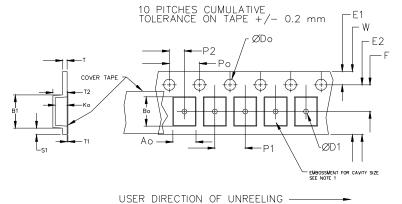
Constant Dimensions Table 1									
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max	
8mm		1.0			2.0				
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05				
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1	
24mm		1.5			<u>+</u> 0.1				

Variable Dimensions Table 2									
Tape B1 Size E2 Min Max F P1 T2 Max W Max Ao, Bo & Ko									
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1		

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm Not to scale





		REE	REEL DIMENSIONS								
Α	inches	7.0	10.0	13.0							
	mm	177.8	254.0	330.2							
В	inches	2.50	4.00	3.75							
	mm	63.5	101.6	95.3	Tape Width						
С	mm	13	widin								
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0						

Reel dimensions may vary from the above



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IMPORTANT NOTICE

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