

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVI)

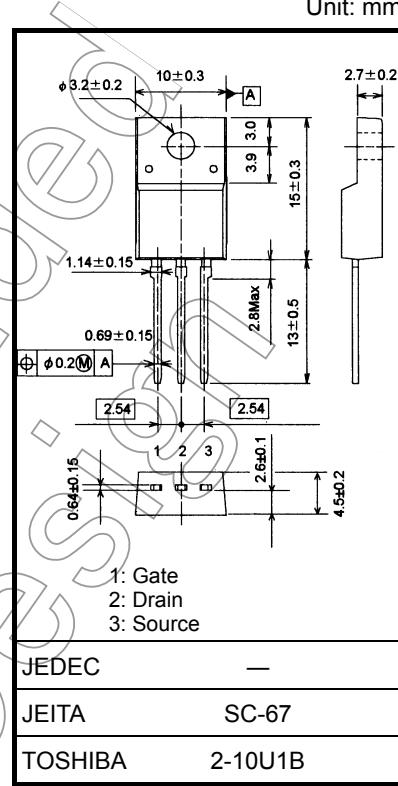
2SK3568

Switching Regulator Applications

- Low drain-source ON-resistance: $R_{DS(ON)} = 0.4 \Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 8.5 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 100 \mu\text{A}$ ($V_{DS} = 500 \text{ V}$)
- Enhancement mode: $V_{th} = 2.0$ to 4.0 V ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	500	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)	V_{DGR}	500	V
Gate-source voltage	V_{GSS}	± 30	V
Drain current	DC (Note 1) I_D	12	A
	Pulse ($t = 1 \text{ ms}$) (Note 1) I_{DP}	48	
Drain power dissipation ($T_c = 25^\circ\text{C}$)	P_D	40	W
Single pulse avalanche energy (Note 2)	E_{AS}	364	mJ
Avalanche current	I_{AR}	12	A
Repetitive avalanche energy (Note 3)	E_{AR}	4	mJ
Channel temperature	T_{ch}	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55 to 150	$^\circ\text{C}$



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

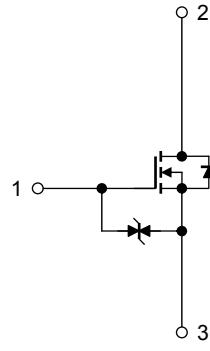
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	$R_{th}(\text{ch-c})$	3.125	$^\circ\text{C/W}$
Thermal resistance, channel to ambient	$R_{th}(\text{ch-a})$	62.5	$^\circ\text{C/W}$

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 4.3 \text{ mH}$, $I_{AR} = 12 \text{ A}$, $R_G = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



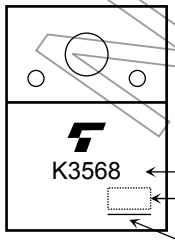
Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	—	—	±10	μA
Gate-source breakdown voltage	V _{(BR) GSS}	I _G = ±10 μA, V _{DS} = 0 V	±30	—	—	V
Drain cut-off current	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	—	—	100	μA
Drain-source breakdown voltage	V _{(BR) DSS}	I _D = 10 mA, V _{GS} = 0 V	500	—	—	V
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	—	4.0	V
Drain-source ON-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 6 A	—	0.4	0.52	Ω
Forward transfer admittance	Y _{fs}	V _{DS} = 10 V, I _D = 6 A	3.5	8.5	—	S
Input capacitance	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	—	1500	—	pF
Reverse transfer capacitance	C _{rss}		—	15	—	
Output capacitance	C _{oss}		—	180	—	
Switching time	Rise time	t _r		—	22	ns
	Turn-on time	t _{on}		—	50	
	Fall time	t _f		—	36	
	Turn-off time	t _{off}		—	170	
Total gate charge	Q _g	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 12 A	—	42	—	nC
Gate-source charge	Q _{gs}		—	23	—	
Gate-drain charge	Q _{gd}		—	19	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	—	—	12	A
Pulse drain reverse current (Note 1)	I _{DRP}	—	—	—	48	A
Forward voltage (diode)	V _{DSF}	I _{DR} = 12 A, V _{GS} = 0 V	—	—	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 12 A, V _{GS} = 0 V, dI _{DR} /dt = 100 A/μs	—	1200	—	ns
Reverse recovery charge	Q _{rr}		—	16	—	μC

Marking



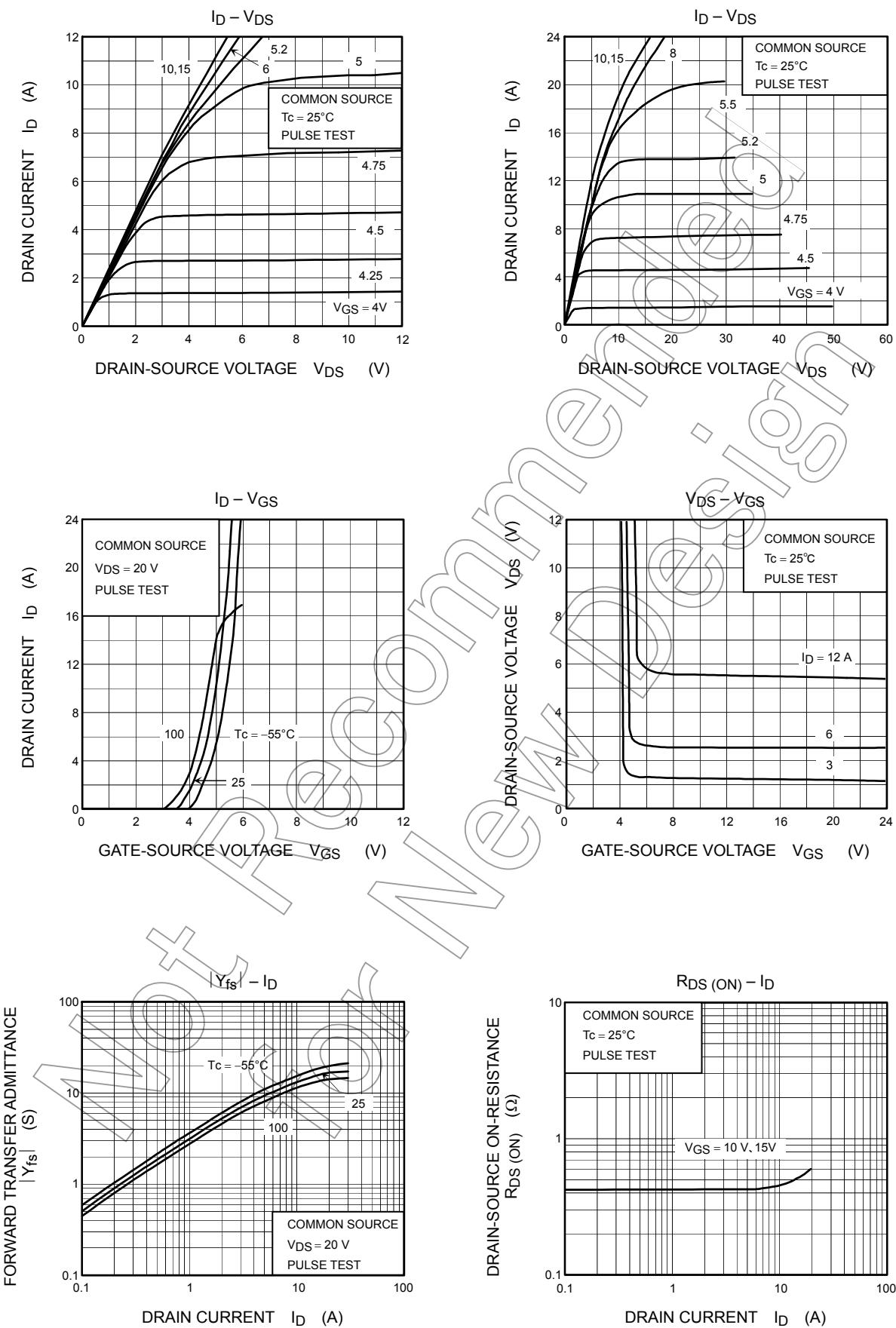
Part No. (or abbreviation code)
Lot No.
Note 4

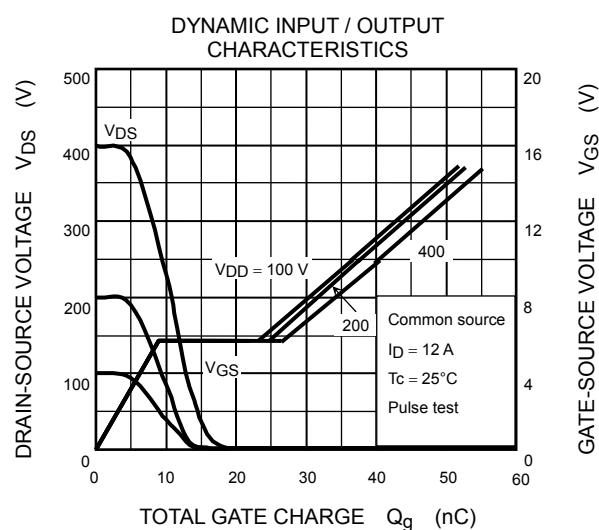
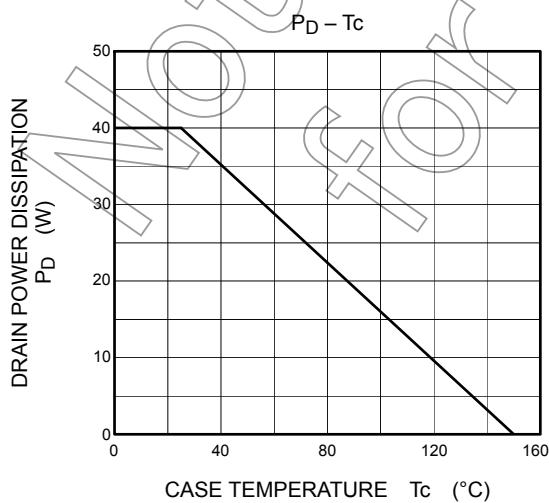
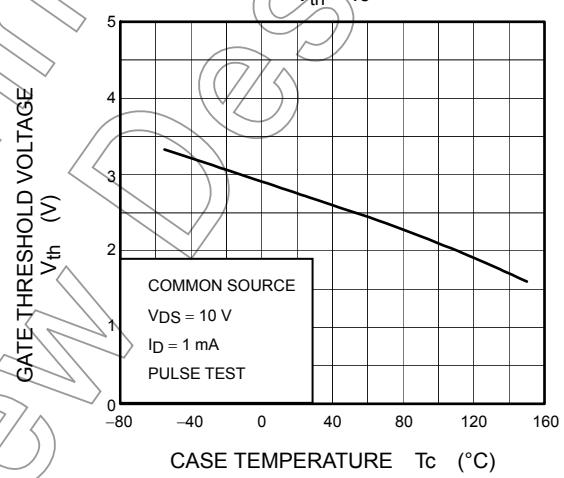
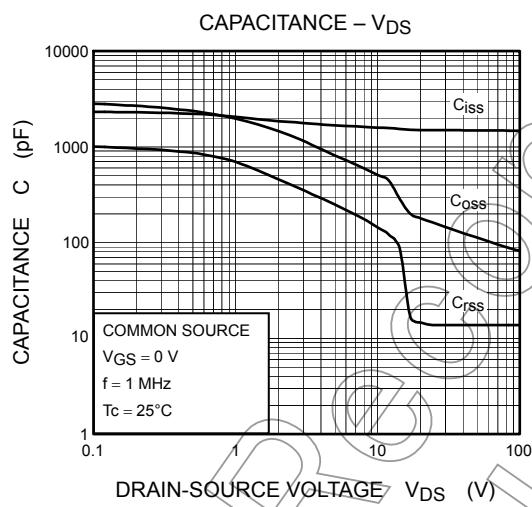
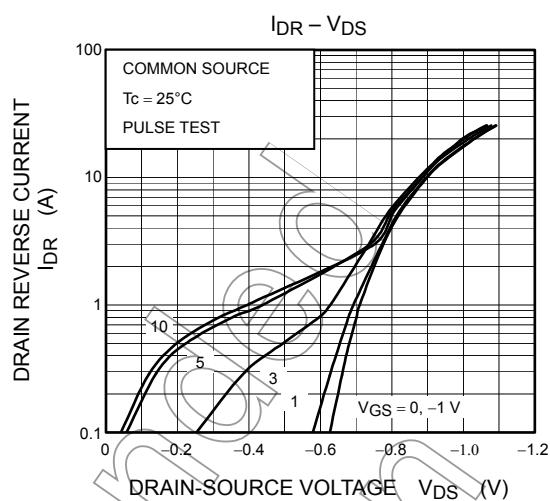
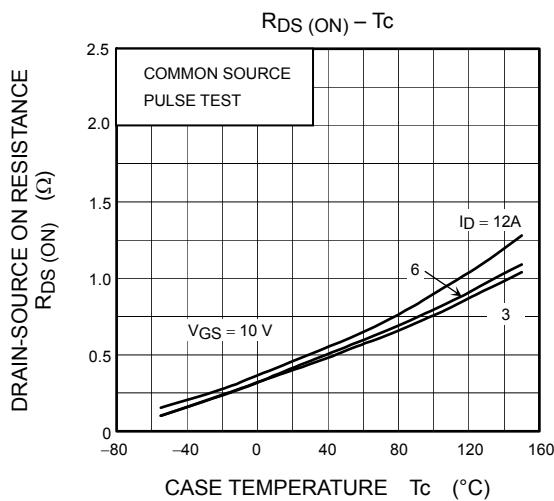
Note 4: A line under a Lot No. identifies the indication of product labels.

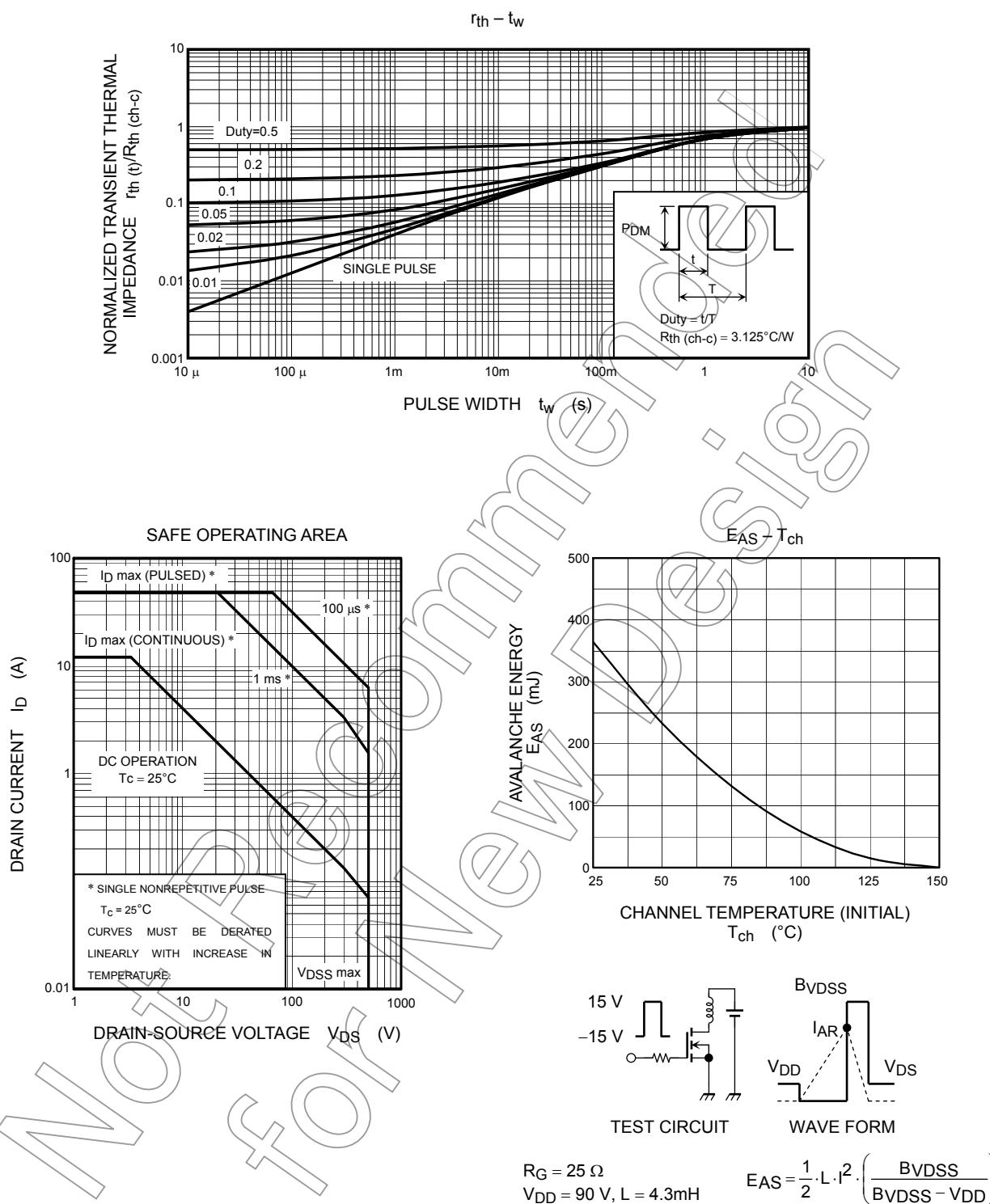
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