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FDMS0300S

October 2014

N-Channel PowerTrench[®] SyncFETTM 30 V, 49 A, 1.8 m Ω

Features

- Max $r_{DS(on)} = 1.8 \text{ m}\Omega$ at $V_{GS} = 10 \text{ V}$, $I_D = 30 \text{ A}$
- Max $r_{DS(on)}$ = 2.0 m Ω at V_{GS} = 4.5 V, I_D = 25 A
- Advanced Package and Silicon combination for low r_{DS(on)} and high efficiency
- SyncFET Schottky Body Diode
- MSL1 robust package design
- 100% UIL tested
- RoHS Compliant



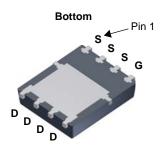
General Description

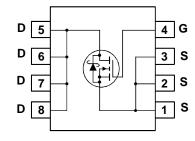
The FDMS0300S has been designed to minimize losses in power conversion application. Advancements in both silicon and package technologies have been combined to offer the lowest $r_{\text{DS}(\text{on})}$ while maintaining excellent switching performance. This device has the added benefit of an efficient monolithic Schottky body diode.

Applications

- Synchronous Rectifier for DC/DC Converters
- Notebook Vcore/ GPU low side switch
- Networking Point of Load low side switch







MOSFET Maximum Ratings TA = 25 °C unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DS}	Drain to Source Voltage			30	V	
V _{GS}	Gate to Source Voltage		(Note 4)	±20	V	
	Drain Current -Continuous (Package limited)	T _C = 25 °C		49		
	-Continuous (Silicon limited)	T _C = 25 °C		194		
^I D	-Continuous	T _A = 25 °C	(Note 1a)	31	Α	
	-Pulsed			180		
E _{AS}	Single Pulse Avalanche Energy		(Note 3)	242	mJ	
P _D	Power Dissipation	T _C = 25 °C		96	W	
	Power Dissipation	T _A = 25 °C	(Note 1a)	2.5	VV	
T _J , T _{STG}	Operating and Storage Junction Temperature R	ange		-55 to +150	°C	

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1	a) 50	*C/VV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDMS0300S	FDMS0300S	Power 56	13 "	12 mm	3000 units

Electrical Characteristics $T_J = 25$ °C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	cteristics					
BV_{DSS}	Drain to Source Breakdown Voltage	I _D = 1 mA, V _{GS} = 0 V	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25 °C		19		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			500	μΑ
I_{GSS}	Gate to Source Leakage Current, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA

On Characteristics

V _{GS(th)}	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1 \text{ mA}$	1.2	1.6	3.0	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25 °C		-5		mV/°C
r _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		1.3	1.8	
		$V_{GS} = 4.5 \text{ V}, I_D = 25 \text{ A}$		1.6	2.0	mΩ
		$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}, T_J = 125 ^{\circ}\text{C}$		1.8	2.5	
g _{FS}	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_{D} = 30 \text{ A}$		161		S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 45 V V 0 V	6545	8705	pF
C _{oss}	Output Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$ $f = 1 \text{ MHz}$	2465	3280	pF
C _{rss}	Reverse Transfer Capacitance	1 = 1 IVII 12	210	315	pF
R_g	Gate Resistance		0.5	1.1	Ω

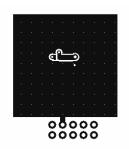
Switching Characteristics

t _{d(on)}	Turn-On Delay Time			22	35	ns
t _r	Rise Time	V _{DD} = 15 V, I _D = 30 A,		12	21	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$		50	80	ns
t _f	Fall Time			7	13	ns
Q_g	Total Gate Charge	V _{GS} = 0 V to 10 V		95	133	nC
Qg	Total Gate Charge	$V_{GS} = 0 \text{ V to } 4.5 \text{ V}$ $V_{DD} = 15 \text{ V}$		43	60	nC
Q _{gs}	Gate to Source Charge	I _D = 30 A	1	18.2		nC
Q _{gd}	Gate to Drain "Miller" Charge			9.1		nC

Drain-Source Diode Characteristics

V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = 2 \text{ A}$ (Note 2)	0.37	0.7	V
		$V_{GS} = 0 \text{ V}, I_S = 30 \text{ A}$ (Note 2)	0.74	1.2	V
t _{rr}	Reverse Recovery Time	I _F = 30 A, di/dt = 300 A/μs	50	81	ns
Q _{rr}	Reverse Recovery Charge	iF = 30 A, αι/αι = 300 A/μs	84	136	nC

^{1.} $R_{\theta JA}$ is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.



a. 50 °C/W when mounted on a $1 \text{ in}^2 \text{ pad of } 2 \text{ oz copper.}$



b. 125 °C/W when mounted on a minimum pad of 2 oz copper.

- 2. Pulse Test: Pulse Width < 300 μ s, Duty cycle < 2.0%.
- 3. E_{AS} of 242 mJ is based on starting T_J = 25 °C, L = 1 mH, I_{AS} = 22 A, V_{DD} = 27 V, V_{GS} = 10 V. 100% test at L = 0.3 mH, I_{AS} = 34 A.
- 4. As an N-ch device, the negative Vgs rating is for low duty cycle pulse occurrence only. No continuous rating is implied.

Typical Characteristics $T_J = 25$ °C unless otherwise noted

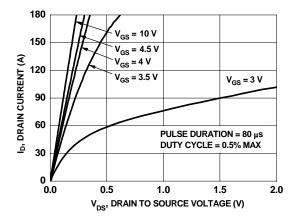


Figure 1. On Region Characteristics

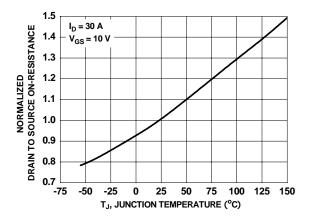


Figure 3. Normalized On Resistance vs Junction Temperature

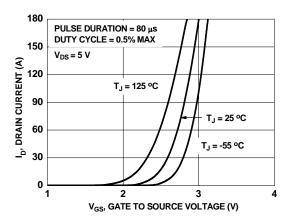


Figure 5. Transfer Characteristics

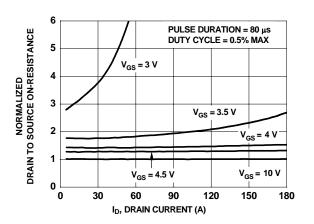


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

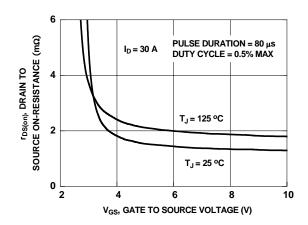


Figure 4. On-Resistance vs Gate to Source Voltage

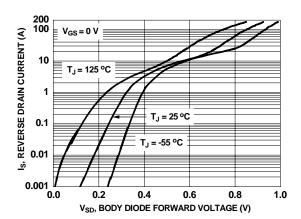


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

Typical Characteristics $T_J = 25$ °C unless otherwise noted

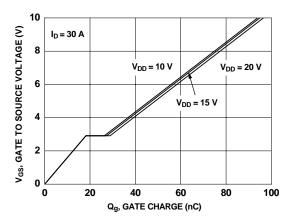


Figure 7. Gate Charge Characteristics

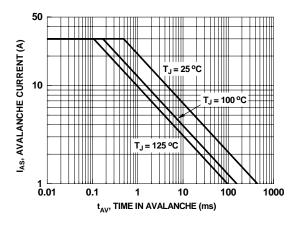


Figure 9. Unclamped Inductive Switching Capability

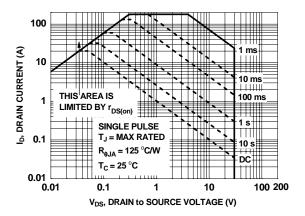


Figure 11. Forward Bias Safe Operating Area

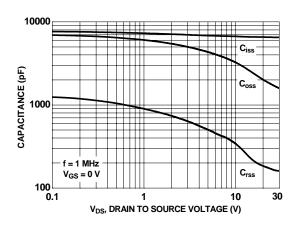


Figure 8. Capacitance vs Drain to Source Voltage

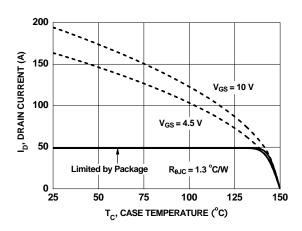


Figure 10. Maximum Continuous Drain Current vs Case Temperature

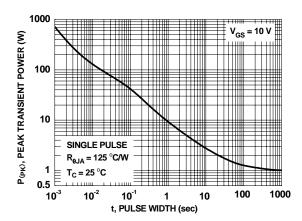


Figure 12. Single Pulse Maximum Power Dissipation

Typical Characteristics T_J = 25 °C unless otherwise noted

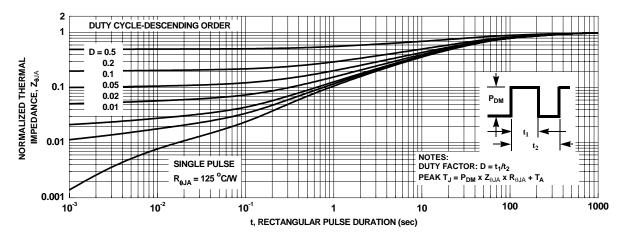


Figure 13. Junction-to-Ambient Transient Thermal Response Curve

Typical Characteristics (continued)

SyncFET Schottky body diode Characteristics

Fairchild's SyncFET process embeds a Schottky diode in parallel with PowerTrench MoSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 14 shows the reverses recovery characteristic of the FDMS0300S.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

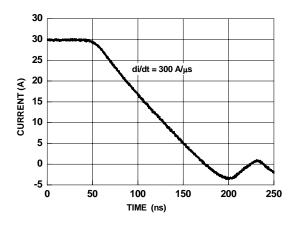


Figure 14. FDMS0300S SyncFET body diode reverse recovery characteristic

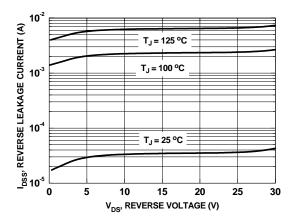
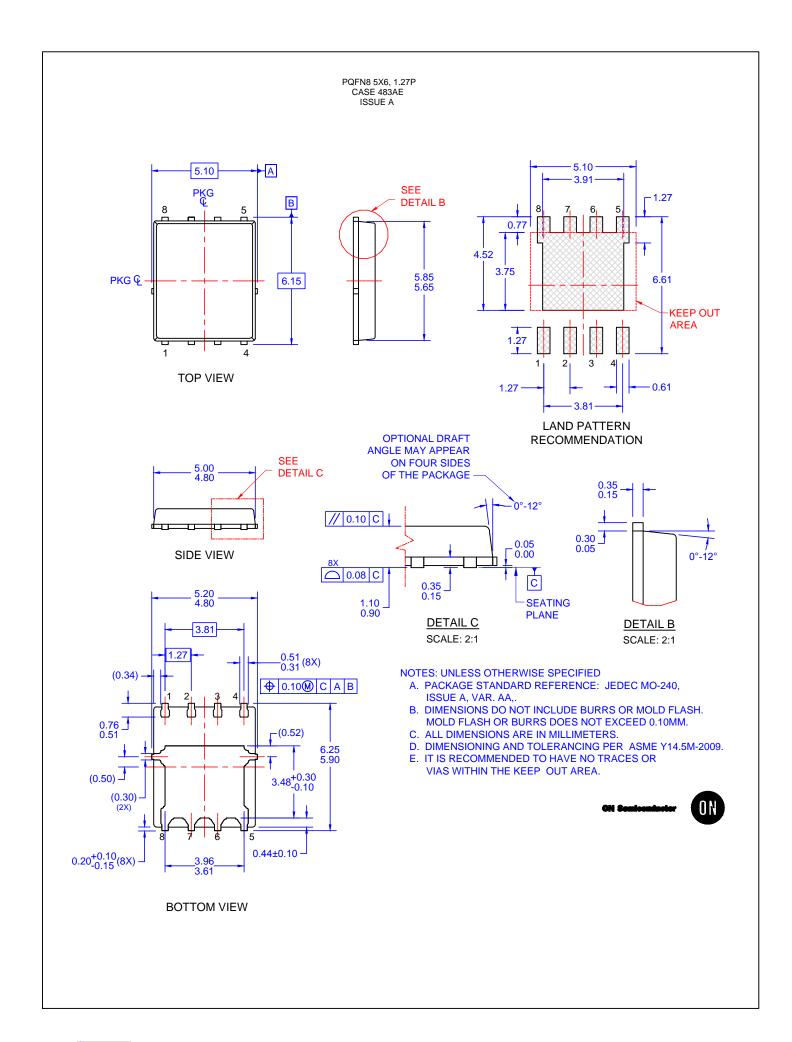


Figure 15. SyncFET body diode reverses leakage versus drain-source voltage



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