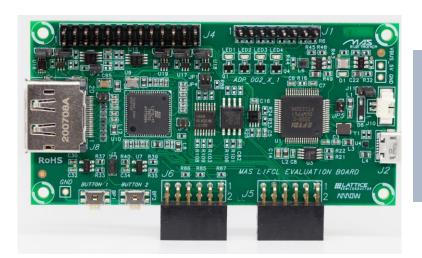
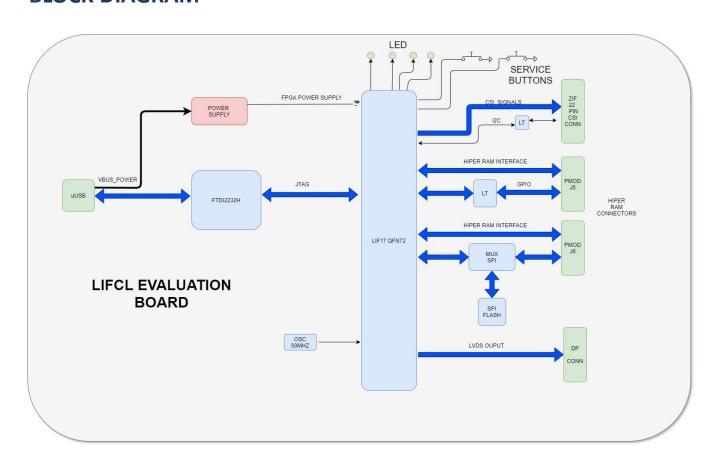
MAS LIFCL EVALUATION BOARD





- Lattice Crosslink NX LIFCL 17 QFN72
- On Board programming
- LVDS and MIPI connectors
- PMOD connectors

BLOCK DIAGRAM











FEATURES OF THE EVALUATION BOARD

FPGA LATTICE	Crosslink NX LIFCL-17 QFN-72	
Video	1x HARD CSI/DSI support 1 to 4x up to 4K 60fps	
	1x Soft MIPI CSI/DSI 1 TO 4X UP TO 4K 30fps (Through Strip connector) or LVDS/FDPLink 4x up to 4K 30fps Through Display Port connector .	
USB	Micro USB for board programming (without programming cable) .	
JTAG	JTAG for for programming	
LED	4 x led for general purpose use	
Button	2 x Push Buttons for general purpose use	
PMOD Connectors	2 x PMOD 3,3V connector or in multi-mode for standard accessories (VGA, HyperRam, HDMI).	
Flash SPI	ON Board 128Mbit SPI FLASH for bit stream or general purpose use	
POWER	5V single supply using the Micro USB connector	
Dimentions	40mm x 80mm	

ORDERING

ORDERING CODE	DESCRIPTION	Comments
ADP_002_0_1	Full feature board	Standard product
ADP_002_1_1	Board without programming IC	Available uppon request





