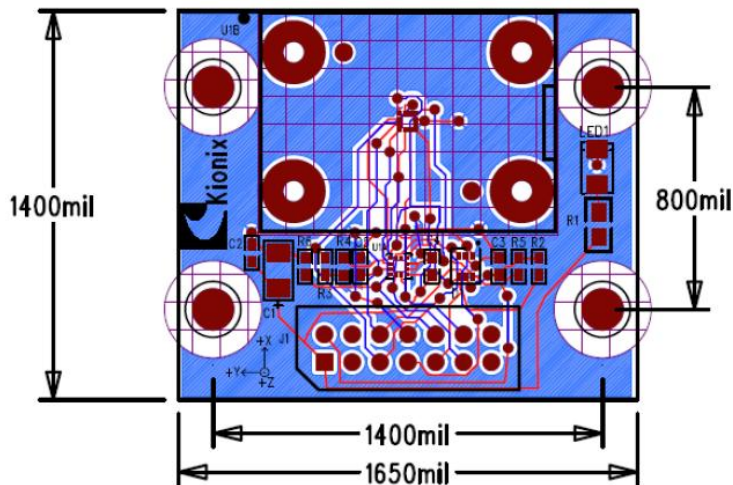


Tri-Axis, User Selectable $\pm 2g$, $4g$, $8g$, Digital (I²C/SPI) Output, Integrated FIFO/FILO buffer Evaluation Board

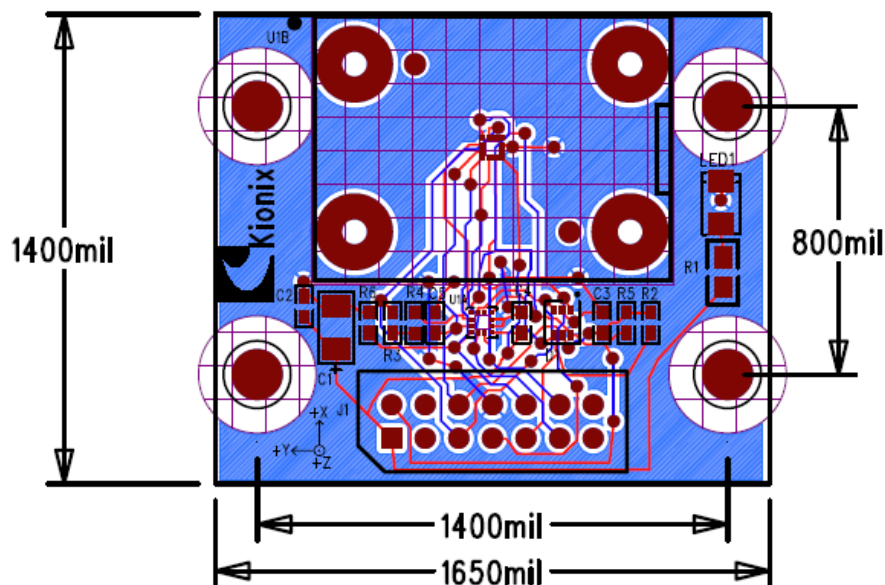
Part Number: EVAL-KX022-1020



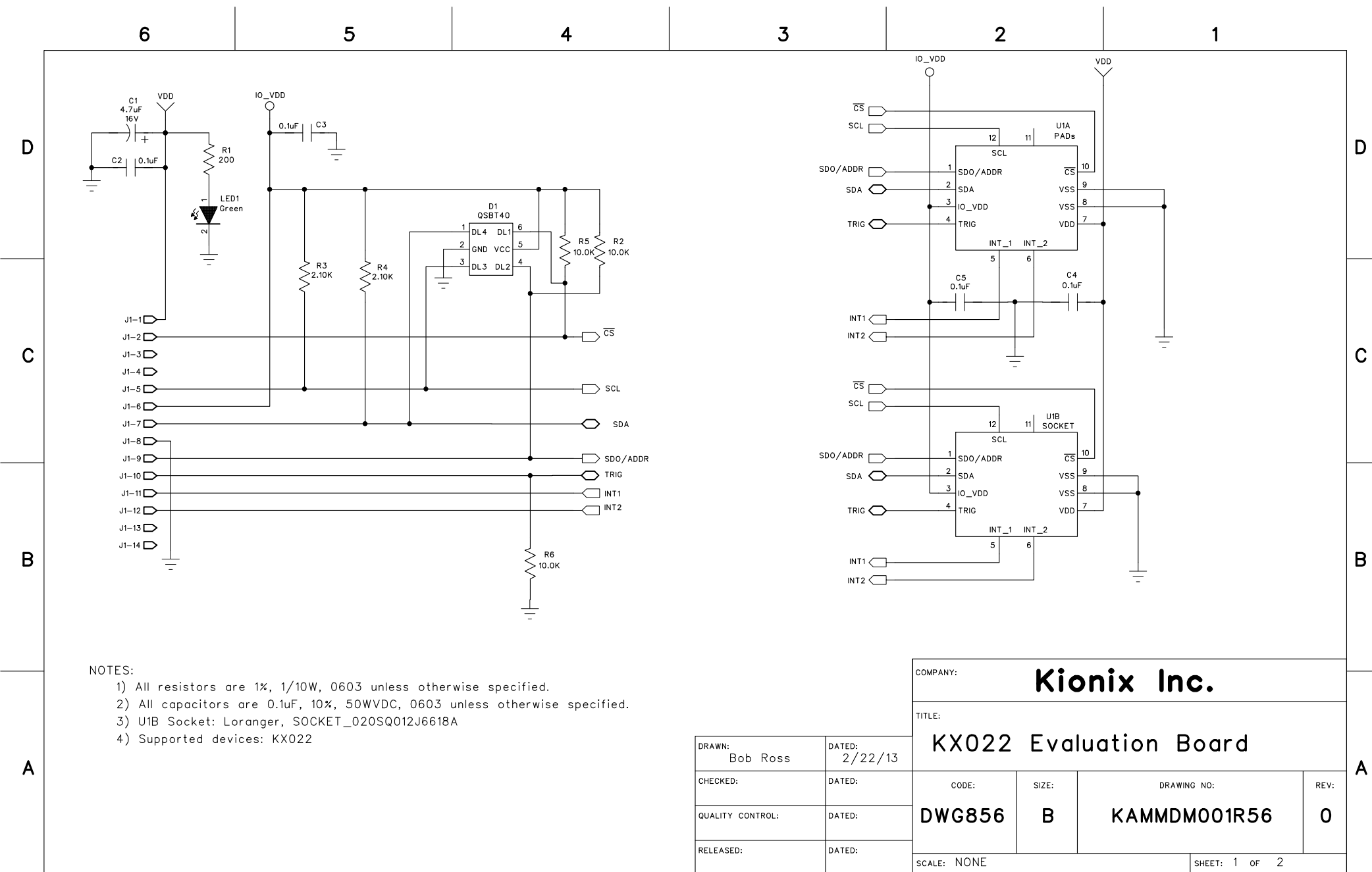
- Description: KX022-1020 Evaluation Board
- Range: $2g$, $4g$, $8g$, User-selectable
- Sensitivity: 16384, 8192, 4096 (16-bit), (counts/g)
- Temperature ($^{\circ}C$): -40 to $+85$

PLEASE SEE THE FOLLOWING PAGES FOR THE BOARD LAYOUT AND SCHEMATIC

KX022 Evaluation Board R56 Layout and Pin Description



J1 Connector Pin Description	
pin 1	VDD
pin 2	nCS
pin 3	DNC
pin 4	DNC
pin 5	SCL
pin 6	IO_VDD
pin 7	SDA
pin 8	GND
pin 9	SDO/ADDR
pin 10	TRIG
pin 11	INT1
pin 12	INT2
pin 13	DNC
pin 14	DNC



6		5		4		3		2		1			
REVISION RECORD													
DATE:		DRAWN BY:		DESCRIPTION:						CHECKED BY:		DATE:	
2/22/13		Bob Ross		Initial release.						EP		10/21/2011	
D													
C													
B													
A									COMPANY: Kionix Inc.				
									TITLE: KX022 Evaluation Board				
	DRAWN: Bob Ross		DATED: 2/22/13		CHECKED:		DATED:		CODE: DWG856	SIZE: B	DRAWING NO: KAMMDM001R56	REV: 0	
	QUALITY CONTROL:		DATED:		RELEASED:		DATED:		SCALE: NONE		SHEET: 2 OF 2		