

15A, 50V Low V_F Trench Schottky Surface Mount Rectifier

FEATURES

- Patented Trench Schottky technology
- Excellent high temperature stability
- Low forward voltage
- Lower power loss/ high efficiency
- High forward surge capability
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APF	PLICA	TIONS
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- Switching mode power supply (SMPS)
- Adapters
- DC to DC converter

MECHANICAL DATA

• Case: SMPC4.0

- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.095g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I _F	15	Α	
V_{RRM}	50	V	
I _{FSM}	200	Α	
T _{J MAX}	150	°C	
Package	SMPC4.0		
Configuration	Single die		

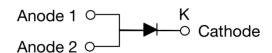








SMPC4.0



PARAMETER	SYMBOL	TSPB15U50S	UNIT
Marking code on the device		B15U50	
Repetitive peak reverse voltage	V_{RRM}	50	V
Reverse voltage, total rms value	V _{R(RMS)}	35	V
Forward current	I _F	15	А
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	200	А
Junction temperature	TJ	- 55 to +150	°C
Storage temperature	T _{STG}	- 55 to +150	°C

Version: I2103



TSPB15U50S Taiwan Semiconductor

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	R _{OJL}	10	°C/W

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
[T] [1]	I _F = 15A, T _J = 25°C	V _F	0.48	0.56	V
Forward voltage ⁽¹⁾	I _F = 15A, T _J = 125°C		0.44	0.50	V
Deviates ourself @ rated V (2)	T _J = 25°C	I _R	_	2	mA
Reverse current @ rated V _R ⁽²⁾	T _J = 125°C		-	140	mA

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE	PACKAGE	PACKING	
TSPB15U50S	SMPC4.0	6,000 / Tape & Reel	



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.1 Forward Current Derating Curve

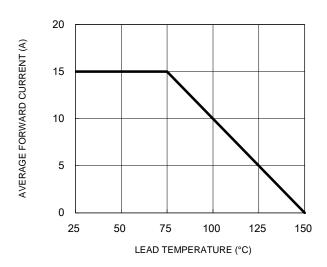


Fig.3 Typical Reverse Characteristics

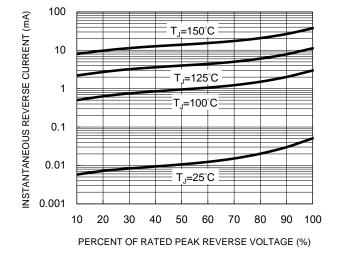


Fig.2 Typical Junction Capacitance

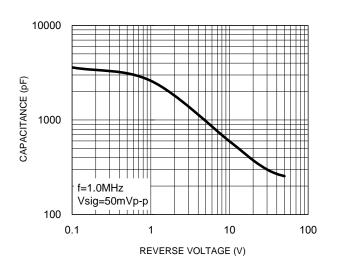
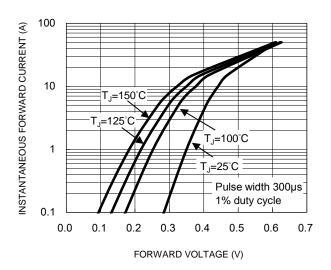


Fig.4 Typical Forward Characteristics

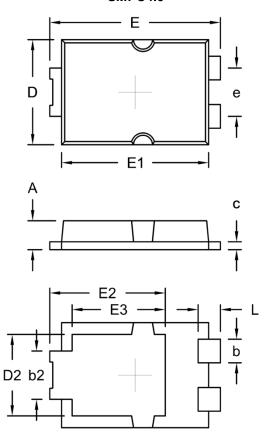


3 Version: I2103



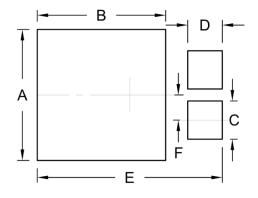
PACKAGE OUTLINE DIMENSIONS

SMPC4.0



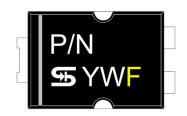
DIM.	Unit (Unit (mm)		Unit (inch)	
DIWI.	Min.	Max.	Min.	Max.		
Α	1.00	1.20	0.039	0.047		
b	0.75	1.05	0.030	0.041		
b2	1.69	1.99	0.067	0.078		
С	0.20	0.40	0.008	0.016		
D	3.95	4.05	0.156	0.159		
D2	2.95	3.25	0.116	0.128		
E	6.35	6.65	0.250	0.262		
E1	5.55	5.65	0.219	0.222		
E2	4.25	4.55	0.167	0.179		
E3	3.40	3.70	0.134	0.146		
е	1.69	1.99	0.067	0.078		
L	0.70	1.00	0.028	0.039		

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
Α	4.80	0.189
В	4.72	0.186
С	1.40	0.055
D	1.27	0.050
E	6.80	0.268
F	0.92	0.036

MARKING DIAGRAM



P/N = Marking Code YW = Date Code F = Factory Code



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