

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

## TC74HC245AP, TC74HC245AF, TC74HC245AFW TC74HC640AP, TC74HC640AF

### Octal Bus Transceiver

|                    |                           |
|--------------------|---------------------------|
| TC74HC245AP/AF/AFW | 3-State,<br>Non-Inverting |
| TC74HC640AP/AF     | 3-State, Inverting        |

Note: xxxFW (JEDEC SOP) is not available in Japan.

The TC74HC245A, 640A are high speed CMOS OCTAL BUS TRANSCEIVERS fabricated with silicon gate C2MOS technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

They are intended for two-way asynchronous communication between data busses. The direction of data transmission is determined by the level of the DIR input.

The enable input ( $\bar{G}$ ) can be used to disable the device so that the busses are effectively isolated.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

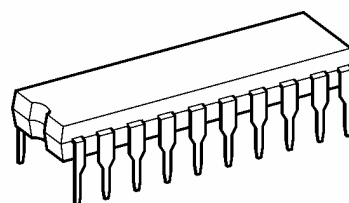
### Features (Note 1)(Note 2)

- High speed:  $t_{pd} = 10 \text{ ns}$  (typ.) at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu\text{A}$  (max) at  $T_a = 25^\circ\text{C}$
- High noise immunity:  $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (min)
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 6 \text{ mA}$  (min)
- Balanced propagation delays:  $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range:  $V_{CC} (\text{opr}) = 2 \sim 6 \text{ V}$
- Pin and function compatible with 74LS245/640

Note 1: Do not apply a signal to any bus terminal when it is in the output mode. Damage may result.

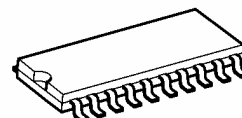
Note 2: All floating (high impedance) bus terminals must have their input levels fixed by means of pull up or pull down resistors.

TC74HC245AP, TC74HC640AP

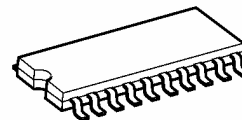


DIP20-P-300-2.54A

TC74HC245AF, TC74HC640AF

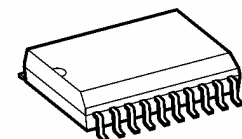


SOP20-P-300-1.27A



SOP20-P-300-1.27

TC74HC245AFW



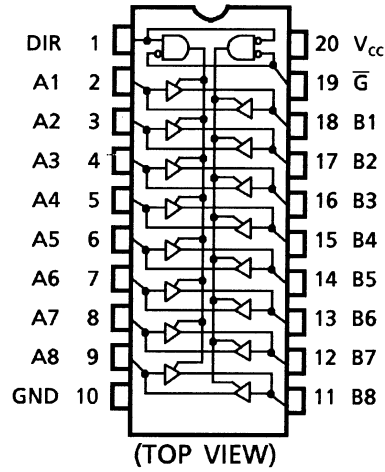
SOL20-P-300-1.27

#### Weight

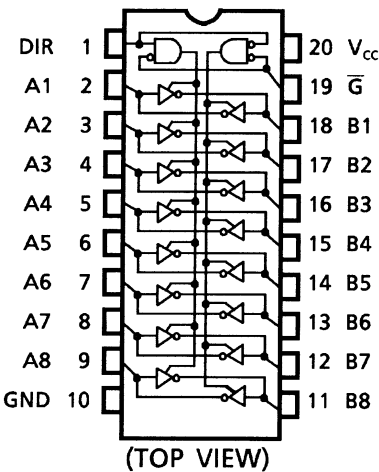
|                   |                 |
|-------------------|-----------------|
| DIP20-P-300-2.54A | : 1.30 g (typ.) |
| SOP20-P-300-1.27A | : 0.22 g (typ.) |
| SOP20-P-300-1.27  | : 0.22 g (typ.) |
| SOL20-P-300-1.27  | : 0.46 g (typ.) |

Pin Assignment

TC74HC245A

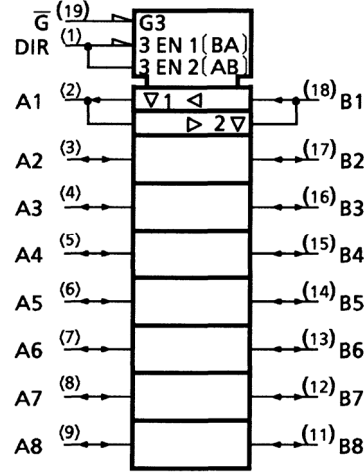


TC74HC640A

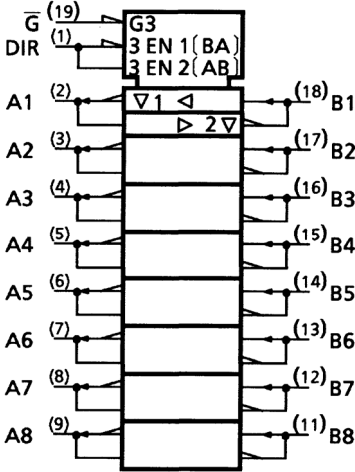


IEC Logic Symbol

TC74HC245A



TC74HC640A



Truth Table

| Inputs         |     | Function |        | Outputs |                    |
|----------------|-----|----------|--------|---------|--------------------|
| $\overline{G}$ | DIR | A Bus    | B Bus  | HC245A  | HC640A             |
| L              | L   | Output   | Input  | $A = B$ | $A = \overline{B}$ |
| L              | H   | Input    | Output | $B = A$ | $B = \overline{A}$ |
| H              | X   | Z        |        | Z       | Z                  |

X: "H" or "L"

Z: High impedance

## Absolute Maximum Ratings (Note 1)

| Characteristics             | Symbol    | Rating                       | Unit |
|-----------------------------|-----------|------------------------------|------|
| Supply voltage range        | $V_{CC}$  | $-0.5 \sim 7$                | V    |
| DC input voltage            | $V_{IN}$  | $-0.5 \sim V_{CC} + 0.5$     | V    |
| DC output voltage           | $V_{OUT}$ | $-0.5 \sim V_{CC} + 0.5$     | V    |
| Input diode current         | $I_{IK}$  | $\pm 20$                     | mA   |
| Output diode current        | $I_{OK}$  | $\pm 20$                     | mA   |
| DC output current           | $I_{OUT}$ | $\pm 35$                     | mA   |
| DC $V_{CC}$ /ground current | $I_{CC}$  | $\pm 75$                     | mA   |
| Power dissipation           | $P_D$     | 500 (DIP) (Note 2)/180 (SOP) | mW   |
| Storage temperature         | $T_{stg}$ | $-65 \sim 150$               | °C   |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: 500 mW in the range of  $T_a = -40$  to  $65^\circ\text{C}$ . From  $T_a = 65$  to  $85^\circ\text{C}$  a derating factor of  $-10 \text{ mW}/^\circ\text{C}$  shall be applied until 300 mW.

## Operating Range (Note)

| Characteristics          | Symbol     | Rating                                                                                                          | Unit |
|--------------------------|------------|-----------------------------------------------------------------------------------------------------------------|------|
| Supply voltage           | $V_{CC}$   | $2 \sim 6$                                                                                                      | V    |
| Input voltage            | $V_{IN}$   | $0 \sim V_{CC}$                                                                                                 | V    |
| Output voltage           | $V_{OUT}$  | $0 \sim V_{CC}$                                                                                                 | V    |
| Operating temperature    | $T_{opr}$  | $-40 \sim 85$                                                                                                   | °C   |
| Input rise and fall time | $t_r, t_f$ | 0~1000 ( $V_{CC} = 2.0 \text{ V}$ )<br>0~500 ( $V_{CC} = 4.5 \text{ V}$ )<br>0~400 ( $V_{CC} = 6.0 \text{ V}$ ) | ns   |

Note: The operating range is required to ensure the normal operation of the device. Unused inputs and bus inputs must be tied to either  $V_{CC}$  or GND. Please connect both bus inputs and the bus outputs with  $V_{CC}$  or GND when the I/O of the bus terminal changes by the function. In this case, please note that the output is not short-circuited.

## Electrical Characteristics

### DC Characteristics

| Characteristics           | Symbol | Test Condition   |                              | Ta = 25°C         |                      |             |                      | Ta = -40~85°C        |                      | Unit |
|---------------------------|--------|------------------|------------------------------|-------------------|----------------------|-------------|----------------------|----------------------|----------------------|------|
|                           |        |                  |                              | VCC (V)           | Min                  | Typ.        | Max                  | Min                  | Max                  |      |
| High-level input voltage  | VIH    | —                |                              | 2.0<br>4.5<br>6.0 | 1.50<br>3.15<br>4.20 | —<br>—<br>— | —<br>—<br>—          | 1.50<br>3.15<br>4.20 | —<br>—<br>—          | V    |
| Low-level input voltage   | VIL    | —                |                              | 2.0<br>4.5<br>6.0 | —<br>—<br>—          | —<br>—<br>— | 0.50<br>1.35<br>1.80 | —<br>—<br>—          | 0.50<br>1.35<br>1.80 | V    |
| High-level output voltage | VOH    | VIN = VIH or VIL | IOH = -20 μA                 | 2.0               | 1.9                  | 2.0         | —                    | 1.9                  | —                    | V    |
|                           |        |                  |                              | 4.5               | 4.4                  | 4.5         | —                    | 4.4                  | —                    |      |
|                           |        |                  | IOH = -6 mA<br>IOH = -7.8 mA | 6.0               | 5.9                  | 6.0         | —                    | 5.9                  | —                    |      |
|                           |        |                  |                              | 4.5               | 4.18                 | 4.31        | —                    | 4.13                 | —                    |      |
| Low-level output voltage  | VOL    | VIN = VIH or VIL | IOL = 20 μA                  | 2.0               | —                    | 0.0         | 0.1                  | —                    | 0.1                  | V    |
|                           |        |                  |                              | 4.5               | —                    | 0.0         | 0.1                  | —                    | 0.1                  |      |
|                           |        |                  | IOL = 6 mA<br>IOL = 7.8 mA   | 6.0               | —                    | 0.0         | 0.1                  | —                    | 0.1                  |      |
|                           |        |                  |                              | 4.5               | —                    | 0.17        | 0.26                 | —                    | 0.33                 |      |
| Input leakage current     | IIN    | VIN = VCC or GND |                              | 6.0               | —                    | —           | ±0.1                 | —                    | ±1.0                 | μA   |
|                           |        | VIN = VCC or GND |                              | 6.0               | —                    | —           | 4.0                  | —                    | 40.0                 | μA   |

## AC Characteristics (input: $t_r = t_f = 6 \text{ ns}$ )

| Characteristics               | Symbol                               | Test Condition                   |         |         | Ta = 25°C |      |     | Ta = -40~85°C |     | Unit |
|-------------------------------|--------------------------------------|----------------------------------|---------|---------|-----------|------|-----|---------------|-----|------|
|                               |                                      |                                  | CL (pF) | VCC (V) | Min       | Typ. | Max | Min           | Max |      |
| Output transition time        | $t_{\text{TLH}}$<br>$t_{\text{THL}}$ | —                                | 50      | 2.0     | —         | 52   | 60  | —             | 75  | ns   |
|                               |                                      |                                  |         | 4.5     | —         | 7    | 12  | —             | 15  |      |
|                               |                                      |                                  |         | 6.0     | —         | 6    | 10  | —             | 13  |      |
| Propagation delay time        | $t_{\text{pLH}}$<br>$t_{\text{pHL}}$ | —                                | 50      | 2.0     | —         | 33   | 90  | —             | 115 | ns   |
|                               |                                      |                                  |         | 4.5     | —         | 12   | 18  | —             | 23  |      |
|                               |                                      |                                  |         | 6.0     | —         | 10   | 15  | —             | 20  |      |
|                               |                                      |                                  | 150     | 2.0     | —         | 48   | 120 | —             | 150 |      |
|                               |                                      |                                  |         | 4.5     | —         | 16   | 24  | —             | 30  |      |
|                               |                                      |                                  |         | 6.0     | —         | 14   | 20  | —             | 26  |      |
| 3-state output enable time    | $t_{\text{pZL}}$<br>$t_{\text{pZH}}$ | $R_{\text{L}} = 1\text{k}\Omega$ | 50      | 2.0     | —         | 48   | 150 | —             | 190 | ns   |
|                               |                                      |                                  |         | 4.5     | —         | 16   | 30  | —             | 38  |      |
|                               |                                      |                                  |         | 6.0     | —         | 14   | 26  | —             | 32  |      |
|                               |                                      |                                  | 150     | 2.0     | —         | 63   | 180 | —             | 225 |      |
|                               |                                      |                                  |         | 4.5     | —         | 21   | 36  | —             | 45  |      |
|                               |                                      |                                  |         | 6.0     | —         | 18   | 31  | —             | 38  |      |
| 3-state output disable time   | $t_{\text{pLZ}}$<br>$t_{\text{pHZ}}$ | $R_{\text{L}} = 1\text{k}\Omega$ | 50      | 2.0     | —         | 37   | 150 | —             | 190 | ns   |
|                               |                                      |                                  |         | 4.5     | —         | 17   | 30  | —             | 38  |      |
|                               |                                      |                                  |         | 6.0     | —         | 15   | 26  | —             | 32  |      |
| Input capacitance             | $C_{\text{IN}}$                      | $\text{DIR}, \text{G}$           |         |         | —         | 5    | 10  | —             | 10  | pF   |
| Bus input capacitance         | $C_{\text{OUT}}$                     | $\text{An}, \text{Bn}$           |         |         | —         | 13   | —   | —             | —   | pF   |
| Power dissipation capacitance | $C_{\text{PD}}$                      | TC74HC245A                       |         |         | —         | 39   | —   | —             | —   | pF   |
|                               | (Note)                               | TC74HC640A                       |         |         | —         | 37   | —   | —             | —   |      |

Note:  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$$

DIP20-P-300-2.54A

Technical drawing of the 24-pin connector showing three views: top, side, and front views with dimensions.

**Top View Dimensions:**

- Pin 1 to Pin 11 distance: 20
- Pin 10 to Pin 11 distance: 11
- Overall width:  $6.4 \pm 0.2$

**Side View Dimensions:**

- Pin height:  $7.62$
- Pin angle:  $0 \sim 15^\circ$
- Pin thickness:  $0.25^{+0.1}_{-0.05}$

**Front View Dimensions:**

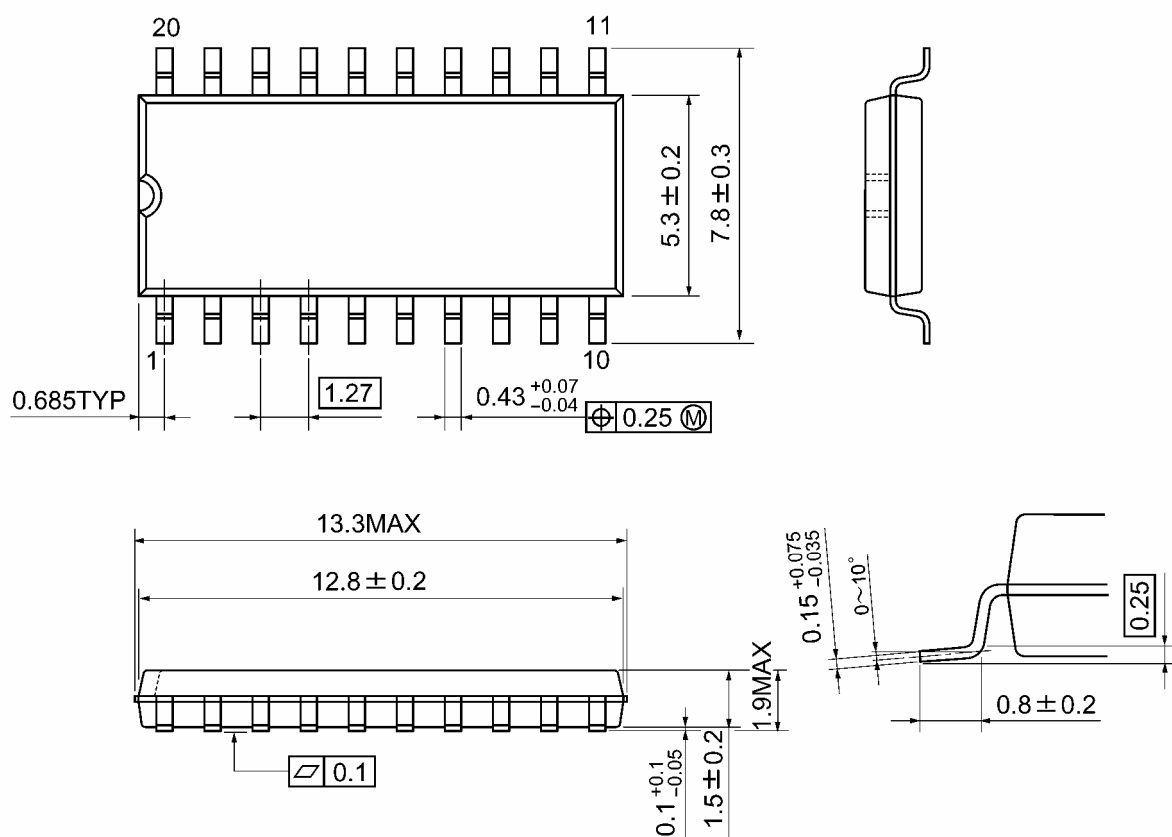
- Overall length:  $25.1 \text{ MAX}$
- Pin pitch:  $2.54$
- Pin 1 to Pin 11 distance:  $24.6 \pm 0.2$
- Pin 1 to Pin 11 distance (alternative):  $0.95 \pm 0.1$
- Pin 1 to Pin 11 distance (alternative):  $0.87 \text{ TYP}$
- Pin 1 to Pin 11 distance (alternative):  $1.4 \pm 0.1$
- Pin 1 to Pin 11 distance (alternative):  $0.5 \pm 0.1$
- Pin 1 to Pin 11 distance (alternative):  $0.51 \text{ MIN}$
- Pin 1 to Pin 11 distance (alternative):  $3.5 \pm 0.2$
- Pin 1 to Pin 11 distance (alternative):  $4.15 \pm 0.3$
- Pin 1 to Pin 11 distance (alternative):  $3.3 \pm 0.3$
- Pin 1 to Pin 11 distance (alternative):  $0.25 \text{ (M)}$

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## Package Dimensions

SOP20-P-300-1.27A

Unit: mm

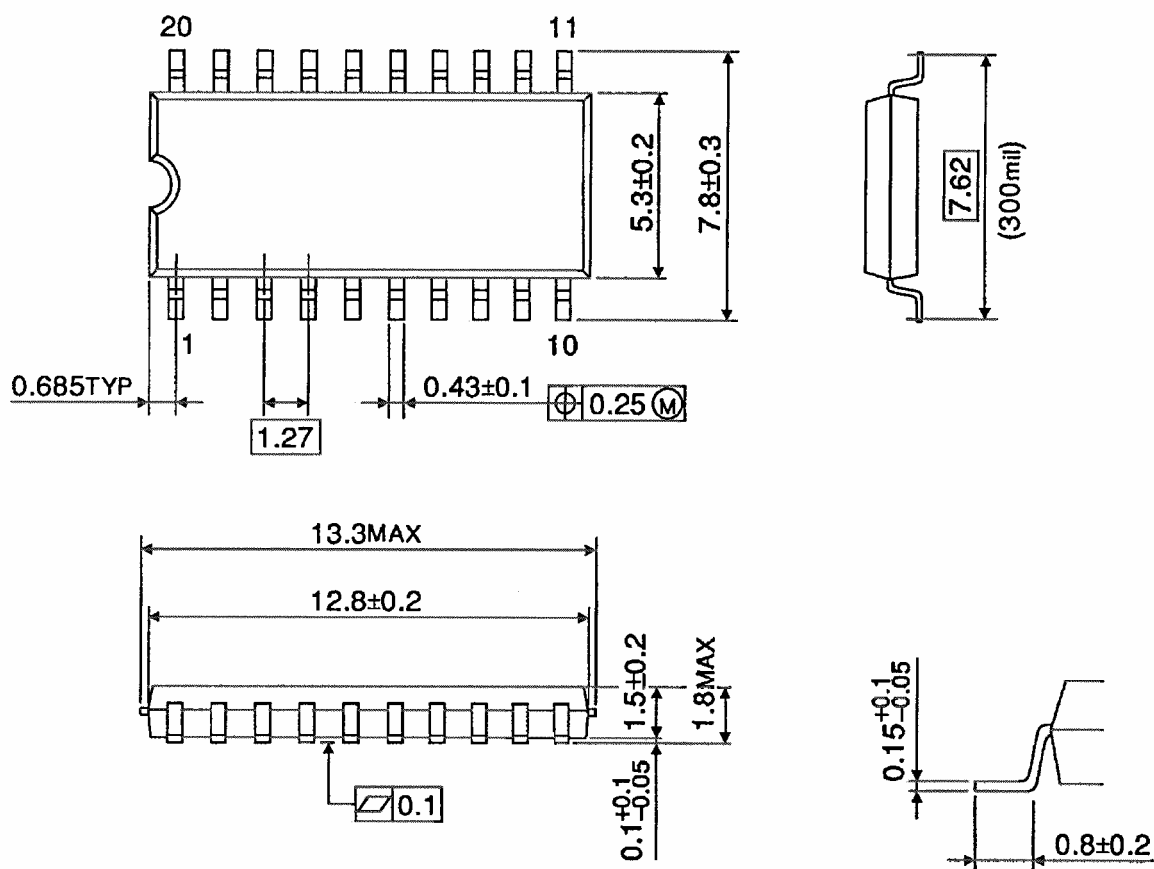


Weight: 0.22 g (typ.)

## Package Dimensions

SOP20-P-300-1.27

Unit : mm

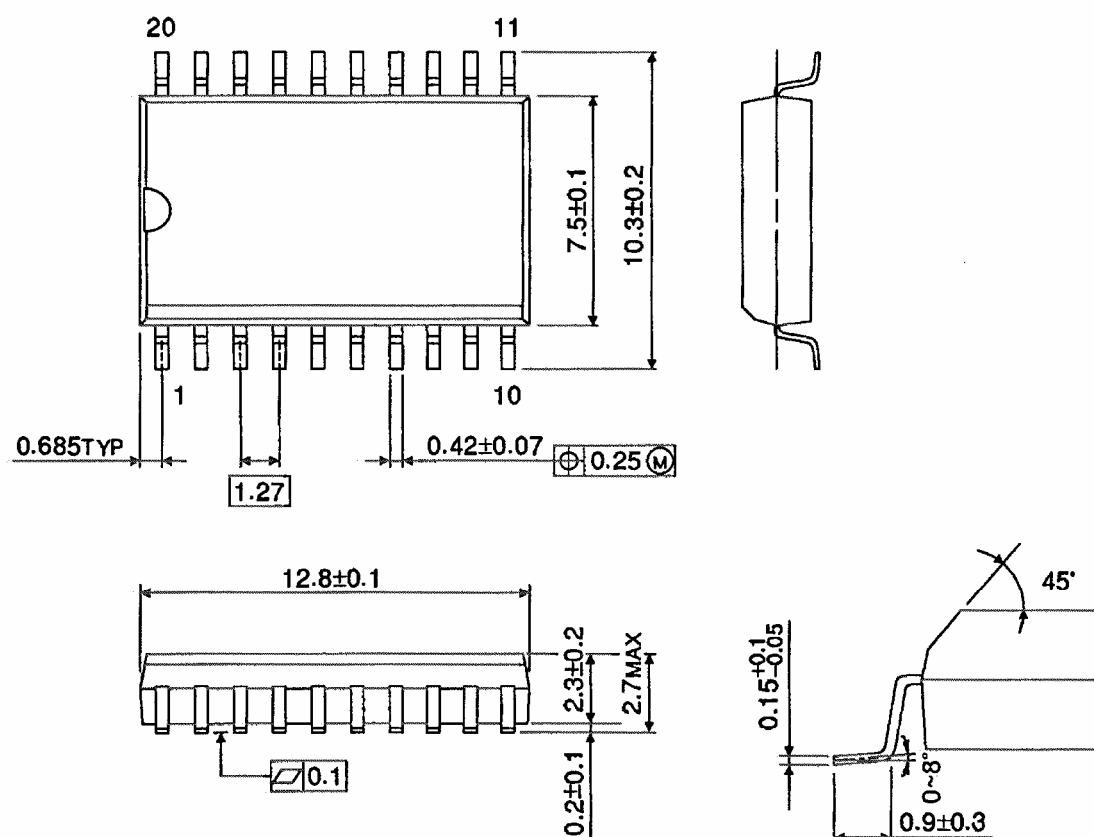


Weight: 0.22 g (typ.)

## Package Dimensions (Note)

SOL20-P-300-1.27

Unit : mm



Note: This package is not available in Japan.

Weight: 0.46 g (typ.)

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