Multilayer Ceramic Chip Capacitors

C3225X7R1E475K200AA

Product Status	Production (NRND) Recommended Alternate Part No. : C3225X7R1H475K250AB (Interchangeability is not guaranteed.)
TDK Item Description	C3225X7R1E475KT****
Applications	Commercial Grade
Feature	General (Up to 75V)
Series	C3225 [EIA 1210]
Brand	TDK
Environmental Compliance	RoHS, REACH, Halogen Free, Lead Free



Images are for reference only and show example products.

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Tech Info

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DOCUMENTS
- Specification
- RoHS Certificate
- SVHC / REACH Certificate
- Sample Kits

TECHNICAL SUPPORT TOOLS
- Parts Selection Tool TDK Meister
- S-parameter
- SPICE Netlist (Simple)
- SPICE Netlist (Precision)
- Equivalent Circuit Model
- DC Bias / Superimpotision Model (HSpice)
- DC Bias / Superimpotision Model (LTSpice)
- DC Bias / Superimpotision Model (PSpice)

3RD PARTY ECAD MODELS
- SnapMagic (Symbol, Footprint, 3D model)

Size

Length(L)	3.20mm ±0.40mm
Width(W)	2.50mm ±0.30mm
Thickness(T)	2.00mm ±0.20mm
Terminal Width(B)	0.20mm Min.
Recommended Land Pattern (PA)	2.00mm to 2.40mm
Recommended Land Pattern (PB)	1.00mm to 1.20mm
Recommended Land Pattern (PC)	1.90mm to 2.50mm

Electrical Characteristics

Capacitance	4.7μF ±10%
Rated Voltage	25VDC
Temperature Characteristic	X7R(±15%)
Dissipation Factor (Max.)	3%
Insulation Resistance (Min.)	106MΩ

Other

Operating Temp. Range	-55 to 125℃
Soldering Method	Reflow
AEC-Q200	NO
Packing	Embossed (Plastic)Taping [180mm Reel]
Package Quantity	1000pcs
FIT (Failure In Time)	This will be displayed when you login to myTDK

Related articles



Replacing Electrolytic Capacitor with MLCC, Revised Guide

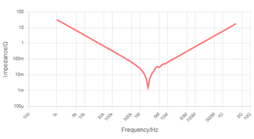
Traditionally, aluminum electrolytic capacitors and tantalum electrolytic capacitors have been widely used for...

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Characteristic Graph

This data is for reference only and does not guarantee product characteristics.

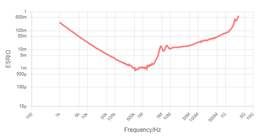
Impedance



C3225X7R1E475K200AA

GRAPH SETTINGS

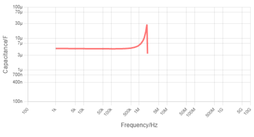
ESR



C3225X7R1E475K200AA

GRAPH SETTINGS

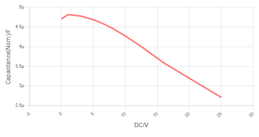
Capacitance



C3225X7R1E475K200AA

GRAPH SETTINGS

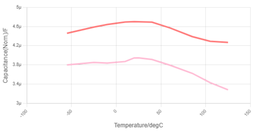
DC Bias Characteristic



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GRAPH SETTINGS

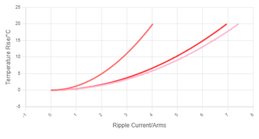
Temperature Characteristic



C3225X7R1E475K200AA(No Bias)
C3225X7R1E475K200AA(DC Bias + 12.5V)

GRAPH SETTINGS

Ripple Temperature Rising



C3225X7R1E475K200AA(100kHz)
C3225X7R1E475K200AA(500kHz)
C3225X7R1E475K200AA(1MHz)

GRAPH SETTINGS

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MULTILAYER CERAMIC CHIP CAPACITORS
C1005NP01H3R3C050BA
Capacitance=3.3pF
Edc=50V
T.C.=NP0
LxWxT:1x0.5x0.5mm
High Temp. (Up to 150°C)



MULTILAYER CERAMIC CHIP CAPACITORS
C1005X5R0G335K050BB
Capacitance=3.3μF
Edc=4V
T.C.=X5R
LxWxT:1x0.5x0.5mm
General



MULTILAYER CERAMIC CHIP CAPACITORS
C1005X5R1A473K050BA
Capacitance=47nF
Edc=10V
T.C.=X5R
LxWxT:1x0.5x0.5mm
General

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