

Product Summary

Device	BV _{DSS}	R _{Ds(ON)} Max	I _D T _A = +25°C
Q1 N-Channel	100V	160mΩ @ V _{GS} = 10V	2.0A
		200mΩ @ V _{GS} = 4.5V	1.5A
Q2 P-Channel	-100V	250mΩ @ V _{GS} = -10V	-1.7A
		300mΩ @ V _{GS} = -4.5V	-1.3A

Features and Benefits

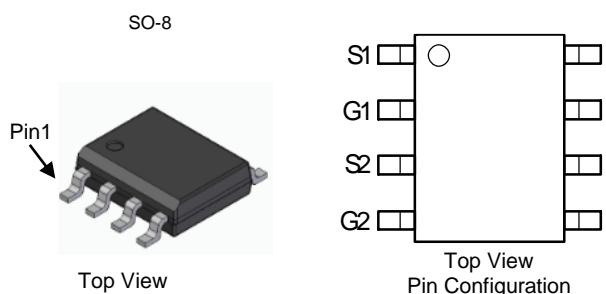
- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](#) or your local Diodes representative.

<https://www.diodes.com/quality/product-definitions/>

Description and Applications

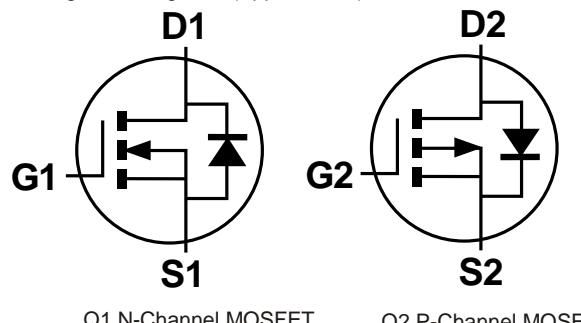
This new generation MOSFET has been designed to minimize the on-state resistance (R_{Ds(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC converters
- Power management functions
- Backlighting



Mechanical Data

- Package: SO-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.074 grams (Approximate)



Q1 N-Channel MOSFET Q2 P-Channel MOSFET

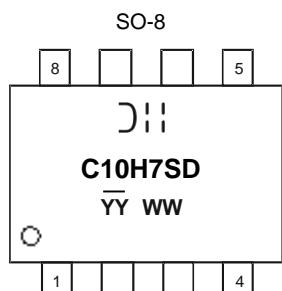
Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMC10H172SSD-13	SO-8	2,500	Tape & Reel

Notes:

- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



C10 = Manufacturer's Marking
 C10H7SD = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Year (ex: 22 = 2022)
 WW = Week (01 to 53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Q1	Q2	Units
Drain-Source Voltage		V_{DSS}		100	-100	V
Gate-Source Voltage		V_{GSS}		± 20	± 20	V
Continuous Drain Current (Note 6) Q1: $V_{GS} = 10\text{V}$ Q2: $V_{GS} = -10\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	2.0 1.6	-1.7 -1.4	A
Maximum Body Diode Forward Current (Note 6)			I_S	2.0	-1.7	A
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)			I_{DM}	16	-13	A
Avalanche Current, $L = 1.43\text{mH}$			I_{AS}	5.3	-6	A
Avalanche Energy, $L = 1.43\text{mH}$			E_{AS}	20	25	mJ

 Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Total Power Dissipation (Note 5)			P_D	1.1	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State		$R_{\theta JA}$	110	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)			P_D	1.5	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		$R_{\theta JA}$	80	$^\circ\text{C/W}$
Operating and Storage Temperature Range			T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

 Electrical Characteristics N-Channel Q1 (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	100	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	103	160	$\text{m}\Omega$	$V_{GS} = 10\text{V}, I_D = 1.6\text{A}$
		—	110	200		$V_{GS} = 4.5\text{V}, I_D = 1.3\text{A}$
Diode Forward Voltage	V_{SD}	—	0.7	1.2	V	$V_{GS} = 0\text{V}, I_S = 1.1\text{A}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	1145	—	pF	$V_{DS} = 50\text{V}, V_{GS} = 0\text{V}$ $f = 1\text{MHz}$
Output Capacitance	C_{oss}	—	26	—		
Reverse Transfer Capacitance	C_{rss}	—	20	—		
Gate Resistance	R_G	—	1.3	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge ($V_{GS} = 4.5\text{V}$)	Q_g	—	9.6	—	nC	$V_{DS} = 50\text{V}, I_D = 1\text{A}$
Total Gate Charge ($V_{GS} = 10\text{V}$)	Q_g	—	19.6	—		
Gate-Source Charge	Q_{gs}	—	2.7	—		
Gate-Drain Charge	Q_{gd}	—	3.3	—		
Turn-On Delay Time	$t_{D(ON)}$	—	9	—	ns	$V_{DS} = 50\text{V}, V_{GS} = 4.5\text{V}, R_G = 6.8\Omega, I_D = 1\text{A}$
Turn-On Rise Time	t_R	—	13	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	22	—		
Turn-Off Fall Time	t_F	—	7.5	—		
Body Diode Reverse Recovery Time	t_{RR}	—	19	—	ns	$I_F = 1\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Body Diode Reverse Recovery Charge	Q_{RR}	—	14	—	nC	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

Electrical Characteristics P-Channel Q2 (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-100	—	—	V	$\text{V}_{\text{GS}} = 0\text{V}$, $\text{I}_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	μA	$\text{V}_{\text{DS}} = -100\text{V}$, $\text{V}_{\text{GS}} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 100	nA	$\text{V}_{\text{GS}} = \pm 20\text{V}$, $\text{V}_{\text{DS}} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	-1.0	—	-3.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}$, $\text{I}_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$\text{R}_{\text{DS(ON)}}$	—	195	250	$\text{m}\Omega$	$\text{V}_{\text{GS}} = -10\text{V}$, $\text{I}_D = -1\text{A}$
		—	210	300		$\text{V}_{\text{GS}} = -4.5\text{V}$, $\text{I}_D = -1\text{A}$
Diode Forward Voltage	V_{SD}	—	-0.8	-1.2	V	$\text{V}_{\text{GS}} = 0\text{V}$, $\text{I}_S = -1\text{A}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	1030	—	pF	$\text{V}_{\text{DS}} = -50\text{V}$, $\text{V}_{\text{GS}} = 0\text{V}$, $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	33	—		
Reverse Transfer Capacitance	C_{rss}	—	24	—		
Gate Resistance	R_{g}	—	13	—	Ω	$\text{V}_{\text{DS}} = 0\text{V}$, $\text{V}_{\text{GS}} = 0\text{V}$, $f = 1.0\text{MHz}$
Total Gate Charge ($\text{V}_{\text{GS}} = -4.5\text{V}$)	Q_{g}	—	9	—	nC	$\text{V}_{\text{DS}} = -50\text{V}$, $\text{I}_D = -1\text{A}$
Total Gate Charge ($\text{V}_{\text{GS}} = -10\text{V}$)	Q_{g}	—	18	—		
Gate-Source Charge	Q_{gs}	—	2	—		
Gate-Drain Charge	Q_{gd}	—	1.9	—	ns	$\text{V}_{\text{DD}} = -50\text{V}$, $\text{R}_{\text{G}} = 9.1\Omega$, $\text{I}_D = -1\text{A}$
Turn-On Delay Time	$\text{t}_{\text{D(ON)}}$	—	8	—		
Turn-On Rise Time	t_{R}	—	16	—		
Turn-Off Delay Time	$\text{t}_{\text{D(OFF)}}$	—	36	—		
Turn-Off Fall Time	t_{F}	—	18	—	ns	$\text{I}_{\text{F}} = -1\text{A}$, $\text{di/dt} = 100\text{A}/\mu\text{s}$
Body Diode Reverse Recovery Time	t_{RR}	—	16	—		
Body Diode Reverse Recovery Charge	Q_{RR}	—	11	—	nC	

Notes: 7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

N-Channel

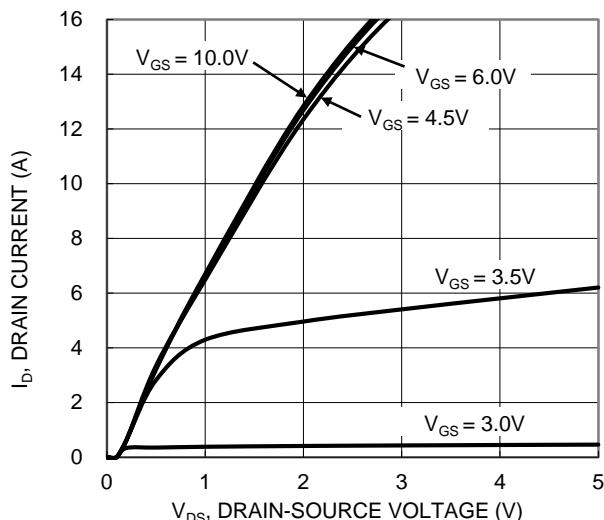


Figure 1. Typical Output Characteristic

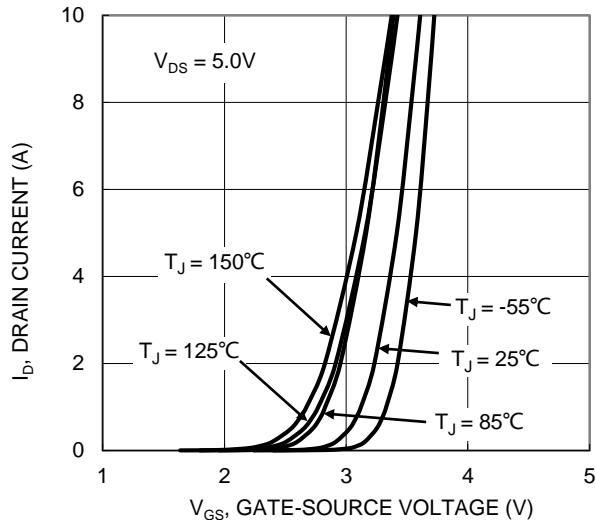


Figure 2. Typical Transfer Characteristic

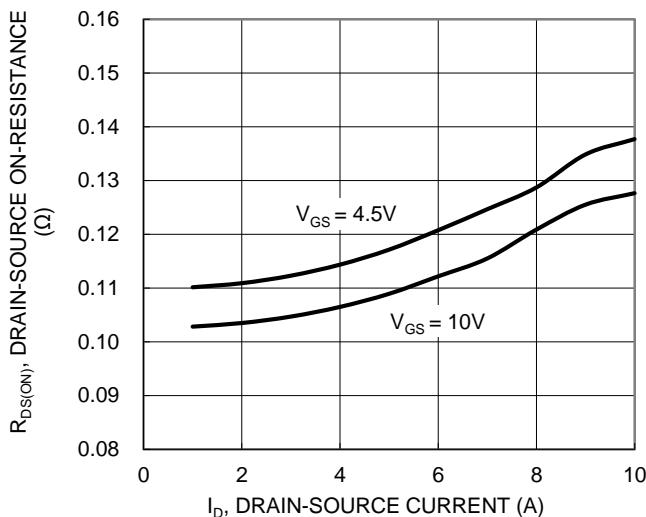


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

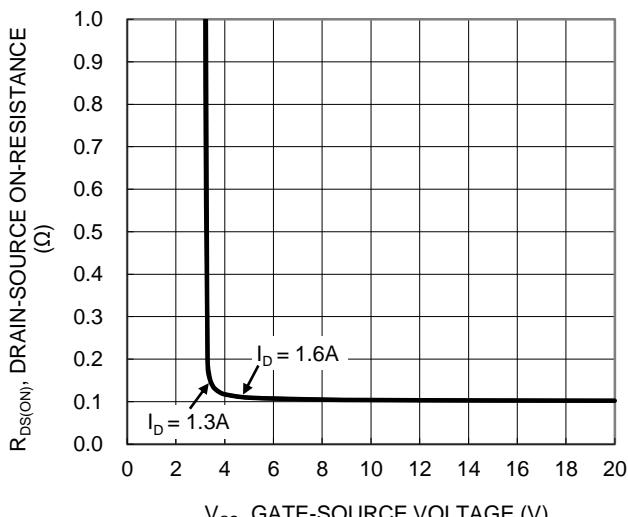


Figure 4. Typical Transfer Characteristic

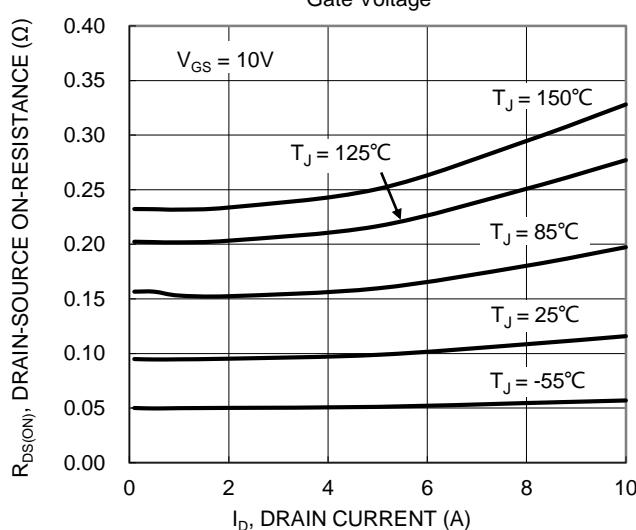


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

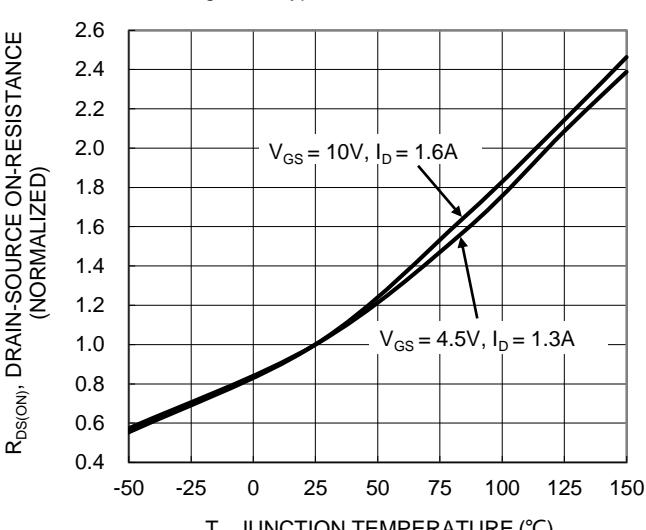


Figure 6. On-Resistance Variation with Junction Temperature

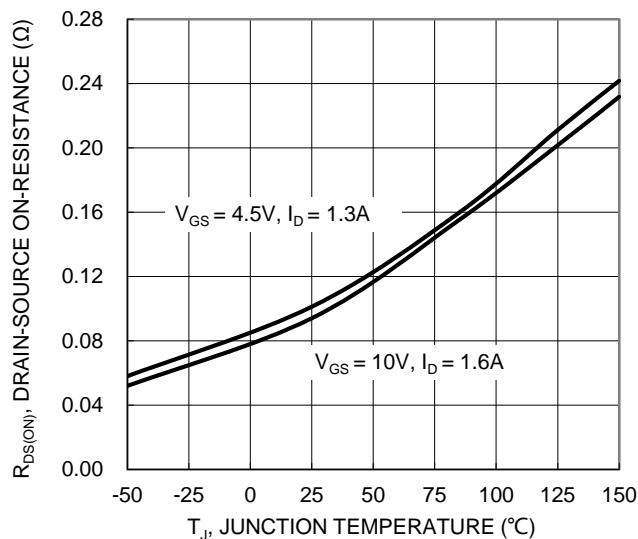


Figure 7. On-Resistance Variation with Junction Temperature

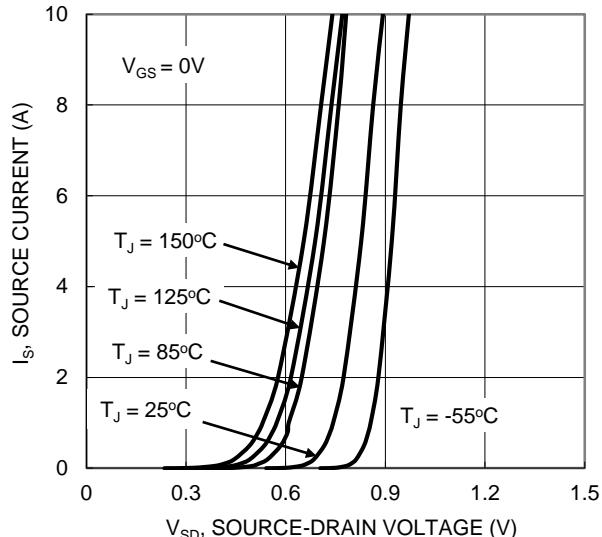


Figure 9. Diode Forward Voltage vs. Current

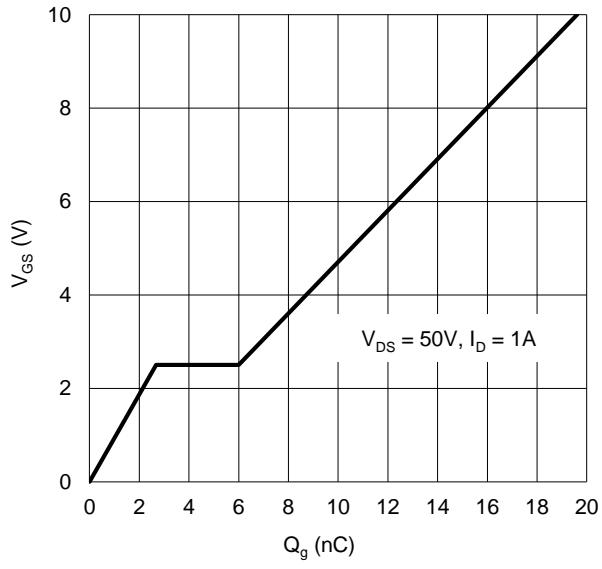


Figure 11. Gate Charge

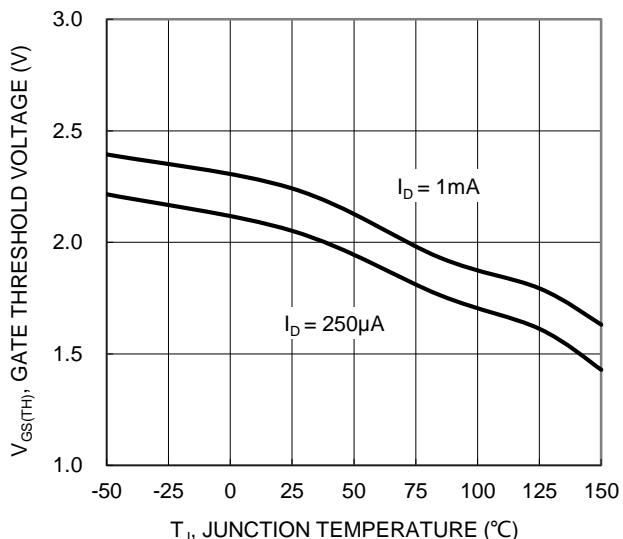


Figure 8. Gate Threshold Variation vs. Junction Temperature

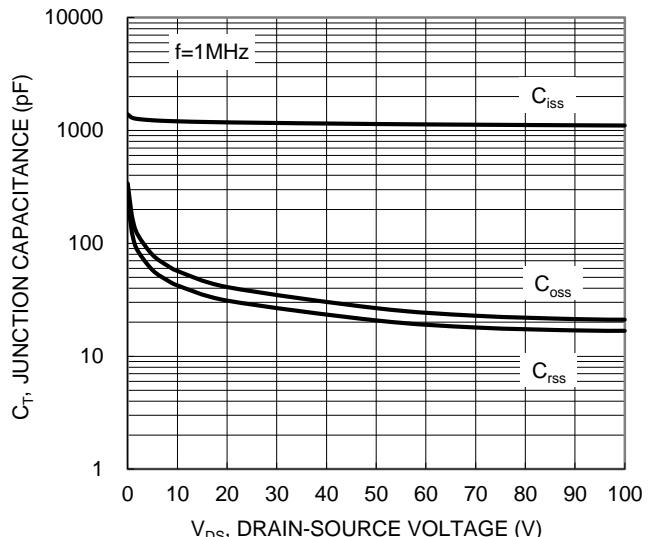


Figure 10. Typical Junction Capacitance

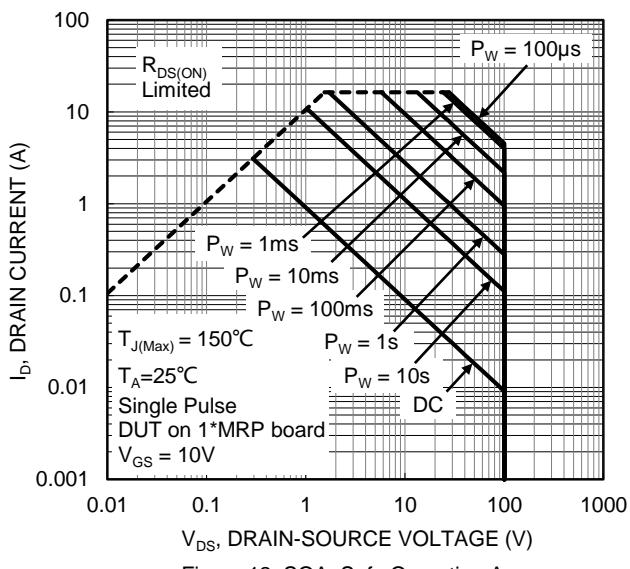


Figure 12. SOA, Safe Operation Area

P-Channel

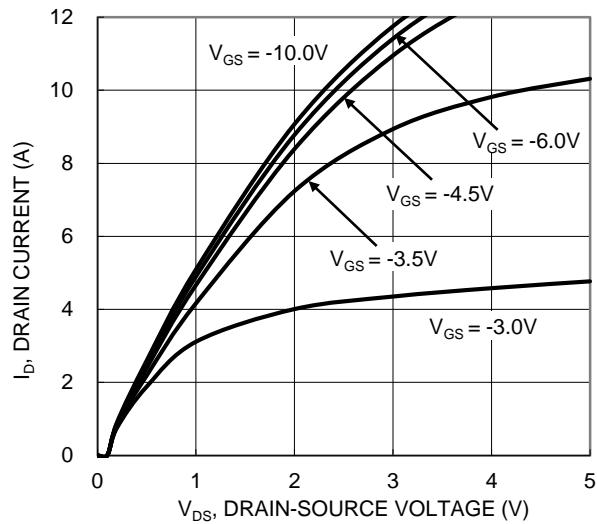


Figure 13. Typical Output Characteristic

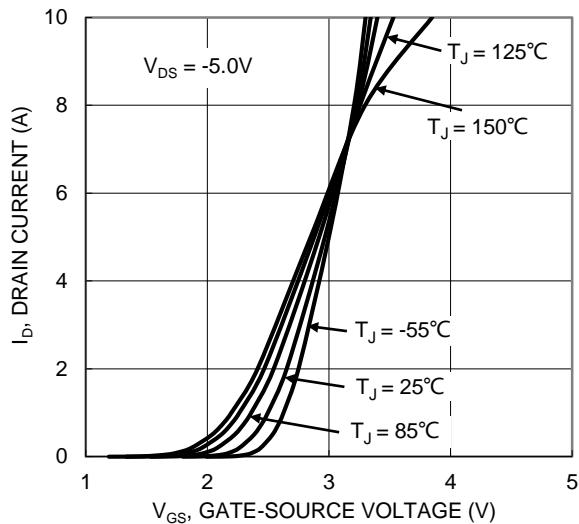


Figure 14. Typical Transfer Characteristic

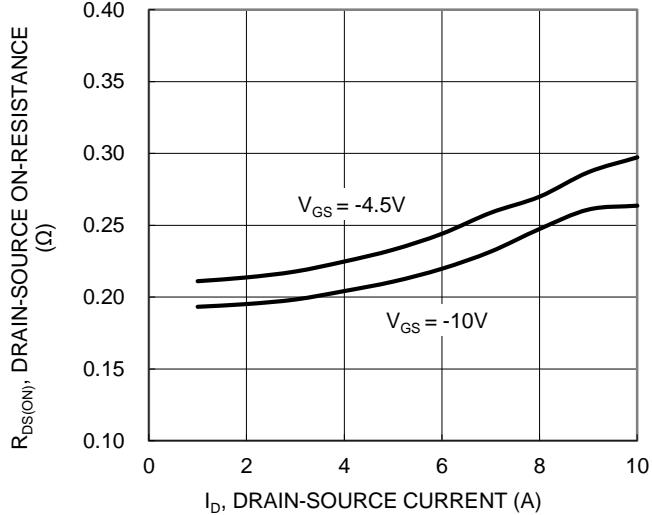


Figure 15. Typical On-Resistance vs. Drain Current and Gate Voltage

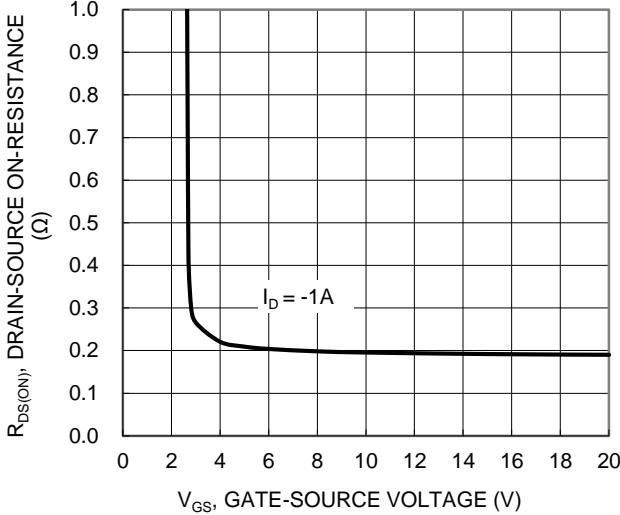


Figure 16. Typical Transfer Characteristic

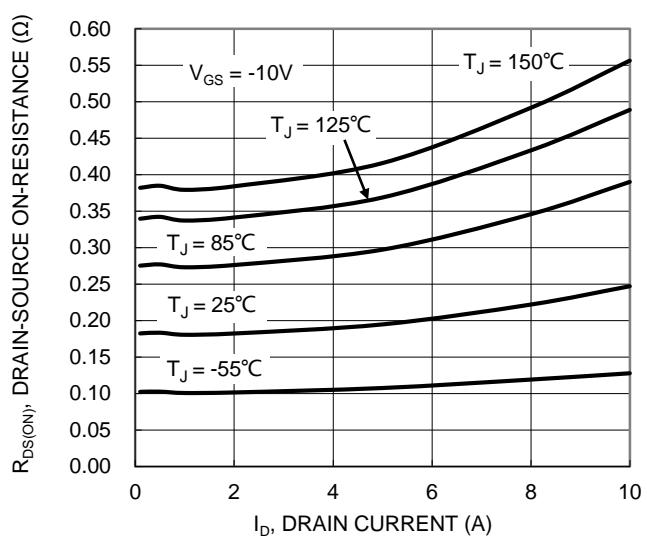


Figure 17. Typical On-Resistance vs. Drain Current and Junction Temperature

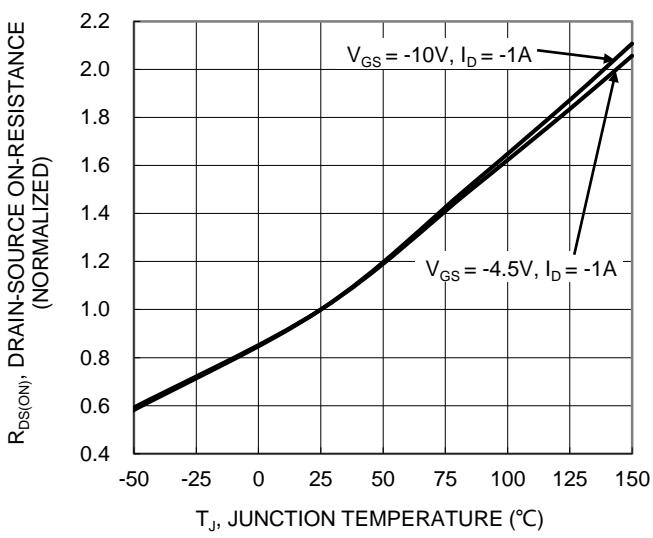


Figure 18. On-Resistance Variation with Junction Temperature

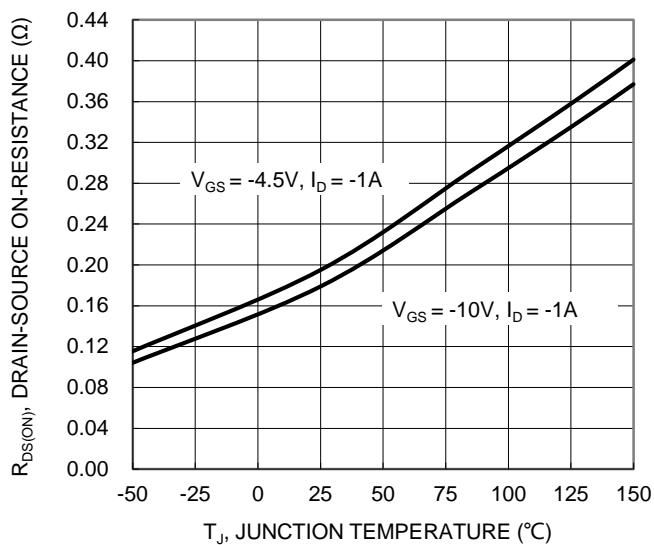


Figure 19. On-Resistance Variation with Junction Temperature

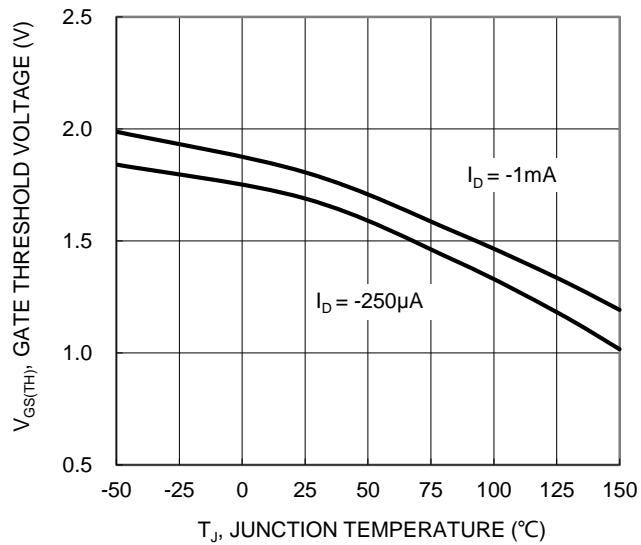


Figure 20. Gate Threshold Variation vs. Junction Temperature

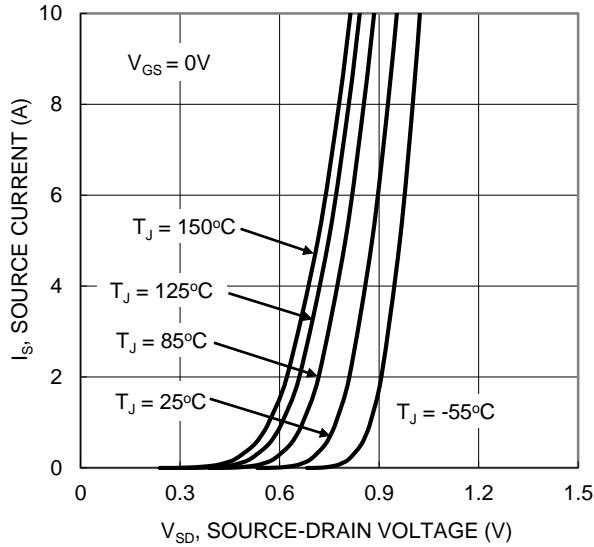


Figure 21. Diode Forward Voltage vs. Current

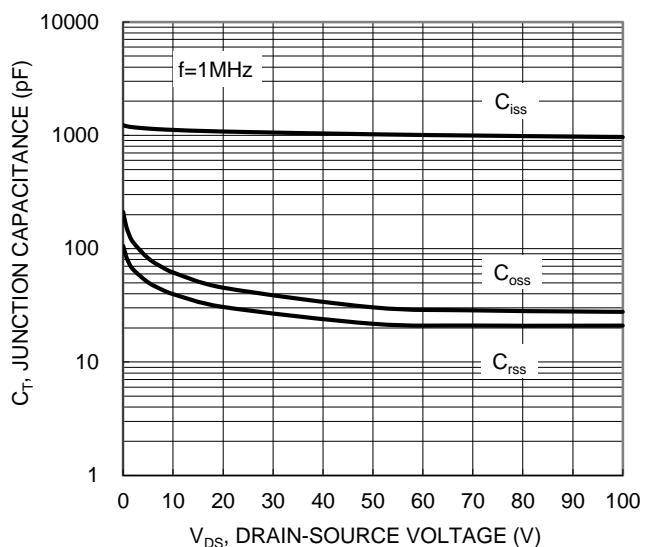


Figure 22. Typical Junction Capacitance

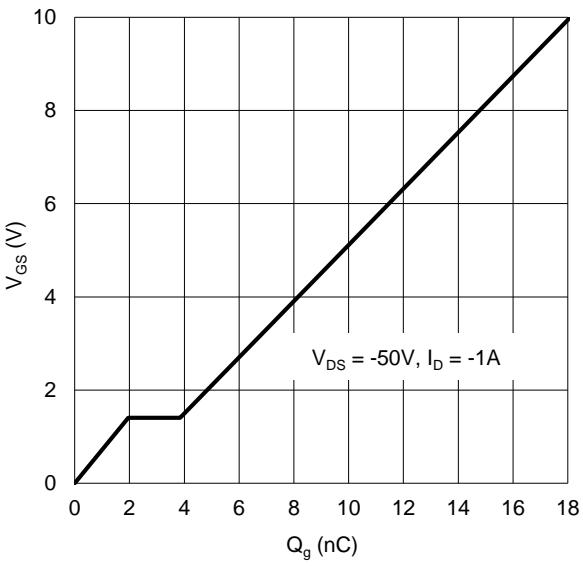


Figure 23. Gate Charge

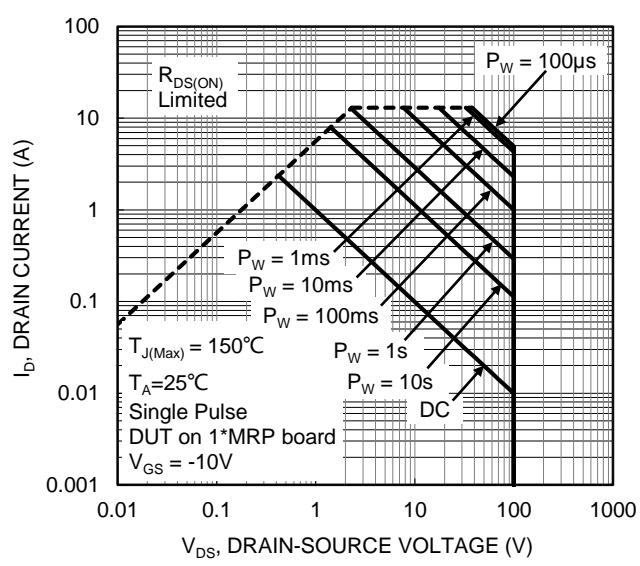


Figure 24. SOA, Safe Operation Area

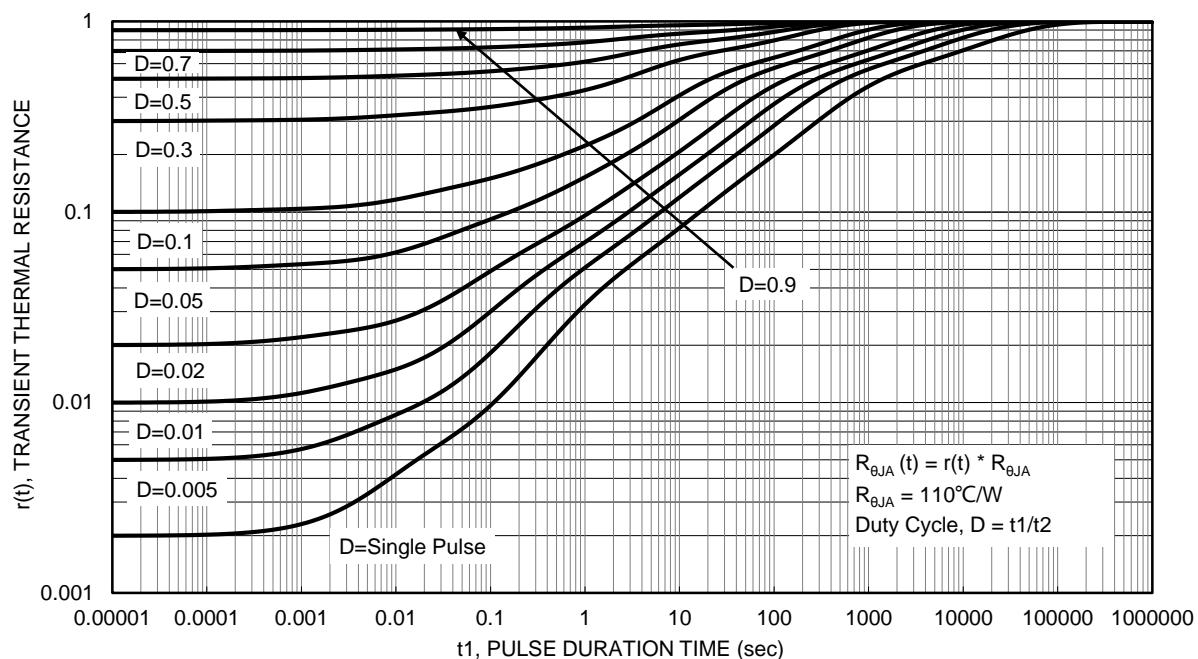
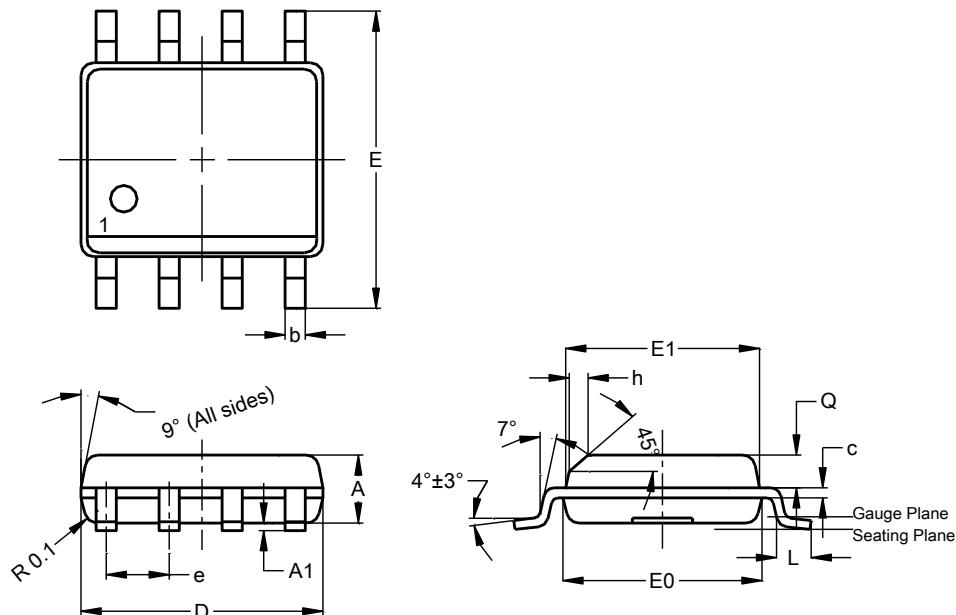


Figure 25. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8



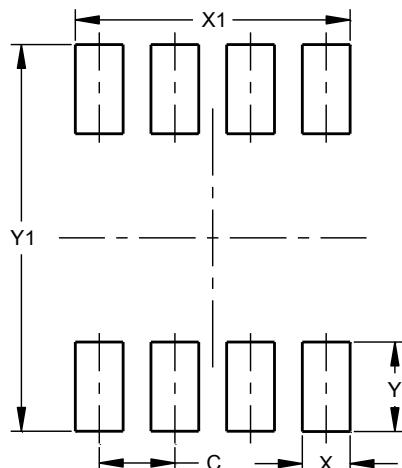
SO-8			
Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	--	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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