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## ISD15D00

				ISD1	5D00			
tasheet	ISD15D00.pdf							
	integrated analogical playback for a two	s a digital ChipCorde og/digital audio signal vo-chip solution. The g frequency, and a sig	l paths. Th ISD15D00	ne ISD15D0 0 provides a	0 utilizes : n I 2S dig	serial flash me ital audio inter	emory to provide	non-volatile au
Description	The ISD15D00 can take digital audio data via I 2S or SPI interface. When I 2S input is selected, it will replace the analog audio inputs and will support sample rates of 32, 44.1 or 48 kHz depending upon clock configuration. When SPI interface is chosen, the sample rate of the audio data sent must be one of the ISD15D00 supported sample rates.							
	The ISD15D00 has inbuilt analog audio inputs, analog audio line driver, and speaker driver output.							
	The analog audio input, Aux-in, has a fixed gain configured by SPI command. Aux-in can directly feed-through to the							
	analog outputs; it can also mix with the DAC output and then feed-through to the analog outputs. Analog outputs are available in two forms: (1) Aux-out is an analog single-ended voltage output; (2) Class-AB BTL							
		d) is an analog differe						
	Class-D PWMd	lirect-drive is also ava	ilable, whi	ich delivers	1-watt ou	tput power at \	/CCSPK=5V	
agram		N C +	anagement mmand	Digital Signal Pa Digital Filters Resampling Volume Control De-Compression Flash Memor Controller	n n	SUM2 +		UXQUD O AUXQUT
	External Memory. The ISD15D00 supports the following flash:							
	Manufacturer	Winbond		Numonyx		MXIC		
	Family	25X 25Q	25P	25PX	25PE			
	JEDEC ID	EF 30 1X EF 40 1X	20 20 1X	20 71 1X		25L / 25V C2 20 1X		
eatures	The addres ADPCM Inbuilt 3V Fast Digital Programm MemoryMa Store pre- Use a sin Execute p and play! Sample Ra Seven sal frequencia sample Ra Seven sal frequencia sample Ra Compressi For 12S of scaling ac Compressi For Pre-R µ-Law, I best poo Oscillator Internal of 12S bit do Inputs Aux-in: An Outputs PVMt Cla Deliver Class-AB D	essing ability of ISD13 voltage regulator to p Programming ming rate can go up to nagement -recorded audio (Voic nple index-based con ore-programmed mac back Voice Prompts site mpling frequencies a es of 4, 5.3, 6.4, 8, 12 ate. peration, 32, 44.1 and coordingly. on Algorithms teccorded Voice Prompt Differential µ-Law, PC ssible compression g scillator with internal bck input talog input with 2-bit g ssillator with internal bck input talog input with 2-bit g I-watt output power a BTL: an analog differ 1-watt output power a BTL can drive an 8 ace: MISO, MOSI, SCL ace: 12S_CLK, 12S_N ns: can trigger Voice Mac e Control set by SPI of /oltage: 2.7 ~ 5.5V Irrent: 1uA typical L-LQFP re Options:	5000 is up rovide pov o 1Mbits/s e Prompts nmand for ro scripts equences re availab d.8, 16 and d.48kHz m ots M, Enhan given a me reference: gain contro o directly of t V CCSPI ive an 8Ω: Ω speakei K, SSB fo NS, 12S_S ro for a pu	p to 128Mbi wer source econd main s) using hig r playback (Voice Mac s. de for a give d 32kHz are haster samp aced ADPCI etric of SNF child a source bl configure drive an 8 $\Omega$ K = 5V output age output K = 5V speaker or r or buzzer or command SDI, I 2S_SI ishbutton a	20 80 1X t, which is to the exten ily limited in quality of ros) desig available of rates a A Variable and back cillator with d by SPI of speaker of buzzer <i>i</i> a an exten ls and dig DO for digi oplication	C2 20 1X 64-minute pla mal flash mer by the flash mer ligital compres ned to control sample rate. F when the devic re available wi -bit-rate optim ground noise l th external resi ommand or buzzer	nory emory write rate ssion the configuration for example, the ce is clocked at a ith playback sam ized compression levels. istor, External cr	n of the device sampling a 32kHz master npling frequenci
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Audio Enhancement Foundry Service

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