Click here for production status of specific part numbers.

MAX17505EVKITBE# Evaluation Kit

Evaluates: MAX17505 in 5V Output-Voltage Application

General Description

The MAX17505EVKITBE# evaluation kit (EV kit) provides a proven design to evaluate the MAX17505 high-voltage, high-efficiency, synchronous step-down DC-DC converter. The EV kit is preset for 5V output at load currents up to 1.7A and features a 500kHz switching frequency for optimum efficiency and component size. The EV kit features adjustable input undervoltage lockout, adjustable soft-start, open-drain $\overline{\text{RESET}}$ signal, and external clock synchronization. The EV kit also provides a good layout example, which is optimized for conducted, radiated EMI, and thermal performance. For more details about the IC benefits and features, refer to the MAX17505 data sheet.

Features

- Operates from a 6.5V to 60V Input Supply
- Programmed 5V Output Voltage, 1.7A Load Current
- 500kHz Switching Frequency
- Enable/UVLO Input, Resistor-Programmable UVLO Threshold
- Adjustable Soft-Start Time
- MODE Pin to Select Among PWM, PFM, or DCM Modes
- Open-Drain $\overline{\text{RESET}}$ Output
- External Clock Synchronization
- Overcurrent and Overtemperature Protection
- Proven PCB Layout
- Fully Assembled and Tested
- Complies with CISPR22(EN55022) Class B Conducted and Radiated Emissions

***Ordering Information* appears at end of data sheet.**

Quick Start

Recommended Equipment

- MAX17505EVKITBE#
- 6.5V to 60V, 4A DC input power supply
- Load capable of sinking 2A
- Digital voltmeter (DVM)

Equipment Setup and Test Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify the board operation.

Caution: Do not turn on power supply until all connections are completed.

1) Set the power supply at a voltage between 6.5V and 60V. Disable the power supply.
2) Connect the positive terminal of the power supply to the VIN PCB pad and the negative terminal to the nearest PGND PCB pad. Connect the positive terminal of the 1.7A load to the VOUT PCB pad and the negative terminal to the nearest PGND PCB pad.
3) Connect the DVM across the VOUT PCB pad and the nearest PGND PCB pad.
4) Verify that shunts are installed across pins 1-2 on jumper JU1 and pins 2-3 on jumper JU3 (see Table 1 and Table 3 for details).
5) Select the shunt position on jumper JU2 according to the intended mode of operation (see Table 2 for details).
6) Turn on the DC power supply.
7) Enable the load.
8) Verify that the DVM displays 5V.

319-100343; Rev 0; 4/19

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Detailed Description

MAX17505EVKITBE# provides a proven design to evaluate the MAX17505 high-voltage, high-efficiency, synchronous step-down DC-DC converter. The EV kit is preset for 5V output from 6.5V to 60V input at load currents up to 1.7A and features a 500kHz switching frequency for optimum efficiency and component size.

The EV kit includes an EN/UVLO PCB pad and jumper JU1 to enable the output at a desired input voltage. The SYNC PCB pad and jumper JU3 allow an external clock to synchronize the device. Jumper JU2 allows the selection of a particular mode of operation based on light-load performance requirements. An additional $\overline{\text{RESET}}$ PCB pad is available for monitoring whether the converter output is in regulation.

Soft-Start Input (SS)

The EV kit offers an adjustable soft-start function to limit inrush current during startup. The soft-start time is adjusted by the value of external soft-start capacitor (C10) connected between SS and SGND. The selected output capacitance (C_{SEL}) and the output voltage (V_{OUT}) determine the minimum value of C10, as shown by the following equation:

$$C10 \geq 28 \times 10^{-6} \times C_{SEL} \times V_{OUT}$$

The soft-start time (t_{SS}) is related to C10 by the following equation:

$$t_{SS} = C10/(5.55 \times 10^{-6})$$

For example, to program a 1ms soft-start time, C10 should be 5.6nF.

Enable/Undervoltage-Lockout (EN/UVLO) Programming

The MAX17505 offers an Enable and adjustable input undervoltage lockout feature. In this EV kit, for normal operation, leave the EN/UVLO jumper (JU1) open. When JU1 is left open, the MAX17505 is enabled when the input voltage rises above 6.4V. To disable MAX17505, install a jumper across pins 2–3 on JU1. See Table 1 for JU1 settings. The EN/UVLO PCB pad on the EV kit supports external Enable/Disable control of the device. Leave JU1 open when external Enable/Disable control is desired. A potential divider formed by R1 and R2 sets the input voltage (V_{INU}) above which the converter is enabled when JU1 is left open.

Choose R1 to be 3.32MΩ (max), and then calculate R2 as follows:

$$R_2 = \frac{R_1 \times 1.215}{(V_{INU} - 1.215)}$$

where, V_{INU} is the voltage at which the device is required to turn on, and R1 and R2 are in kΩ,

For more details about setting the undervoltage lockout level, refer to the MAX17505 data sheet.

Mode Selection (MODE)

The EV kit provides a jumper (JU2) that allows the MAX17505 to operate in PWM, PFM, and DCM modes. Refer to the MAX17505 data sheet for more details on the modes of operation.

Table 2 shows the mode selection (JU2) settings that can be used to configure the desired mode of operation.

Table 1. Converter EN/UVLO Jumper (JU1) Settings

SHUNT POSITION	EN/UVLO PIN	MAX17505 OUTPUT
1-2*	Connected to VIN	Enabled
Not installed	Connected to the center node of resistor-divider R1 and R2	Enabled, UVLO level set through the R1 and R2 resistors
2-3	Connected to SGND	Disabled

**Default position.*

Table 2. Mode Selection (JU2) Settings

SHUNT POSITION	MODE PIN	MAX17505 MODE
Not installed*	Unconnected	PFM mode of operation
2-3	Connected to SGND	PWM mode of operation
1-2	Connected to VCC	DCM mode of operation

**Default position.*

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External Clock Synchronization (SYNC)

The EV kit provides a SYNC PCB pad to synchronize the MAX17505 to an optional external clock. Leave Jumper (JU3) open when external clock signals are applied. In the presence of a valid external clock for synchronization, the MAX17505 operates in PWM mode only. For more details about external clock synchronization, refer to the MAX17505 data sheet.

Active-Low, Open-Drain Reset Output (RESET)

The EV kit provides a $\overline{\text{RESET}}$ PCB pad to monitor the status of the converter. $\overline{\text{RESET}}$ goes high when VOUT rises above 95% (typ) of its nominal regulated output voltage. $\overline{\text{RESET}}$ goes low when VOUT falls below 92% (typ) of its nominal regulated voltage.

Table 3. External Clock Synchronization (JU3) Settings

SHUNT POSITION	SYNC PIN	MAX17505 SYNC
1-2	Connected to test loop on PCB	Frequency can be synchronized with an external clock
2-3*	Connected to SGND	SYNC feature unused

**Default position.*

Hot Plug-In and Long Input Cables

The MAX17505EVKITBE# PCB layout provides an optional electrolytic capacitor (CIN4 = 68µF/100V).

This capacitor limits the peak voltage at the input of the MAX17505 when the DC input source is “Hot-Plugged” to the EV kit input terminals with long input cables. The equivalent series resistance (ESR) of the electrolytic capacitor dampens the oscillations caused by interaction of the inductance of the long input cables, and the ceramic capacitors at the buck converter input.

Electromagnetic Interference (EMI)

Compliance to conducted emissions (CE) standards requires an EMI filter at the input of a switching power converter. The EMI filter attenuates high-frequency currents drawn by the switching power converter and limits the noise injected back into the input power source.

The MAX17505EVKITBE# PCB has designated footprints for the placement of conducted EMI filter components as per the optional Bill of Material (BOM). Use of these filter components results in lower conducted EMI below CISPR22 Class B limits. Remove the 0Ω resistor, which is placed on the L1 footprint before installing conducted EMI filter components. The MAX17505EVKITBE# EV kit PCB layout is also designed to limit radiated emissions from switching nodes of the power converter, resulting in radiated emissions below CISPR22 Class B limits.

Component Suppliers

SUPPLIER	WEBSITE
Coilcraft, Inc.	www.coilcraft.com
Murata Americas	www.murata.com
Panasonic Corp.	www.panasonic.com
SullinsCorp	www.sullinscorp.com
Taiyo Yuden	www.ty-top.com
Vishay Dale	www.vishay.com

***Note:** Indicate that you are using the MAX17505 when contacting these component suppliers.*

Ordering Information

PART	TYPE
MAX17505EVKITBE#	EV Kit

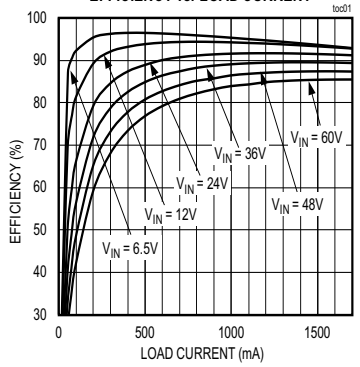
#Denotes RoHS compliant.

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EV Kit Performance Report

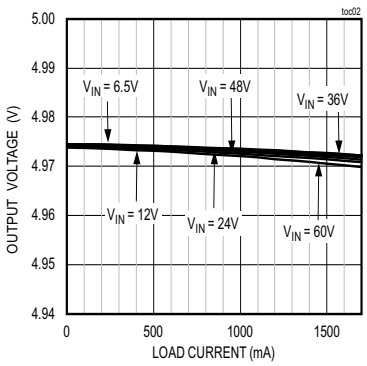
(V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 1.7A, f_{SW} = 500kHz, T_A = +25°C, unless otherwise noted.)

EFFICIENCY vs. LOAD CURRENT



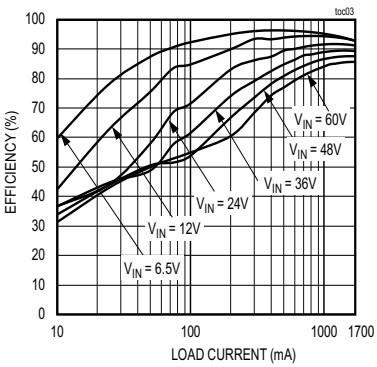
CONDITIONS: PWM MODE

LOAD AND LINE REGULATION



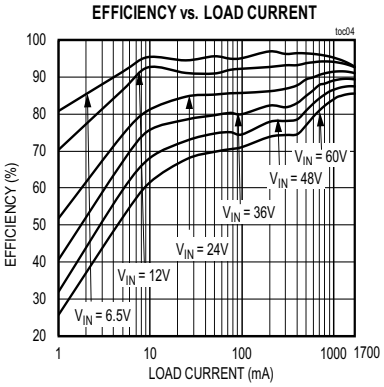
CONDITIONS: PWM MODE

EFFICIENCY vs. LOAD CURRENT



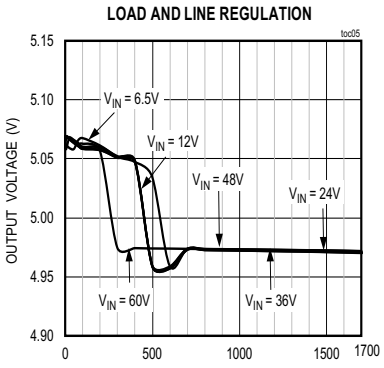
CONDITIONS: DCM MODE

EFFICIENCY vs. LOAD CURRENT



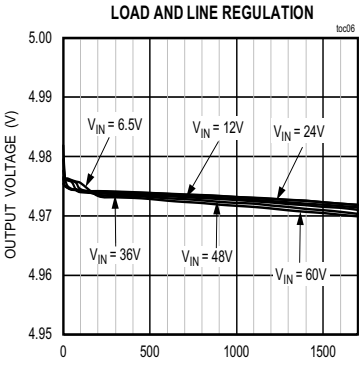
CONDITIONS: PFM MODE

LOAD AND LINE REGULATION



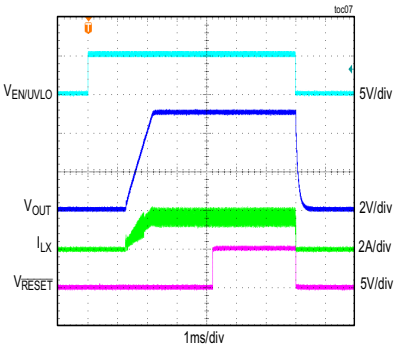
CONDITIONS: PFM MODE

LOAD AND LINE REGULATION



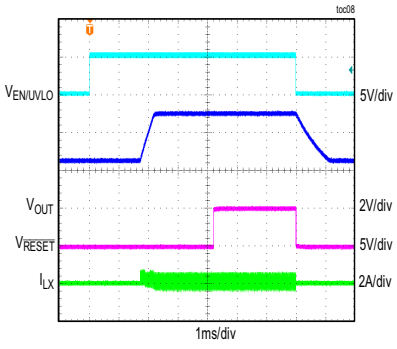
CONDITIONS: DCM MODE

SOFT-START / SHUTDOWN THROUGH EN/UVLO



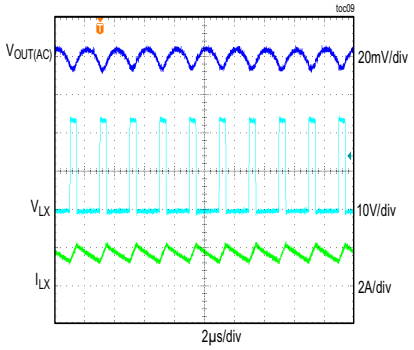
CONDITIONS: PWM MODE, 1.7A LOAD

SOFT-START WITH 2.5V PREBIAS VOLTAGE



CONDITIONS: PWM MODE, 25mA LOAD

STEADY-STATE PERFORMANCE



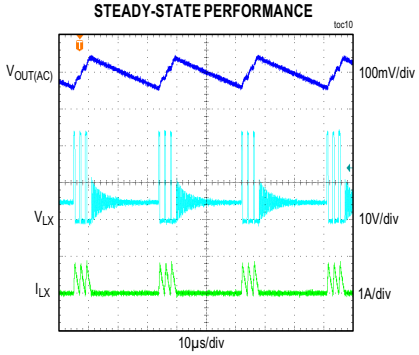
CONDITIONS: PWM MODE, 1.7A LOAD

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EV Kit Performance Report (continued)

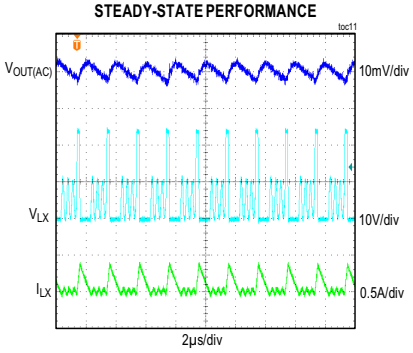
(V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 1.7A, f_{SW} = 500kHz, T_A = +25°C, unless otherwise noted.)

STEADY-STATE PERFORMANCE



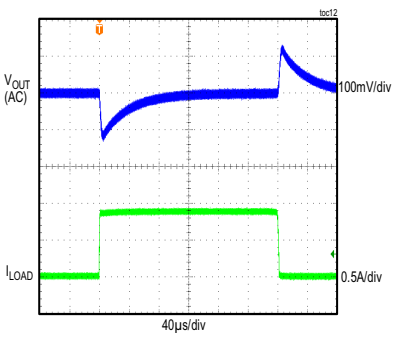
CONDITIONS: PFM MODE, 25mA LOAD

STEADY-STATE PERFORMANCE



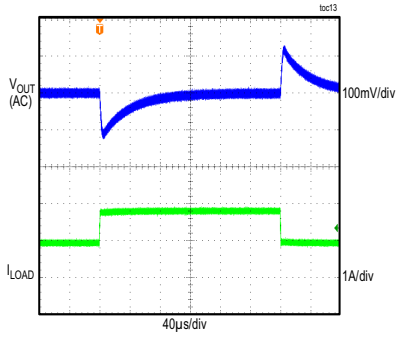
CONDITIONS: DCM MODE, 25mA LOAD

LOAD TRANSIENT RESPONSE BETWEEN 0A AND 0.85A



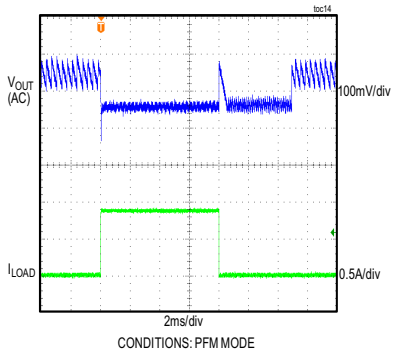
CONDITIONS: PWM MODE

LOAD TRANSIENT RESPONSE BETWEEN 0.85A TO 1.7A



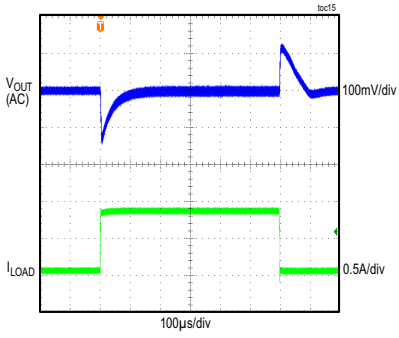
CONDITIONS: PWM MODE

LOAD TRANSIENT RESPONSE BETWEEN 5mA TO 0.85A



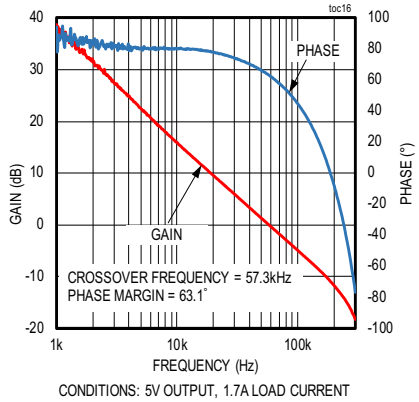
CONDITIONS: PFM MODE

LOAD TRANSIENT RESPONSE BETWEEN 50mA TO 0.85A



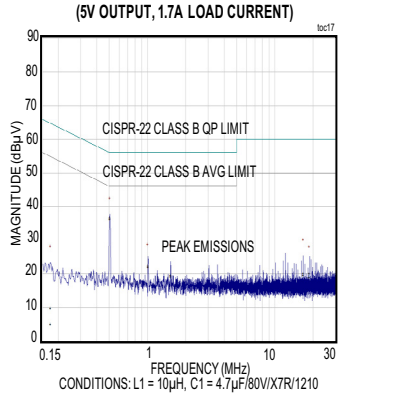
CONDITIONS: DCM MODE

BODE PLOT



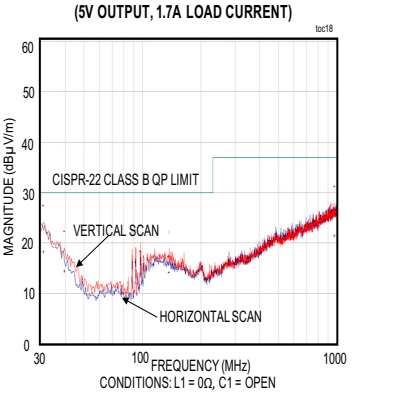
CONDITIONS: 5V OUTPUT, 1.7A LOAD CURRENT

CONDUCTED EMISSIONS PLOT
(5V OUTPUT, 1.7A LOAD CURRENT)



CONDITIONS: L1 = 10µH, C1 = 4.7µF/80V/X7R/1210

RADIATED EMISSIONS PLOT
(5V OUTPUT, 1.7A LOAD CURRENT)



CONDITIONS: L1 = 0Ω, C1 = OPEN

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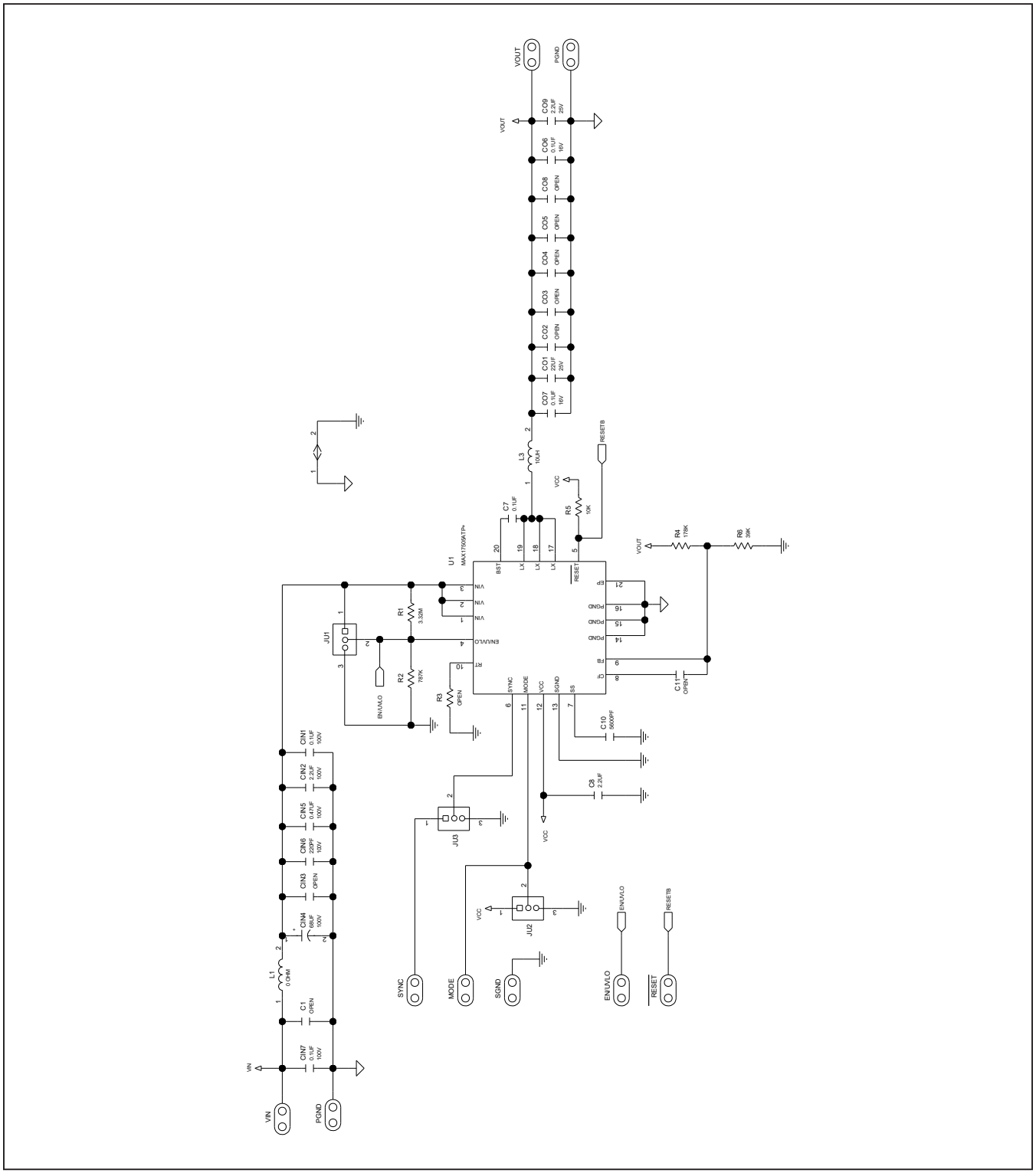
MAX17505EVKITBE# Bill of Materials

No.	DESIGNATOR	DESCRIPTION	QUANTITY	PART NUMBER
1	CIN1, CIN7	0.1µF, 10%, 100V, X7R, Ceramic capacitor (0603)	2	TAIYO YUDEN HMK107B7104KA-T
2	CIN2	2.2µF, 10%, 100V, X7R, Ceramic capacitor (1210)	1	MURATA GRM32ER72A225KA35
3	CIN4	ALUMINUM-ELECTROLYTIC; 68UF; 100V; TOL=20%; MODEL = EEV SERIES	1	PANASONIC EEV-FK2A680Q
4	CIN5	0.47µF, 10%, 100V, X7R, Ceramic capacitor (0805)	1	MURATA GRM21BR72A474KA73
5	CIN6	220pF, 5%, 100V, COG, Ceramic capacitor (0603)	1	TDK C1608C0G2A221J080AA
6	C8	2.2µF, 10%, 10V, X7R, Ceramic capacitor (0603)	1	MURATA GRM188R71A225KE15
7	C10	5600pF, 2%, 50V, COG, Ceramic capacitor (0402)	1	MURTA GRM1555C1H562GE01
8	CO1	22µF, 10%, 25V, X7R, Ceramic capacitor (1210)	1	MURATA GRM32ER71E226ME15
9	C7, CO6, CO7	0.1µF, 10%, 16V, X7R, Ceramic capacitor (0402)	3	TAIYO YUDEN EMK105B7104KV-F
10	CO9	2.2µF, 10%, 10V, X7R, Ceramic capacitor (0603)	1	MURATA GRM188R71A225KE15
11	L1	RES+, 0Ω, 1W (1812)	1	VISHAY RCA12180000Z0EKLS
12	L3	INDUCTOR, 10µH, 4.7A (10mm x 10mm)	1	COILCRAFT MSS1048-103ML
13	R1	RES+, 3.32MΩ, 1% (0402)	1	VISHAY DALE CRCW04023M32FK
14	R2	RES+, 787KΩ, 1% (0402)	1	VISHAY DALE CRCW0402787KFK
15	R4	RES+, 178KΩ, 1% (0402)	1	BOURNS CR0402-FX-1783GLF
16	R5	RES+, 10KΩ, 1% (0402)	1	VISHAY DALE CRCW040210K0FK
17	R6	RES+, 39KΩ, 1% (0402)	1	VISHAY DALE CRCW040239K0FK
18	U1	HIGH-EFFICIENCY; SYNCHRONOUS STEP-DOWN DC-DC CONVERTER; (TQFN20-EP 4mm x 4mm)	1	MAX17505ATP+
19	JU1-JU3	3-pin header (36-pin header 0.1" centers)	3	Sullins: PEC03SAAN
20	-	Shunts	3	SULLINS STC02SYAN
21	MH1-MH4	MACHINE SCREW; SLOTTED	4	EAGLE PLASTIC DEVICES P440.375
22	MH1-MH4	HEX STANDOFF #4-40 NYLON 3/8"	4	KEYSTONE ELECTRONICS 1902B
23	C1	OPTIONAL: 4.7µF, 10%, 80V, X7R, Ceramic capacitor (1210)	1	MURATA GRM32ER71K475KE14
24	L1	OPTIONAL: INDUCTOR, 10µH, 3.1A (4mm x 4mm)	1	COILCRAFT XAL4040-103ME
25	CO2, CIN3	OPEN: Capacitor (1210)	0	
26	CO3, CO4, CO5	OPEN: Capacitor (0805)	0	
27	CO8	OPEN: Capacitor (0603)	0	
28	C11	OPEN: Capacitor (0402)	0	
29	R3	OPEN: Resistor (0402)	0	

DEFAULT JUMPER TABLE	
JUMPER	SHUNT POSITION
JU1	1 - 2
JU2	1
JU3	2 - 3

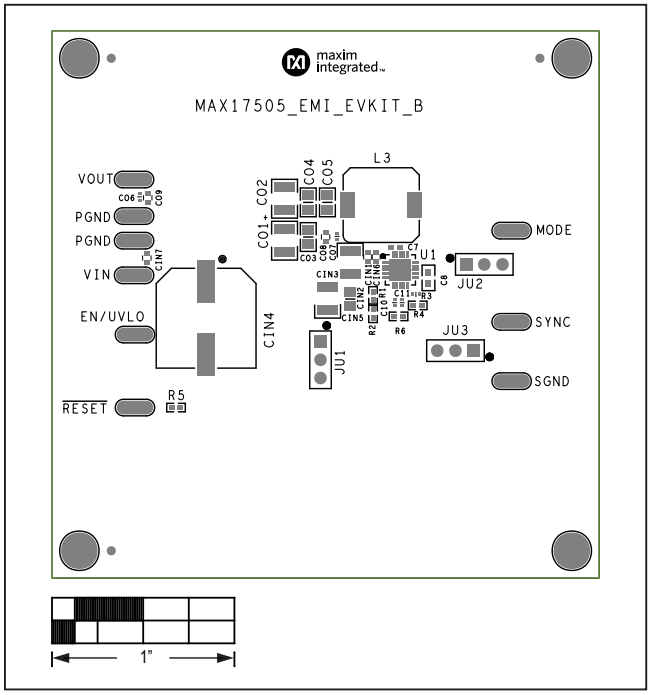
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MAX17505EVKITBE# Schematic

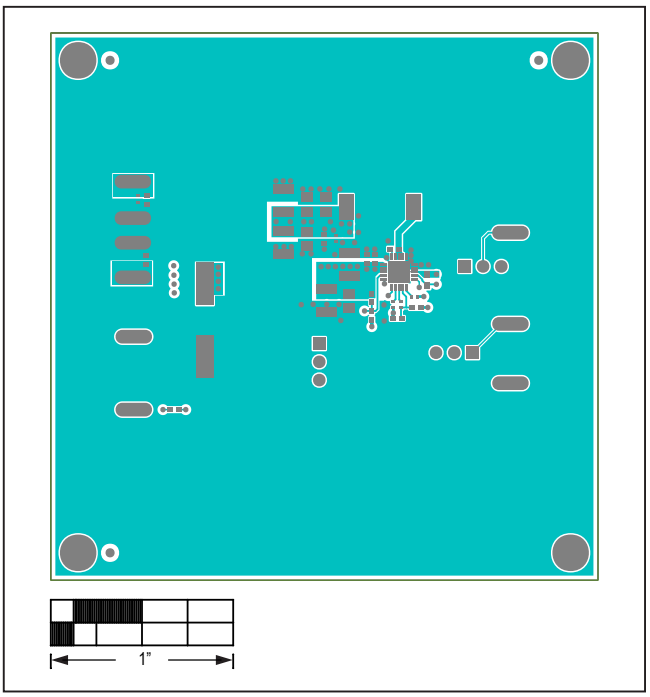


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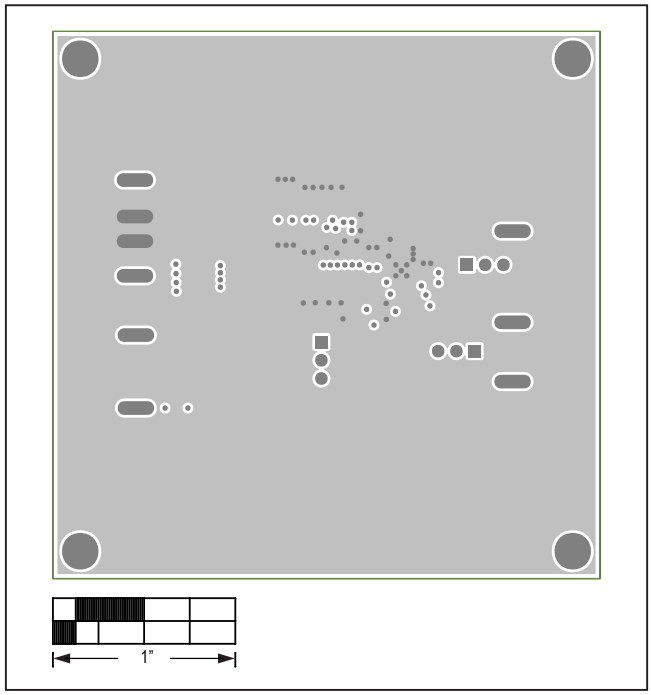
MAX17505EVKITBE# PCB Layout



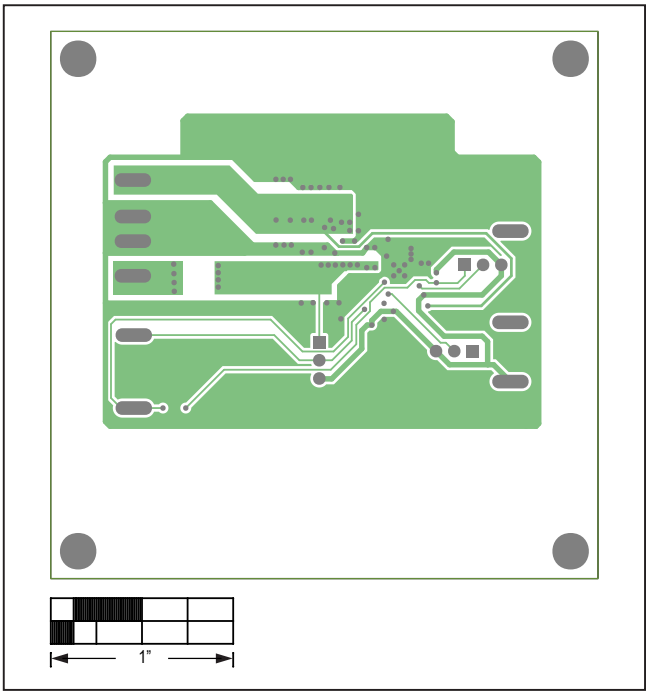
MAX17505EVKITBE# PCB Layout—Top Silkscreen



MAX17505EVKITBE# PCB Layout—Top Layer



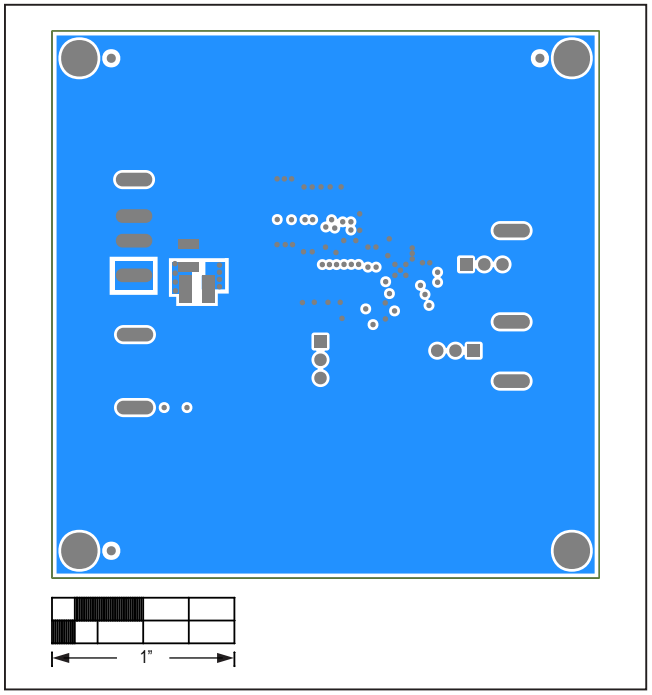
MAX17505EVKITBE# PCB Layout—Layer 2



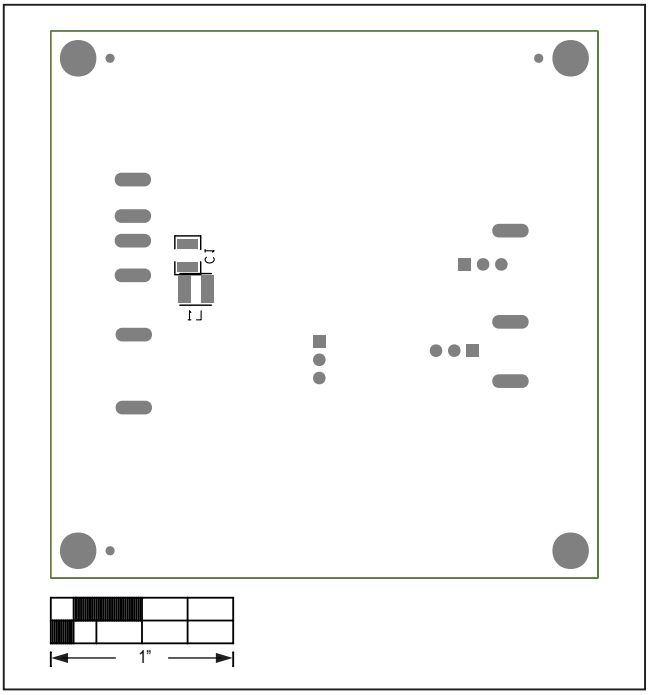
MAX17505EVKITBE# PCB Layout—Layer 3

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MAX17505EVKITBE# PCB Layout (Continued)



MAX17505EVKITBE# PCB Layout—Bottom Layer



MAX17505EVKITBE# PCB Layout—Bottom Silkscreen

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	4/19	Initial release	—

For pricing, delivery, and ordering information, please visit Maxim Integrated's online storefront at https://www.maximintegrated.com/en/storefront/storefront.html.

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