# MOSFET – Power, P-Channel, SOT-23 -20 V, -1 A

#### **Features**

- Ultra Low On–Resistance Provides Higher Efficiency and Extends Battery Life
  - $R_{DS(on)} = 0.180 \ \Omega, \ V_{GS} = -10 \ V$  $R_{DS(on)} = 0.280 \ \Omega, \ V_{GS} = -4.5 \ V$
- Power Management in Portable and Battery-Powered Products
- Miniature SOT-23 Surface Mount Package Saves Board Space
- Mounting Information for SOT-23 Package Provided
- NVR Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

#### **Applications**

- DC-DC Converters
- Computers
- Printers
- PCMCIA Cards
- Cellular and Cordless Telephones

## **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	$V_{DSS}$	-20	V
Gate-to-Source Voltage - Continuous	V <sub>GS</sub>	±20	V
Drain Current - Continuous @ $T_A$ = 25°C - Pulsed Drain Current ( $t_p \le 1 \mu s$ )	I <sub>D</sub> I <sub>DM</sub>	-1.0 -2.67	Α
Total Power Dissipation @ T <sub>A</sub> = 25°C	$P_{D}$	400	mW
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	– 55 to 150	°C
Thermal Resistance; Junction-to-Ambient	$R_{\theta JA}$	300	°C/W
Maximum Lead Temperature for Soldering Purposes, (1/8" from case for 10 s)	TL	260	°C

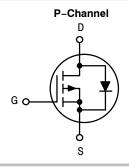
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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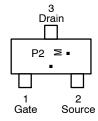
V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> TYP	I <sub>D</sub> MAX	
-20 V	148 mΩ @ –10 V	-1.0 A	



#### MARKING DIAGRAM/ PIN ASSIGNMENT



SOT-23 CASE 318 STYLE 21



P2 = Specific Device Code

M = Date Code = Pb-Free Package

(Note: Microdot may be in either location)

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NTR1P02T1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
NTR1P02T3G	SOT-23 (Pb-Free)	10000 / Tape & Reel
NVR1P02T1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				
Drain-to-Source Breakdown Voltage $(V_{GS}=0\ V,\ I_D=-10\ \mu A)$ (Positive Temperature Coefficient)	V <sub>(BR)DSS</sub>	-20	32		V mV/°C
Zero Gate Voltage Drain Current $ (V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 25^{\circ}\text{C}) $ $ (V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 150^{\circ}\text{C}) $	I <sub>DSS</sub>			-1.0 -10	μΑ
Gate-Body Leakage Current ( $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$ )	I <sub>GSS</sub>			±100	nA
ON CHARACTERISTICS (Note 1)					
Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = -250 \mu A)$ (Negative Temperature Coefficient)	V <sub>GS(th)</sub>	-1.1	-1.9 -4.0	-2.3	V mV/°C
Static Drain-to-Source On-State Resistance ( $V_{GS} = -10 \text{ V}$ , $I_D = -1.5 \text{ A}$ ) ( $V_{GS} = -4.5 \text{ V}$ , $I_D = -0.75 \text{ A}$ )	R <sub>DS(on)</sub>		0.148 0.235	0.180 0.280	Ω
DYNAMIC CHARACTERISTICS					
Input Capacitance $(V_{DS} = -5 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz})$	C <sub>iss</sub>		165		pF
Output Capacitance $(V_{DS} = -5 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz})$	C <sub>oss</sub>		110		
Reverse Transfer Capacitance $(V_{DS} = -5 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz})$	C <sub>rss</sub>		35		
SWITCHING CHARACTERISTICS (Note 2)					
Turn–On Delay Time $(V_{DD} = -15 \text{ V}, I_D = -1 \text{ A}, V_{GS} = -5 \text{ V}, R_G = 2.5 \Omega)$	<sup>t</sup> d(on)		7.0		ns
Rise Time $(V_{DD} = -15 \text{ V}, I_D = -1 \text{ A}, V_{GS} = -5 \text{ V}, R_G = 2.5 \Omega)$	t <sub>r</sub>		9.0		
Turn–Off Delay Time $(V_{DD}=-15~V,~I_{D}=-1~A,~V_{GS}=-5~V,~R_{G}=2.5~\Omega)$	t <sub>d(off)</sub>		9.0		
Fall Time $(V_{DD} = -15 \text{ V}, \text{ I}_{D} = -1 \text{ A}, \text{ V}_{GS} = -5 \text{ V}, \text{ R}_{G} = 2.5 \Omega)$	t <sub>f</sub>		3.0		
Total Gate Charge $(V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_D = -0.8 \text{ A})$	Q <sub>tot</sub>		2.5		nC
Gate-Source Charge $(V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_D = -0.8 \text{ A})$	$Q_{gs}$		0.75		
Gate-Drain Charge $(V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_D = -0.8 \text{ A})$	$Q_{gd}$		1.0		
BODY-DRAIN DIODE RATINGS (Note 1)		•		•	
Diode Forward On–Voltage (Note 2) $ (I_S = -0.6 \text{ A, V}_{GS} = 0 \text{ V}) $ $ (I_S = -0.6 \text{ A, V}_{GS} = 0 \text{ V, T}_J = 150^{\circ}\text{C}) $	V <sub>SD</sub>		-0.8 -0.6	-1.0	V
Reverse Recovery Time	t <sub>rr</sub>		13.5		ns
$(I_S = -1 \text{ A, } dI_S/dt = 100 \text{ A/}\mu\text{s, } V_{GS} = 0 \text{ V})$	ta		10.5		1
	t <sub>b</sub>		3.0		1
Reverse Recovery Stored Charge ( $I_S = -1 \text{ A}, \text{ d}I_S/\text{d}t = 100 \text{ A}/\mu\text{s}, \text{V}_{GS} = 0 \text{ V}$ )	Q <sub>RR</sub>		0.008		μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2%.

2. Switching characteristics are independent of operating junction temperature.

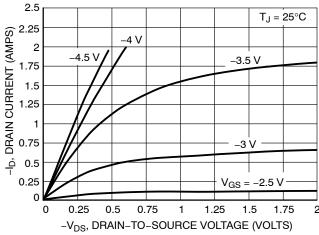


Figure 1. On-Region Characteristics

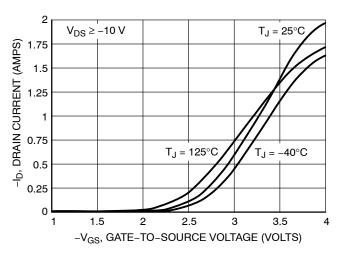


Figure 2. Transfer Characteristics

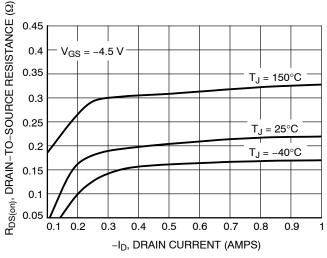


Figure 3. On-Resistance versus Drain Current and Temperature

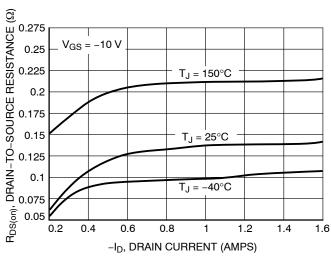


Figure 4. On-Resistance versus Drain Current and Temperature

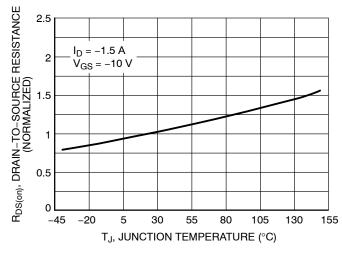


Figure 5. On–Resistance Variation with Temperature

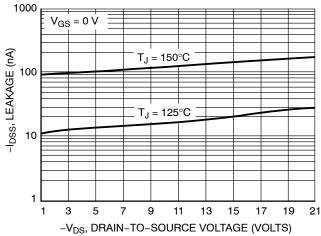


Figure 6. Drain-to-Source Leakage Current versus Voltage

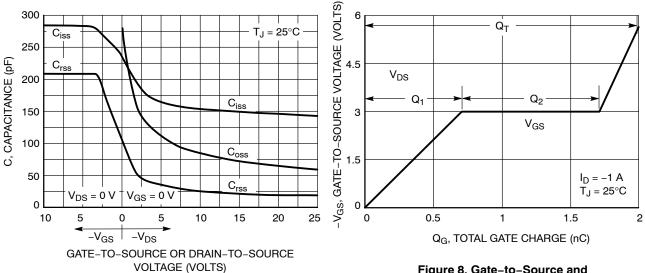


Figure 7. Capacitance Variation

Figure 8. Gate-to-Source and Drain-to-Source Voltage versus Total Charge

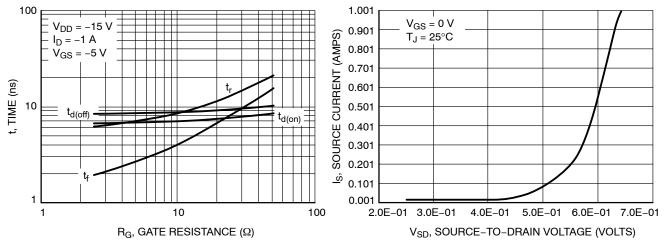


Figure 9. Resistive Switching Time Variation versus Gate Resistance

Figure 10. Diode Forward Voltage versus Current

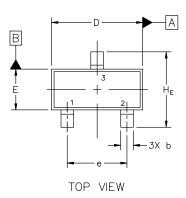


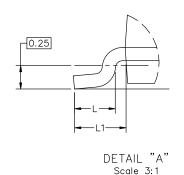


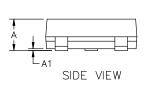
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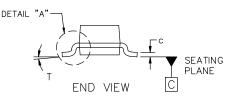
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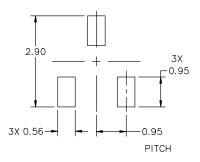
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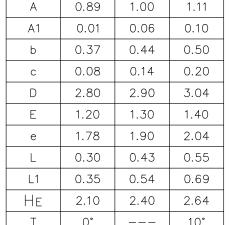












**MILLIMETERS** 

MIN

NOM

#### NOTES:

DIM

- DIMENSIONING AND TOLERANCING 1. PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS:
- MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

## **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

= Date Code

= Pb-Free Package

## RECOMMENDED MOUNTING FOOTPRINT

\* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **STYLES ON PAGE 2**

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<sup>\*</sup>This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

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DATE 14 AUG 2024

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR			
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	2. CATHODE 2.	2: STYLE 13: CATHODE PIN 1. SOURCE CATHODE 2. DRAIN ANODE 3. GATE	STYLE 14: PIN 1. CATHODE 2. GATE 3. ANODE
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	2. ANODE 2.	3: STYLE 19: NO CONNECTION PIN 1. CATHODE CATHODE 2. ANODE ANODE 3. CATHODE-ANODE	STYLE 20: PIN 1. CATHODE 2. ANODE 3. GATE
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT			STYLE 26: PIN 1. CATHODE 2. ANODE 3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE			

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