OSD DISPLAYS

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Customer:	
Model Number:	OSD0101T4317-71TS
Specification Type:	
Date:	08/30/2019
Version:	A1.3

For Customer's Acceptance

Approved by	Comments

Approved by	Reviewed by	Prepared by

Revision History

Revision	Date	Originator	Detail	Remarks
01	2019-06-25	Long Huang	First Issue	
02	2019-08-16	Long Huang	Modify TFT pin description	Page 15
03	2019-08-30	Long Huang	Update OSD logo	Cover page, Header

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1. General Description

This display module consists of a 10.1 inch 1280 RGB x 800, TFT a-Si Active Matrix Color LCD that is electronically and mechanically integrated. The TFT display is capable of displaying 16M colors. Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. The product consists with a full x-y mutual capacitance touch panel with associated controller, true multi-touch function is supported. The touch panel is glass to glass structure with an optical bonded hardened glass lens. The complete LCD and touch sensor assembly shall be RoHS compliant.

2. TFT Display Parameter

2.1. Module Parameter

Features	Details	Unit
Display Size(Diagonal)	10.1”	Inch
LCD type	α-Si TFT	
Display Mode	TN/Transmissive /Normally white	
Resolution	1280 RGB x 800	landscape
View Direction	Free	
Grayscale Inversion Direction	-	
Module Outline	251.96（H）×170.60（V）×8.94（T）	mm
Active Area	216.96（H）*135.60（V）	mm
Pixel Size	0.15(H)×0.15 (V)	mm
Pixel Arrangement	Stripe	
Polarizer Surface Treatment	Normal	
Display Colors	16.7M	
Interface	LVDS	

2.2. Touch Panel Parameter

Features	Details	Note
Application Size(Diagonal)	10.1”	Unit: Inch
Resolution	1280 x 800	Origin (0,0) is on left-top
Operation Technology	Projected capacitive	--
Input Method	Bare or gloved finger or thick conductive stylus	--
Number of Simultaneous Touches	**5**	Points
Touch Controller	MXT1066T2	Microchip product
Interface to Host	I2C	Maximum bus speed 400K Hz
I2C Address	0x4A	--
Optical Transmittance	>86%	--
Life of Touches	>10 million over lifetime	With correct input method
Connection Type	ZIF connector	--
Response Time/Speed	<20ms	--
Min. spacing between 2 touches	18	Unit: mm
Positional Accuracy	Center :±1.5mm ,edge : 2.5mm	5.5mm copper cylinder
Minimum Touch Area	30	Unit: mm2
Minimum Touch Pressure	0	Unit: N
FG Weight	TBD	g
Config/Firmware Version	TBD	--

3. Absolute Maximum Ratings

Ta=25±2ºC

Item	Symbol	Min.	Max.	Unit
Supply Voltage	3.3V(VDD)	-0.3	+3.6	V
Storage temperature	T_{STG}	-10	+60	ºC
Operating temperature	T_{OP}	-20	+70	ºC
Humidity	RH	-	90%(Max)	RH

Note1: If Ta below 50ºC, the maximum humidity is 90%RH, if Ta over 50ºC, maximum humidity should be less than 60%RH.

Note2: GND=0V, Ta=25ºC

4. DC Characteristics

4.1. TFT display DC characteristics

Ta=25±2ºC

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage (Note 1)	3.3V(VDD)	3.0	3.3	3.6	V
Logic Low input voltage	V_{IL}	GND	-	0.3*VDD	V
Logic High input voltage	V_{IH}	0.7*VDD	-	VDD	V
Logic Low output voltage	V_{OL}	GND	-	0.2*VDD	V
Logic High output voltage	V_{OH}	0.8*VDD	-	VDD	V
Current Consumption(Note 2)	$\mathbf{I_{VDD}}$	-	TBD	TBD	mA
Frame Frequency	f_{FR}	-	60	-	HZ

Note1: test condition is all on the still pattern and Ta is 25ºC, VCI=VDDI=3.3V (backlight current is not included). Value would be updated after samples build.

Note2: Excluding the LED current consumption , just LCD display part consumption

Note3: for OTP unit, VLCD output voltage is possibly out of the requirement, but it can still be acceptable as long as display performances meet the spec.

4.2. TP Panel DC Characteristics

Ta=25±2ºC

Parameter	Description	Min.	Typ.	Max.	Units	Notes
VDDD_TP	Operating limits	-	3.3	-	V	±5%, Note1
VDDA_TP	Operating limits	-	3.3	-	V	±5%, Note1
IDD_TP	Active	-	6.5	-	mA	VDD
	Sleep	-	TBD	-	uA	VDD
Vil	Low input logic level	-0.3	-	0.3Vddio	V	Vddio=3.3V
Vih	High input logic level	0.7Vddio	-	Vddio+0.3	V	
Vol	Low output logic level	-	-	0.3Vddio	V	
Voh	High output logic level	0.7Vddio	-	-	V	

Note : VDDA_TP must be stable and have a nominal tolerance in the host systerm of +/_ 5% or better

4.3. Backlight Characteristic

Ta=25±2ºC

Item	Symbol	Condition	Min	Typ	Max	Unit
Forward Voltage	V_f	Ta=25 ºC,IF=360mA	10.8	12.8	13.6	V
Forward Current	I_f	Ta=25 ºC, V_F=12.8V	-	360	-	mA
Power dissipation	P_d	Ta=25 ºC,I_F=360mA	-	4608	4896	mW
Drive method	Constant current 360mA					
Life time	>= 20,000 hrs					
LED Configuration	36 White LED ,4 in series ,9 in Parallel					

Note1: Test condition I_f =360mA, Ta=25ºC.

Note2 : The LED life time is defined as the module surface luminance decrease to the 50% of the original luminance when the ambient temperature is 25 ºC and the operating current is 360mA. The LED life time would decrease if operating current is larger than 360mA.

5. Optical Characteristics

The optical specification is valid for optimized LCD drive voltage, room temperature and the recommended initialization setting unless otherwise stated under the respective section.

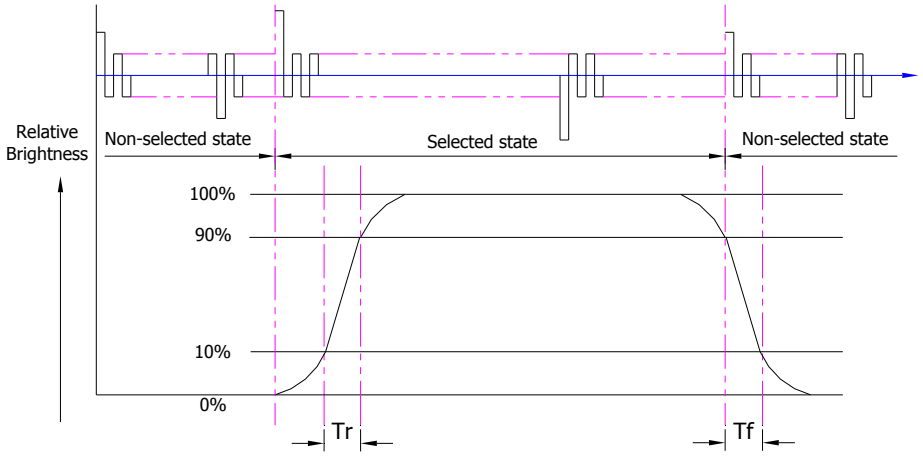
5.1. Optical Characteristics

Ta=25ºC, VDD=3.2V, TN LC+ Polarizer

	Item		Symbol	Condition	Specification			Unit
					Min.	Typ.	Max.	
Backlight On (Transmissive Mode)	Surface Luminance (On TP Lens Surface, I_f =60mA) (See 5.6)		*Lv*	θ= 0° Normal viewing angle	656	820		cd/m²
	Uniformity(CTP surface) (see 5.6)		Avg		65	-		%
	Contrast ratio(See 6.3)		*CR*		700	900		
	Response time (See 5.2)		*TR+TF*			30	-	ms
	Chromaticity Transmissive (See 5.5)	Red	*XR*			TBD		
			YR			TBD		
		Green	*XG*			TBD		
			YG			TBD		
		Blue	*XB*			TBD		
			YB			TBD		
		White	*XW*			TBD		
			YW			TBD		
	Viewing Angle (See 5.4)	Horizontal	θX+	Center CR≥10	-	80	-	Deg.
			θX-		-	80	-	
		Vertical	θY+		-	80	-	
			θY-		-	80	-	
	NTSC Ratio(Gamut)				-	TBD	-	%

5.2. Definition of Response Time

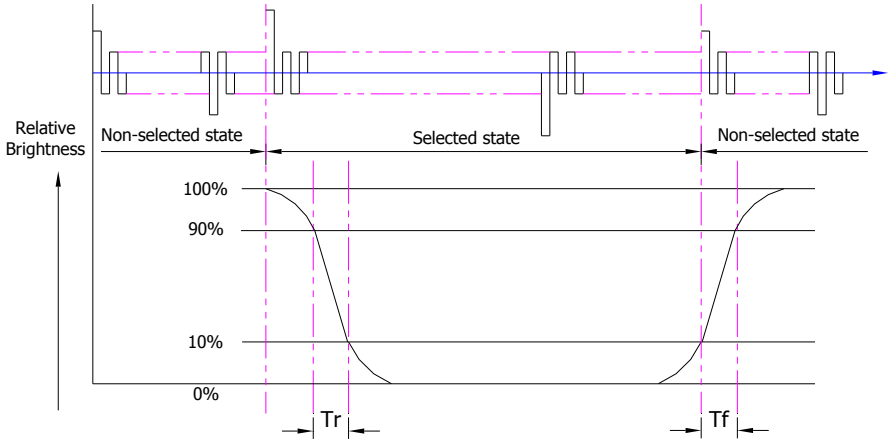
5.2.1. Normally Black Type (Negative)



Tr is the time it takes to change form non-selected stage with relative luminance 10% to selected state with relative luminance 90%;
Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note : Measuring machine: LCD-5100

5.2.2. Normally White Type (Positive)



Tr is the time it takes to change form non-selected stage with relative luminance 90% to selected state with relative luminance 10%;
Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note : Measuring machine: LCD-5100 or EQUI

5.3. Definition of Contrast Ratio

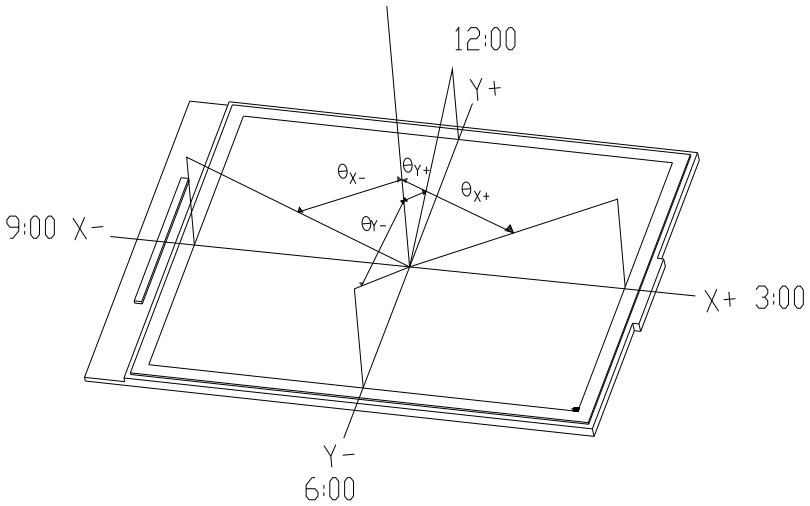
Contrast is measured perpendicular to display surface in reflective and transmissive mode.

The measurement condition is:

Measuring Equipment	BM-7 or Equivalent
Measuring Point Diameter	5mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white
	B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

5.4. Definition of Viewing Angles



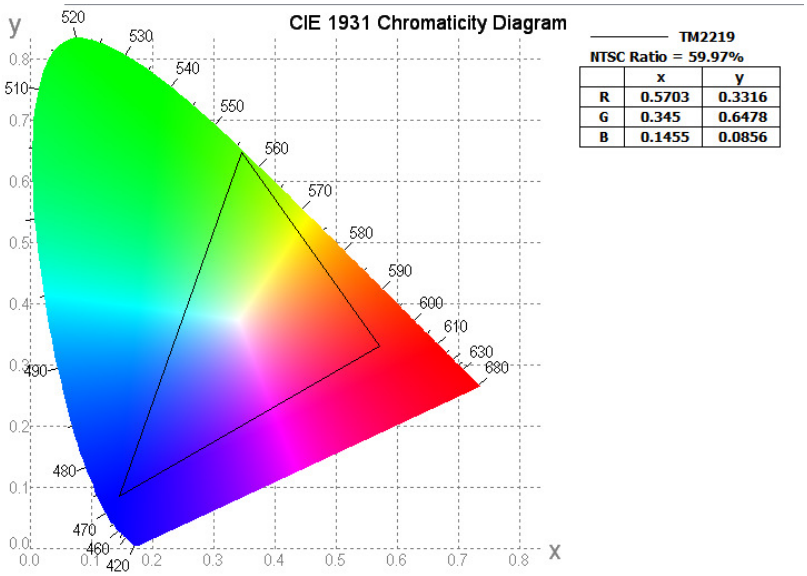
Measuring machine: LCD-5100 or EQUI

5.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7 OR EQUIVALENT)



5.6. Definition of Surface Luminance, Uniformity and Transmittance

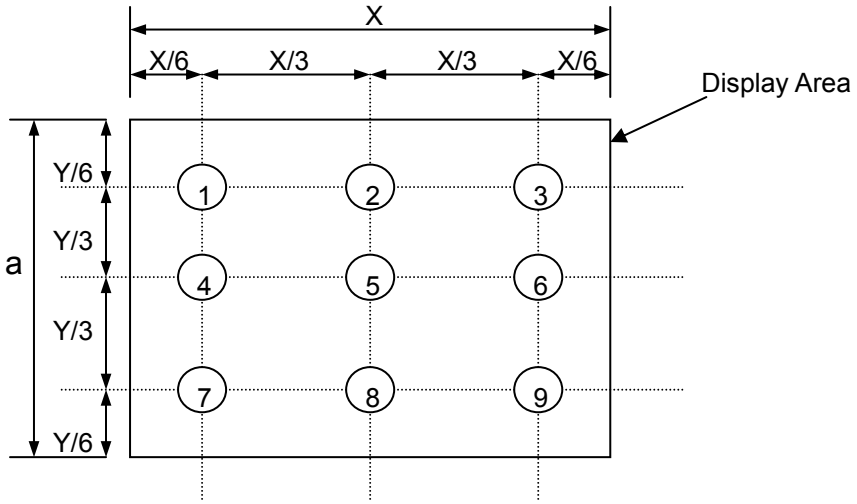
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

5.6.1. Surface Luminance: L_V = average (L_{P1}:L_{P9})

5.6.2. Uniformity = Minimal (L_{P1}:L_{P9}) / Maximal (L_{P1}:L_{P9}) * 100%

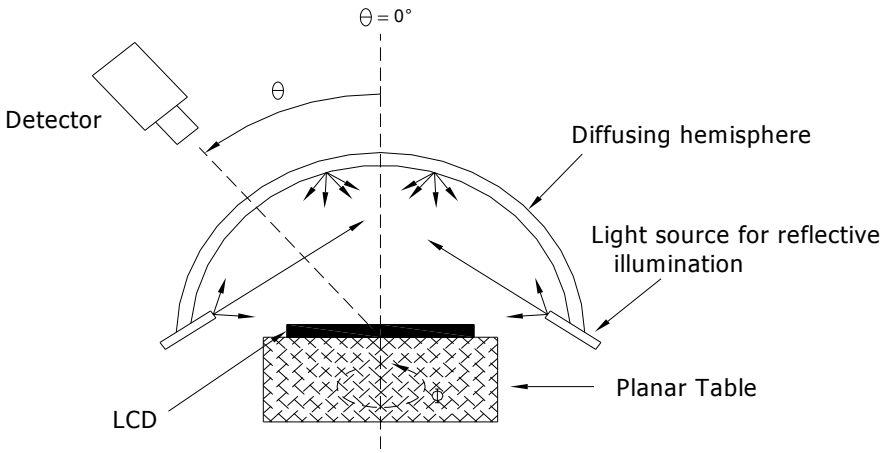
5.6.3. Transmittance = L_V on LCD / L_V on Backlight * 100%

Note : Measuring machine: BM-7 OR EQUIVALENT



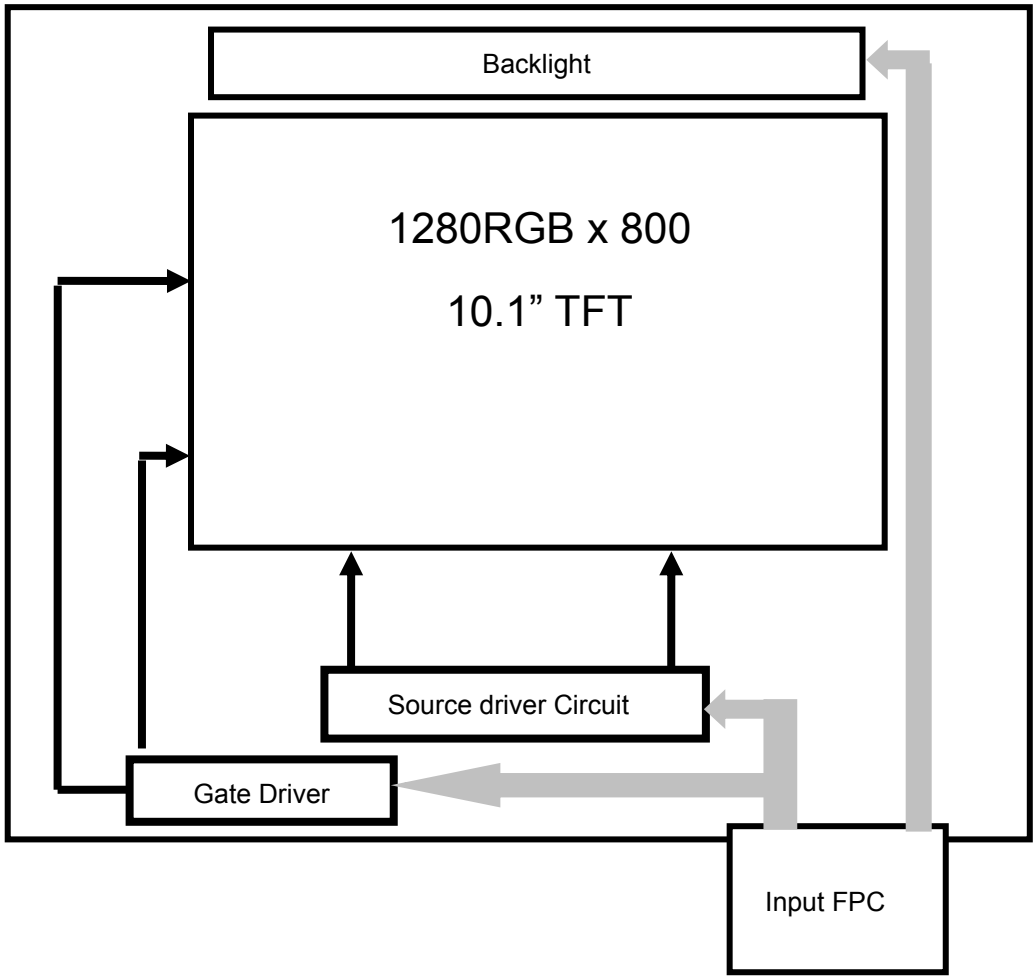
5.7. Definition of Reflectivity

To measure the reflectivity, the detector should be aligned to the normal direction of the LCD surface corresponding azimuthally angle θ=0°

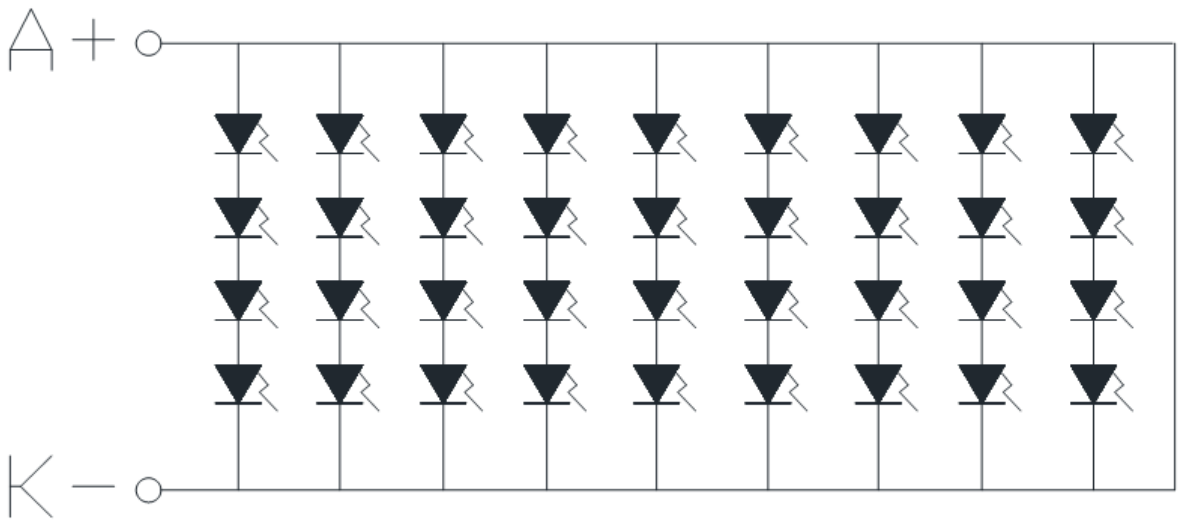


6. Block Diagram and Power Supply

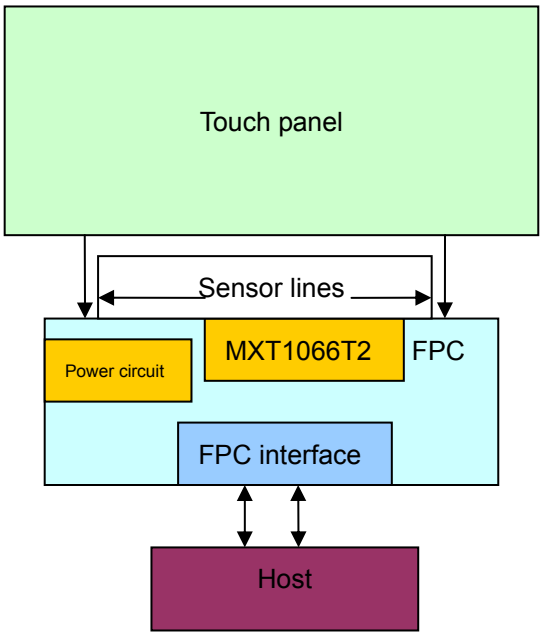
6.1. Block Diagram and Power Supply for Module



Backlight Diagram:



6.2. Block Diagram and Power Supply for TP



7. Interface Pins Definition

7.1. TFT Display Interface Pins Definition

No.	Symbol	I/O	Function
1	GND1	P	Ground
2	ID_CHK	NC	**No connect**
3	NC	NC	No connect
4~8	3.3V	P	Logic power 3.3V
9	WPN	NC	No connect
10	SCL	NC	(**No connect** if not use)Serial interface clock
11	SDA	NC	(**No connect** if not use)Serial interface date
12~14	GND2~GND4	P	Ground
15	RXIN3N	I	-LVDS differential data(R6,R7,G6,G7,B6,B7)
16	RXIN3P	I	+LVDS differential data(R6,R7,G6,G7,B6,B7)
17	GND5	P	Ground
18	LVDS_RX_N	I	-LVDS differential clock input
19	LVDS_RX_P	I	+LVDS differential clock input
20	GND6	P	Ground
21	RXIN2N	I	-LVDS differential data(B2~B5,HS,VS,DE)
22	RXIN2P	I	+LVDS differential data(B2~B5,HS,VS,DE)
23	GND7	P	Ground
24	RXIN1N	I	-LVDS differential data(G1~G5,B0~B1)
25	RXIN1P	I	+LVDS differential data(G1~G5,B0~B1)
26	GND8	P	Ground
27	RXIN0N	I	-LVDS differential data(R0~R5,G0)
28	RXIN0P	I	+LVDS differential data(R0~R5,G0)
29~30	GND9~GND10	P	Ground
31	NC	NC	No connect
32~35	LEDK	P	LED Power cathode
36~38	NC	NC	No connect
39~43	LEDA	P	LED Power anode
44	NC	NC	No connect
45	GND11	P	Ground

Note: I – Input ; O – Output ; P – Power/ground, C: Capacitor pin, NC:NO Connect

1. L/R：left or right setting U/D：up or down setting

7.2. Touch Panel Interface Pins Definition

Pin No.	Name	Type	Function Description
1	VDDA	P	Analog Power Supply 3.3V±5%.
2	GND	P	Ground connection.
3	I2CSDA	OD	Serial Interface Data. None pull-up resistor on CTP FPC, an external pull-up resistor is required, typical 3.3K to VDD.
4	I2CSCL	OD	Serial Interface clock. None pull-up resistor on CTP FPC, an external pull-up resistor is required, typical 3.3K to VDD.
5	NC		NC
6	INT	I	State change interrupt, INT line. None pull-up resistor on CTP FPC, an external pull-up resistor is required, typical 3.3K to VDD.
7	VDDD		Digital Power Supply 3.3V±5%.
8	XRES	OD	Reset low. None pull-up resistor on CTP FPC, an external pull-up resistor is required, typical 10K to VDD.

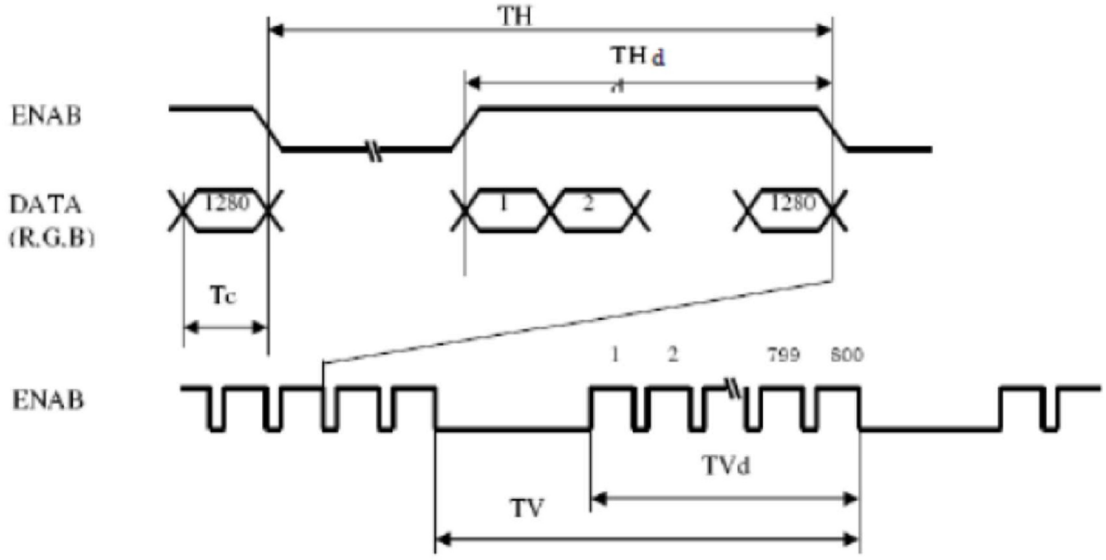
Note: P- Ground or Power, OD- open drain, I- Input, O – Output, NC- No Connection
For more information, refer to the datasheet of this controller IC.

8. AC Characteristics

8.1. TFT Display Timing

8.1.1. Timing Specification

Signal	Item	Symbol	Min	Type	Max	Unit
DCLK	Frequency	1/TC	60	65	70	MHz
	Cycle	Tc	16.66	15.38	14.3	ns
DE	Horizontal Period	THd	1280	1280	1280	Tc
	Horizontal Cycle	TH	1310	1330	1560	Tc
		TH_time	19.5	20.46	21.83	ns
	Vertical Period	TVd	800	800	800	Tc
	Vertical Cycle	TV	-	812	-	Tc

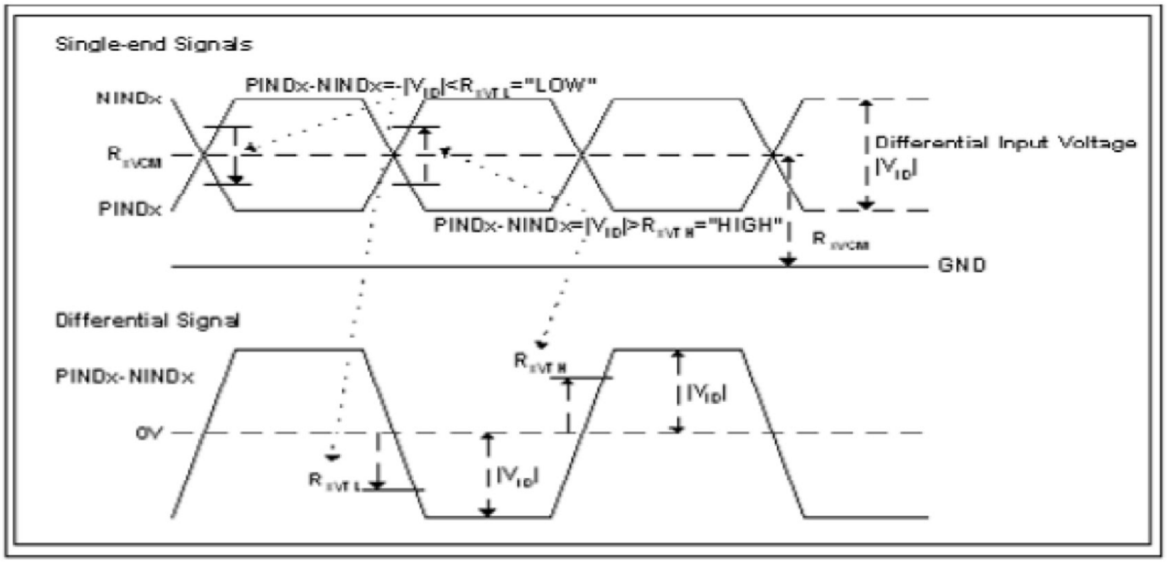


Interface Timing

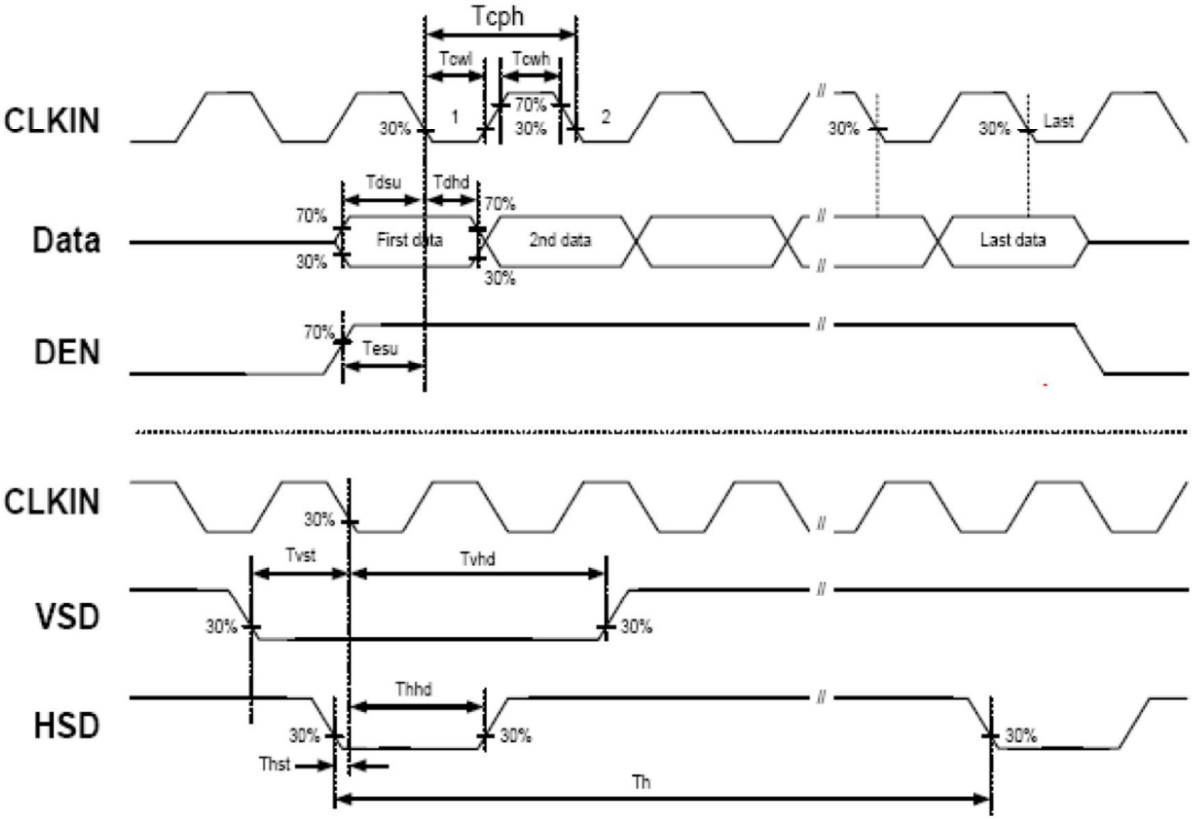
LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 6.

<Table 6. LVDS Rx Interface Timing Specification>



Input Clock and Data Timing Diagram



8.1.2. LVDS mode data input format

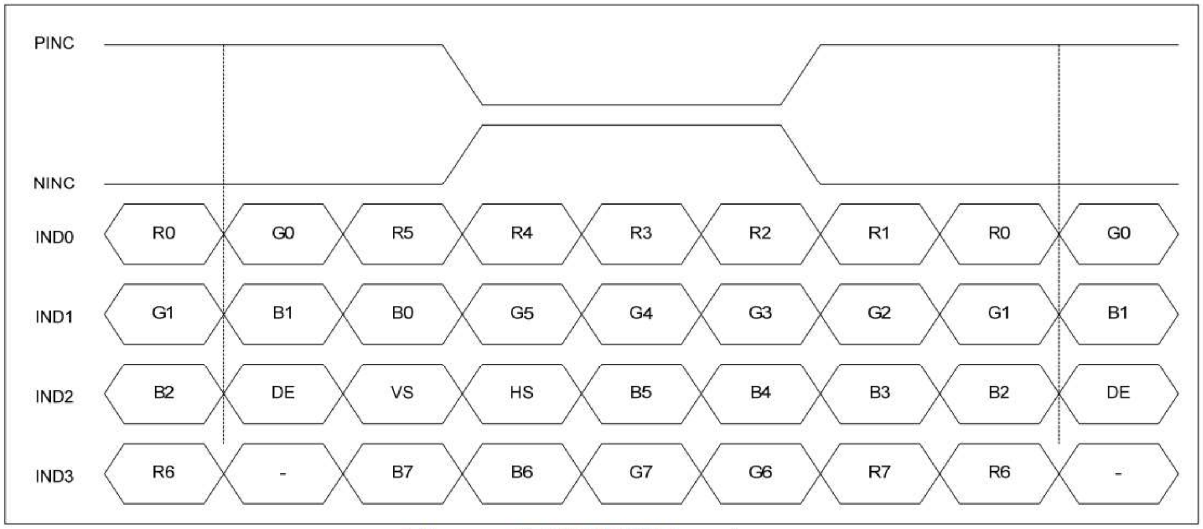


Figure : 8-bit LVDS Input

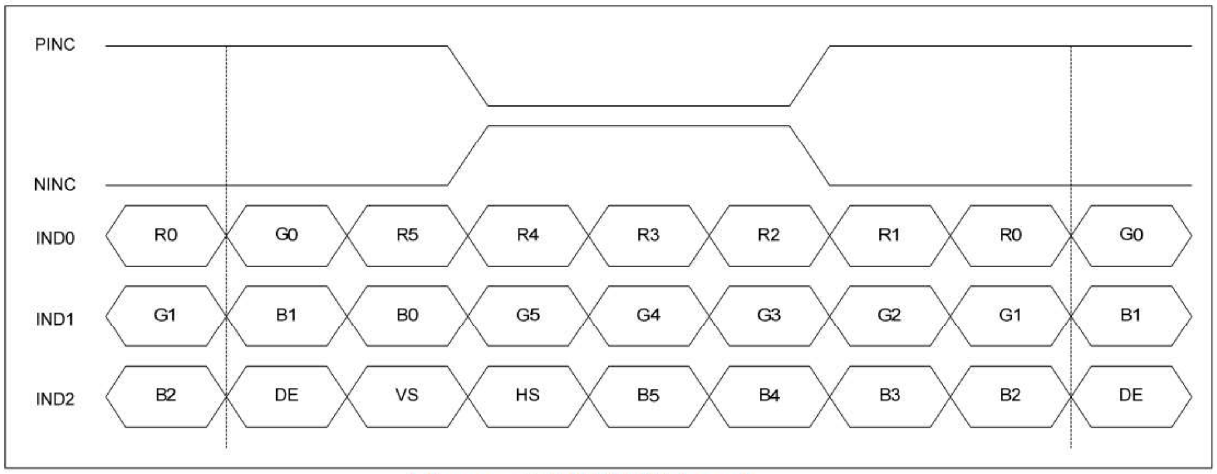
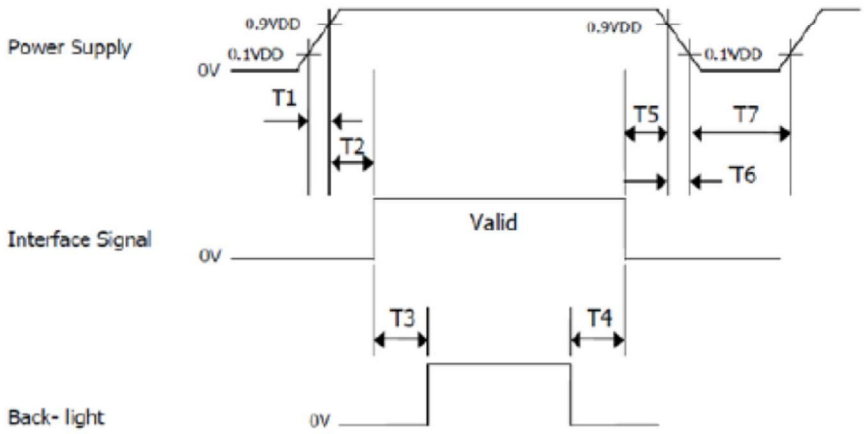


Figure : 6-bit LVDS input

8.1.3. Power Sequence

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below

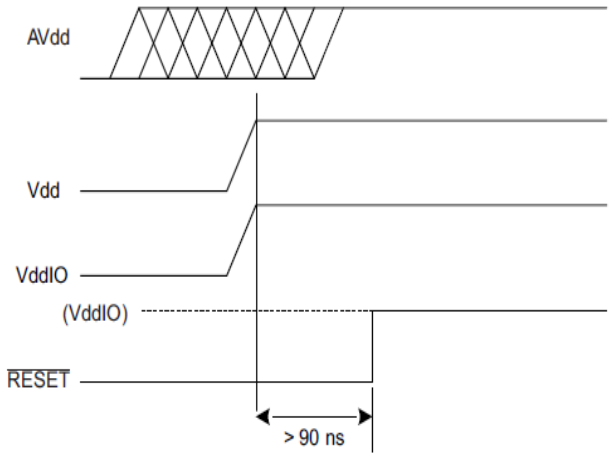
Power-On/Off Timing Sequence:



Parameter	Values			Units
	Min	Typ	Max	
T1	0	-	10	ms
T2	0	-	50	ms
T3	200	-	-	ms
T4	200	-	-	ms
T5	0.5	-	50	ms
T6	0	-	10	ms
T7	500	-	-	ms

8.2. Touch Panel Timing

8.2.1. Power Up / Reset Requirements



Note: When using external $\overline{\text{RESET}}$ at power-up,
VddIO must not be enabled after Vdd

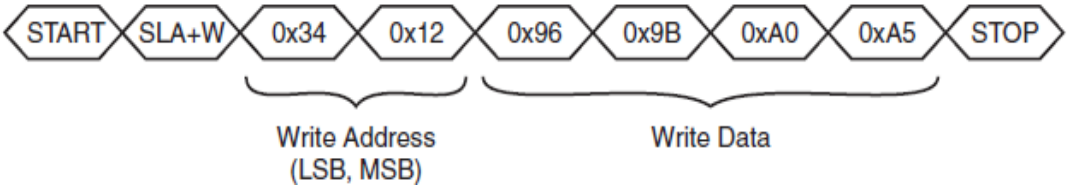
After power-up, the device takes 88 ms before it is ready to start communications.

Note: Any INT line activity before the power-on or reset period has expired should be ignored by the host. Operation of this signal cannot be guaranteed before the power-on/reset periods have expired.

8.2.2. Interface Bus

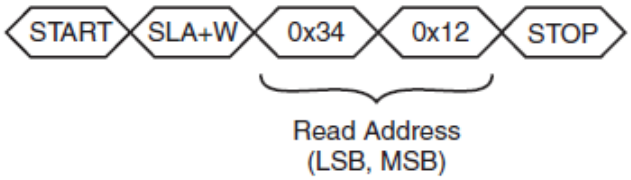
The Touch Panel communicates with the host over an I2C bus.
Please refer to http://www.i2c-bus.org for more detail about the I2C bus.
Please to https://github.com/atmel-maxtouch/linux for the Linux driver.

Example of a Four-byte Write Starting at Address 0x1234

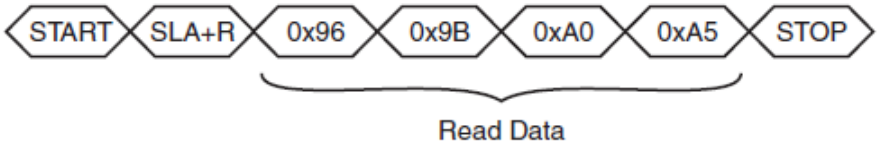


Example of a Four-byte Read Starting at Address 0x1234

Set Address Pointer



Read Data

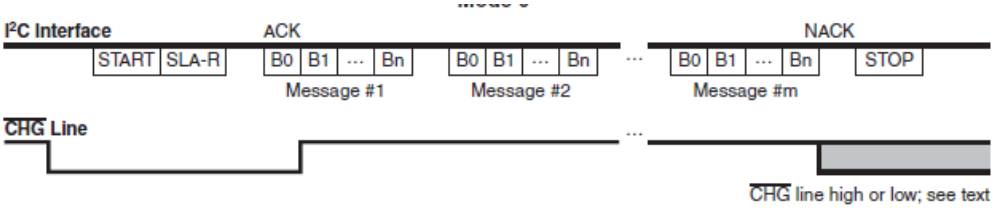


8.2.3. Touch event output

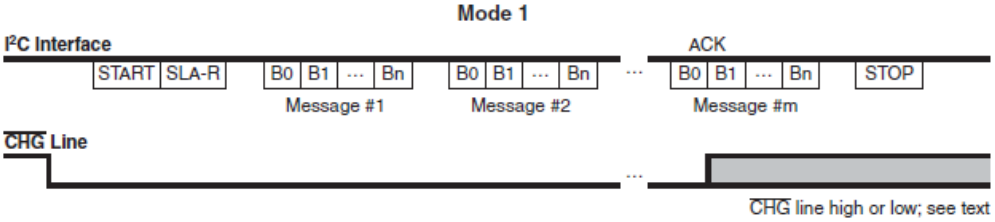
The INT line is an active-low, open-drain output that is used to alert the host that a new message is available in the Message Processor object. This provides the host with an interrupt-style interface with the potential for fast response times. It reduces the need for wasteful I2C-cmpatible communications.

The INT line remains low as long as there are messages to be read. The host should be configured so that the INT line is connected to an interrupt line that is level-triggered. The host should not use an edge-triggered interrupt as this means adding extra software precautions.

The INT line should be allowed to float during normal usage. This is particularly important after power-up or reset.



Mode 1



CHG Line = INT line

9. Recommended Setting and Initialization Flow for Reference

9.1. TFT Display, please reference to IC Datasheet

TBD

9.2. Touch Controller Setting

TBD (copy the config setting here) or N/A

10. Quality Assurance

10.1. Purpose:

The purpose of this specification is to establish the cosmetic standards for inspection and measurement of a NVD TFT & Touch Panel & Cover Glass Sub-Assembly.

10.2. Scope

This specification applies to all TFT & Touch Panels & Cover Glass Sub-Assemblies built by NVD and should be used as the inspection guideline for quality control. The individual drawing specification will have priority if this document conflicts with the drawing.

10.3. Standard for Quality Test:

NVD performs the following tests to ensure the quality of product before shipment.

10.3.1. Sampling Plan:

ANSI / ASQC Z1.4-2008.

General inspection level Ⅱ. Single sampling, normal inspection.

10.3.2. Sampling Criteria:

Visual inspection: AQL 1.0

Electrical functional: AQL 0.65

10.3.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

10.4. Nonconforming Analysis & Disposition

10.4.1. Nonconforming analysis:

10.4.1.1. Customer should provide overall information of non-conforming sample for their complaints.

10.4.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

10.4.1.3. If NVD can not finish the analysis on time, customer will be notified with the progress status.

10.4.2. Disposition of nonconforming:

Non-conforming product over ppm level, NVD will offer corrective actions, not over PPM, NVD can offer FA if customer need. And the failures are confirmed to be NVD responsibility and within the shelf life of 1 year, they will be replaced.

10.5. Agreement Items

NVD and customer shall negotiate if the following situation occurs:

10.5.1. There is any discrepancy in standard of quality assurance.

10.5.2. Additional requirement to be added in product specification.

10.5.3. Any other special problem.

10.6. Standard viewing conditions:

For the purpose of cosmetic inspection, all NVD touch panels are to be clean, dry and viewed under the following conditions:

10.6.1. Lighting: Daylight or cool white fluorescent lighting approximately from 70 to 140 foot-candles (approximately equal to 750 to 1500 Lux). Lighting should be diffused so that shadow is not a factor.

10.6.2. Distance: 12 inches (approximately equal to 30~40 cm) from the eyes.

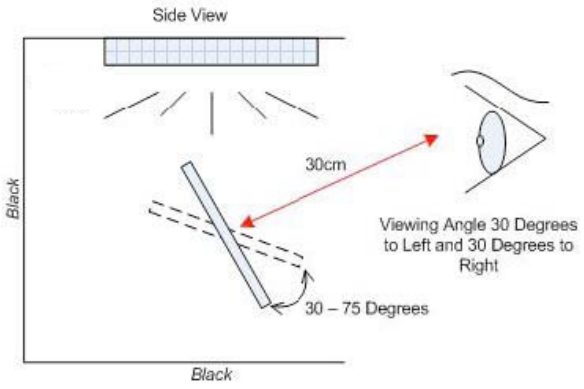
10.6.3. Inspection Time: 10 seconds per surface for a touch panel and/or cover glass assembly.

10.6.4. Inspector qualification: Inspection must be made by an individual with 20/20 or corrected to 20/20 vision with color discrimination capability.

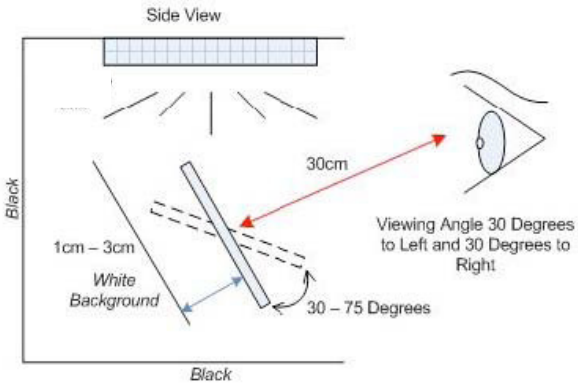
10.6.5. Parts shall not be manipulated to reflect a single light source in order to accentuate surface flaws. Parts shall be viewed without directly reflecting a light source.

10.6.6. Please refer below photos for the inspection environment.

Black Booth or Black Background



White Background



10.7. Cosmetic Specifications and Acceptance Criteria Guidelines

10.7.1. Defects must be found at Incoming Quality Control (IQC), prior to non-NVD cover glass (faceplate) or TFT display bonding, or before final manufacturing assembly.

10.7.2. Bubbles or contamination outside the viewing area are acceptable. These anomalies do not impact functionality, performance or long term reliability.

10.7.3. Bubbles between TFT/cover glass/sensor and protective film are acceptable. These anomalies do not impact functionality, performance or long term reliability.

10.7.4. If a surface blemish or defect can be wiped off easily, removed by cleaning or blown away using a compressed air gun, the touch panel is acceptable.

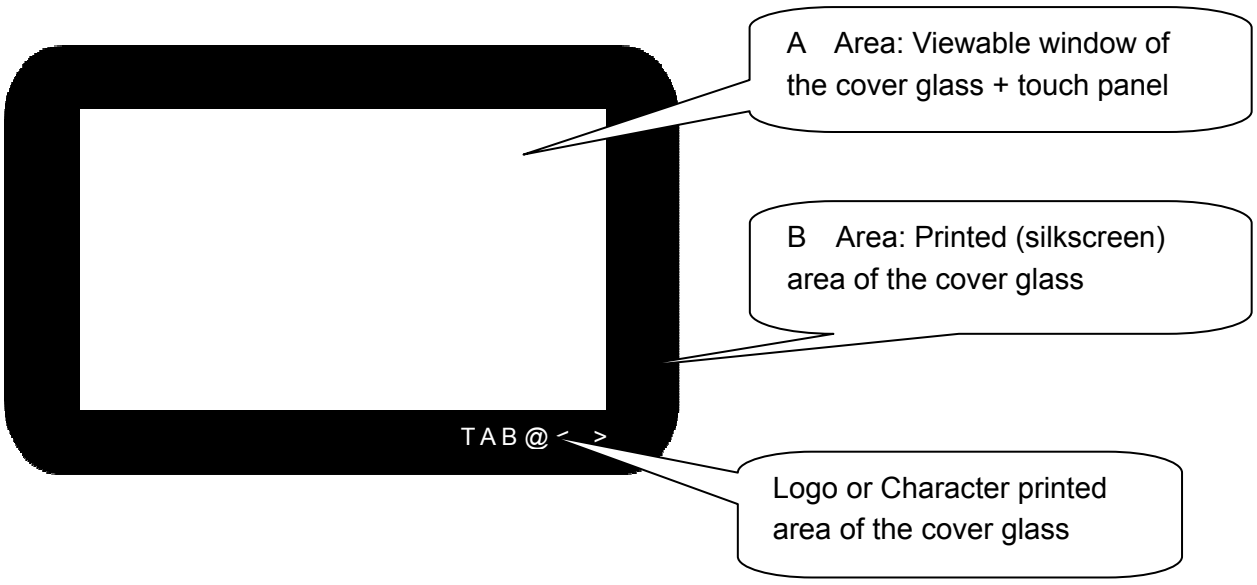
10.7.5. Blemishes or defects on the touch panel back side that are not visible from the front are acceptable. These anomalies do not impact performance, functionality or long term reliability.

10.7.6. Glass chips that do not impact functionality, performance or long term reliability and only observed from the back side are acceptable.

10.7.7. Glass cracks or fractures are not acceptable. This is a defect.

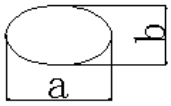
10.7.8. The FPC/PCBA refer to the IPC-A-610 (class 2) or IPC-6013.

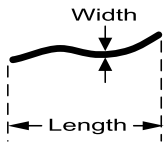
10.8. Product surface area definition

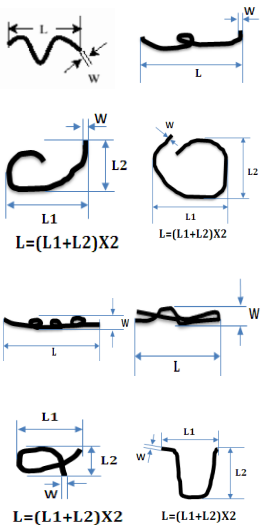


10.9. Inspection items and acceptance criteria

Visual defect

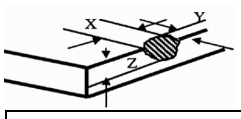
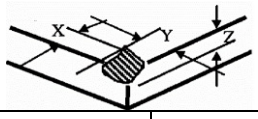
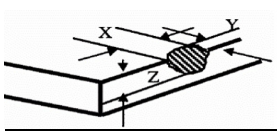
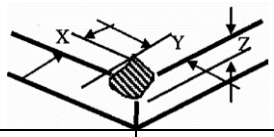
<table>
<tr><th>Item #</th><th>Inspection Item</th><th>Acceptance Criteria</th></tr>
<tr><td>01</td><td>Circular defect (Include contamination, black or white dots/spots, pin hole, bubble etc.)
 

Φ=(a+b) / 2</td><td>1.9.1.1 A area circular defect:
<table>
<tr><th colspan="2">Diameter Φ (mm)</th><th>Accept. qty. within A area</th><th>Minimum distance between blemishes</th></tr>
<tr><td rowspan="3">Dots or Spots</td><td>Φ ≤ 0.2</td><td>Ignored</td><td>/</td></tr>
<tr><td>0.2 < Φ ≤ 0.5</td><td>5</td><td>15 mm</td></tr>
<tr><td>Φ > 0.5</td><td>0</td><td>/</td></tr>
</table>
1.9.2.2 B area circular defect:
<table>
<tr><th colspan="2">Diameter Φ (mm)</th><th>Accept. qty. within B area</th><th>Minimum distance between blemishes</th></tr>
<tr><td rowspan="3">Dots or Spots</td><td>Φ ≤ 0.2</td><td>Ignored</td><td>/</td></tr>
<tr><td>0.2 < Φ ≤ 0.5</td><td>4</td><td>15 mm</td></tr>
<tr><td>Φ > 0.5</td><td>0</td><td>/</td></tr>
</table></td></tr>
<tr><td>02</td><td>Linear fibers, scratches, etc.
 

Length ----L
Width ----W
 
</td><td>Linear defects:
<table>
<tr><th>Length (mm)</th><th>Width (mm)</th><th>Accept. Quantity within A area</th><th>Accept. Quantity within B area</th><th>Minimum distance between blemishes</th></tr>
<tr><td>Ignored</td><td>W ≤ 0.05 mm</td><td colspan="2">Ignored</td><td>/</td></tr>
<tr><td>L ≤ 20 mm</td><td>0.05 < W ≤ 0.10</td><td>3</td><td>1</td><td>15 mm</td></tr>
<tr><td>L ≤ 10 mm</td><td>0.1 < W ≤ 0.20</td><td>3</td><td>1</td><td>15 mm</td></tr>
<tr><td>L > 20 mm</td><td>W > 0.20</td><td>0</td><td>0</td><td>/</td></tr>
</table>
Remarks:
a) Bubbles or contamination outside viewing area is acceptable.
b) It is acceptable if any above defects can be wiped off, or blown away by using a compressed air gun.
c) It is acceptable if any above blemish or defects only can be seen from the rear view (the blemish or defects can't be seen from the front view).</td></tr>
</table>

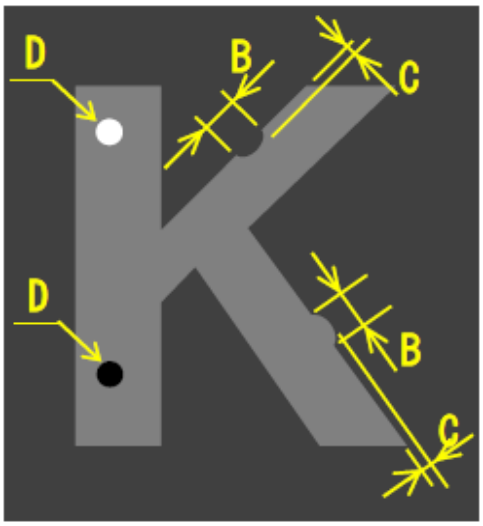
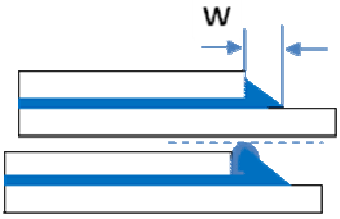
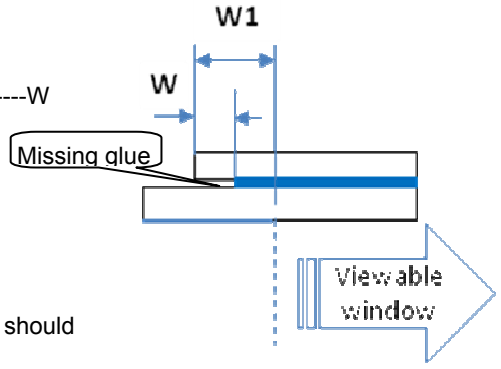
<table>
<tr><td>Item #</td><td>Inspection Item</td><td>Acceptance Criteria</td></tr>
<tr><td>03</td><td>Polarizer bubble</td><td>
<table>
<tr><th>Diameter Φ (mm)</th><th>Accept. qty. within A area</th><th>Minimum distance between blemishes</th></tr>
<tr><td>φ≤0.25</td><td>Ignored</td><td>/</td></tr>
<tr><td>0.25<φ≤0.5</td><td>4</td><td>15mm</td></tr>
<tr><td>0.5<φ</td><td>0</td><td>/</td></tr>
</table>
</td></tr>
<tr><td>04</td><td>Dent</td><td>
<table>
<tr><th>Diameter Φ (mm)</th><th>Accept. qty. within A area</th><th>Minimum distance between blemishes</th></tr>
<tr><td>φ≤0.25</td><td>Ignored</td><td>/</td></tr>
<tr><td>0.25<φ≤0.5</td><td>4</td><td>15mm</td></tr>
<tr><td>0.5<φ</td><td>0</td><td>/</td></tr>
</table>
</td></tr>
<tr><td>05</td><td>Glass chips on the edge</td><td>
a) Chipping on the cover glass (faceplate):

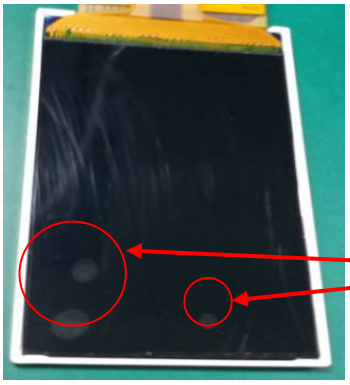
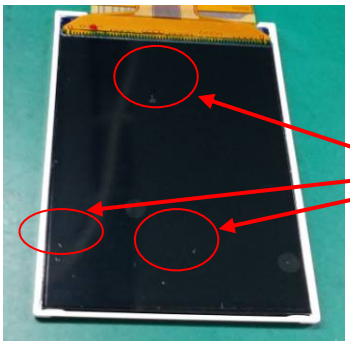
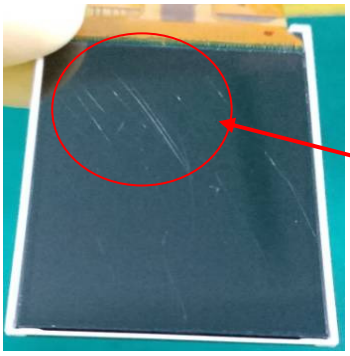
Surface of the Cover Glass: Very small chips on the surface, if any, will be regarded as dot blemishes and evaluated using the dot (circular) defect criteria.

Edge of Cover Glass: Below are the criteria for faceplate chipping:


<table>
<tr><td colspan="2">Chip on any other area – Front View</td><td colspan="2">Chip on corner – Front View</td></tr>
<tr><td>X and Y</td><td>Z</td><td>X and Y</td><td>Z</td></tr>
<tr><td>Ok ≤ 0.5mm</td><td>Ok ≤ 1/2t</td><td>Ok ≤ 0.5mm</td><td>Ok ≤ t</td></tr>
</table>
<table>
<tr><td colspan="3">Chip on any other area – Rear View</td><td colspan="3">Chip on corner – Rear View</td></tr>
<tr><td>X</td><td>Y</td><td>Z</td><td>X</td><td>Y</td><td>Z</td></tr>
<tr><td>Ok≤1.5mm</td><td>Ok≤1.0mm</td><td>Ok ≤ 1/2t</td><td>Ok≤1.0mm</td><td>Ok≤1.0mm</td><td>Ok ≤ t</td></tr>
</table>
b) Chipping on the Touch Sensor (Touch sensor glass) or TFT display:


<table>
<tr><td colspan="3">Chip on edge</td><td colspan="3">Chip on corner</td></tr>
<tr><td>X</td><td>Y</td><td>Z</td><td>X</td><td>Y</td><td>Z</td></tr>
<tr><td>Ok ≤ 4mm</td><td>Ok ≤ 2mm</td><td>Ok ≤ t</td><td>Ok ≤4mm</td><td>Ok ≤ 3mm</td><td>Ok ≤ t</td></tr>
</table>
Remarks: a. The variable t = thickness of glass.

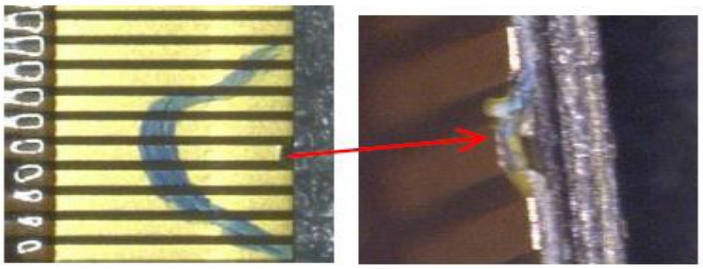
b. The touch panel top glass is bonded to the cover glass (faceplate). NVD will ensure that if there are glass chips, they are not visible from the front. Glass cracks or fractures are not acceptable.

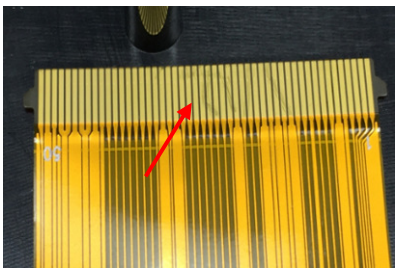
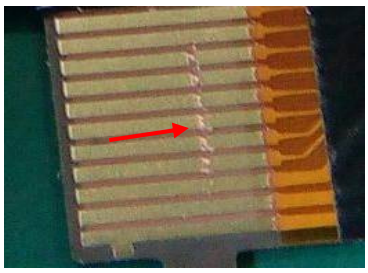
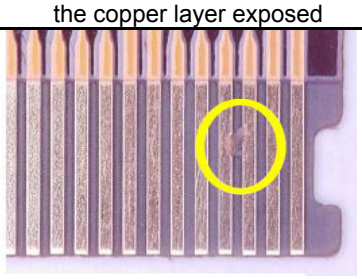
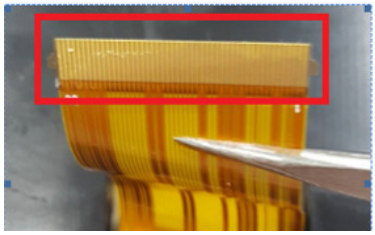
c. Edge chipping cannot be close to the ITO trace/PAD. Chipping if present will not impact function, performance or reliability.
</td></tr>
</table>

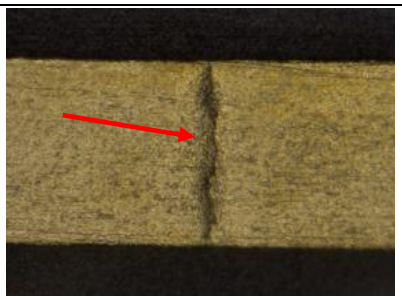
06	Glass Crack	Glass cracks or fractures are not acceptable. 
	Inspection Item	**Acceptance Criteria**
07	Logo print defects	The logo print must be in focus and readable.  a) Character : Slight or chip B <= 1/3 of Line width C <= 1/3 of Line width 1pc on 1 character is accepted b) Character: Thick or blur B <= 1/3 of Line width C <= 1/3 of Line width 1pc on 1 character is accepted c) Pin hole or Splash D < =0.1mm--- Ignored 0.1< D <=0.3mm---1pc on 1 character is accepted
08	Glue Defect	a) Exceed glue: 1) Width of exceed glue----W W<=2 mm  2) The thickness of the exceed glue higher than smaller glass is unacceptable. 3) The exceed glue can't overstep to the edge of the glass.  b) Missing glue: Width of missing glue-----W Width of frame----W1 W<=1/2 W1  Remarks: The large air bubble should be looked as missing glue and can't extend to viewable window.

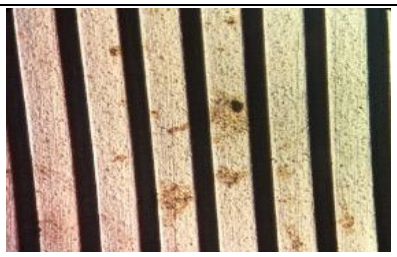
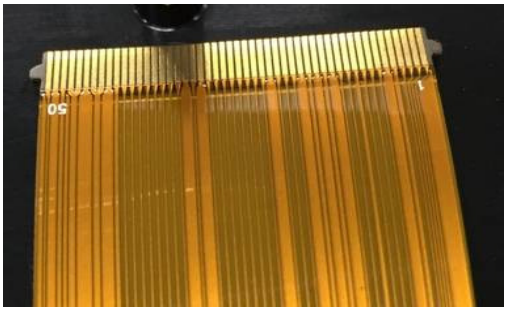
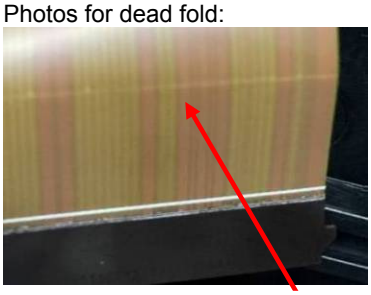
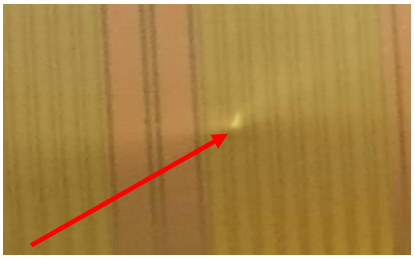
09	Protective Film	a) Protective film misalignment ≤ 2mm from edge should be acceptable. b) Protective film bubbles are acceptable.  Protective film bubbles ---Accept c) Protective film contamination: No impact the part surface under the protective film should be acceptable.  Protective film contamination ---Accept d) Protective film surface scratch: No hurt the part under the protective film should be acceptable.  Protective film surface scratch ---Accept e) Protective film broken to expose the part under the protective film, not allowed.

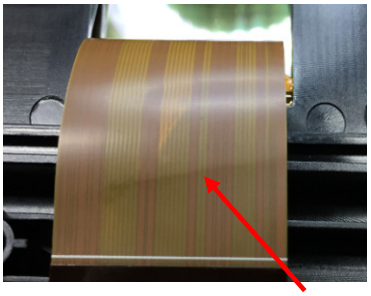
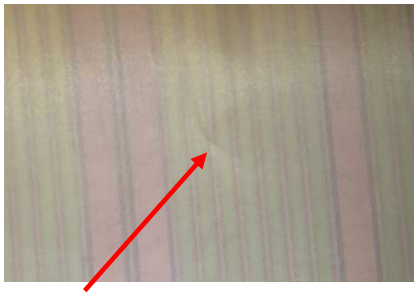
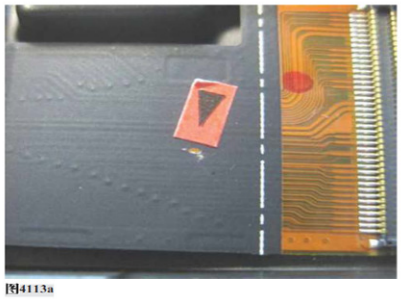
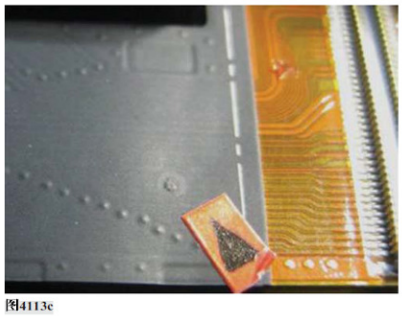
10). FPC Cosmetic Inspection Criteria:

<table>
<tr>
<td>1)</td>
<td>The tip of the terminal missing</td>
<td>


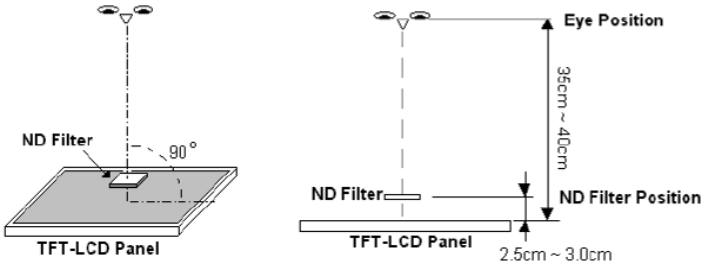
Terminal missing and perk ---Not allowed
<table>
<tr><th>Item</th><th>Accept Qty</th><th>Remark</th></tr>
<tr><td>Chip remain on the PIN</td><td>Not Allowed</td><td>Visual</td></tr>
<tr><td>Debris on the remaining PIN</td><td>Not Allowed</td><td>Visual</td></tr>
<tr><td>Terminal missing and perk</td><td>Not Allowed</td><td>Visual</td></tr>
</table>
</td>
</tr>
<tr>
<td>2)</td>
<td>Scratch on golden finger</td>
<td>

Not copper exposed---Accept

Copper exposed---Not allowed
<table>
<tr><th>Item</th><th>Accept Qty</th><th>Remark</th></tr>
<tr><td>Scratches on the golden fingers but no copper layer exposed</td><td>Ignore</td><td>Check by 10 time Microscope</td></tr>
<tr><td>Damage the golden layer to make the copper layer exposed</td><td>Not allowed</td><td>Check by 10 time Microscope</td></tr>
</table>
</td>
</tr>
<tr>
<td>3)</td>
<td>Contamination or glue remained on the golden finger</td>
<td>

Nonconductive particles remained---OK

Glue remained on golden fingers--- Not Allowed
<table>
<tr><th>Item</th><th>Accept Qty</th><th>Remark</th></tr>
<tr><td>Conductive contamination exist in PINs</td><td>Not Allowed</td><td>Visual</td></tr>
<tr><td>No Moving nonconductive foreigner contamination between two pins</td><td>Ignore</td><td>Visual</td></tr>
<tr><td>Glue remained on the golden finger</td><td>Not Allowed</td><td>Visual</td></tr>
</table>
</td>
</tr>
</table>

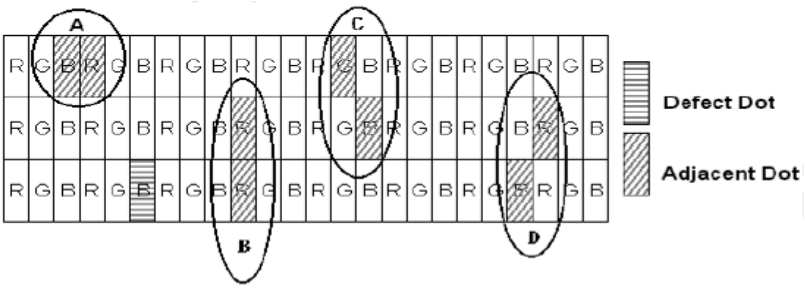
<table>
<tr>
<td>4)</td>
<td>Circuit Crack</td>
<td>

Circuit crack---Not Allowed
<table>
<tr><th>Item</th><th>Accept Qty</th><th>Remark</th></tr>
<tr><td>Circuit crack</td><td>Not Allowed</td><td>Check by CCD or Microscope</td></tr>
</table>
</td>
</tr>
<tr>
<td>5)</td>
<td>Golden finger discoloration</td>
<td>

Golden finger no discoloration
---OK

Golden finger turns black
---NG
<table>
<tr><th>Item</th><th>Accept Qty</th><th>Remark</th></tr>
<tr><td>Golden finger discoloration</td><td>Copper exposure, turns black (as upper photo) is not allowed.</td><td>Check by 10 time Microscope</td></tr>
</table>
</td>
</tr>
<tr>
<td>6)</td>
<td>Scratches on FPC coverlayer</td>
<td>

No copper layer exposed---Accepted
<table>
<tr><th>Item</th><th>Accept Qty</th><th>Remark</th></tr>
<tr><td>Scratches on the coverlayer but no copper layer exposed</td><td>Ignore</td><td>Check by 10 time Microscope</td></tr>
</table>
</td>
</tr>
<tr>
<td>7)</td>
<td>FPC Crease</td>
<td>
Photos for dead fold:


Coverlayer turns white--- Not Allowed
</td>
</tr>
</table>

<table>
<tr><td></td><td></td><td>Photos for not dead fold:
  
Crease existed, but coverlayer not turns white---Accept
<table><tr><th>Item</th><th>Accept Qty</th><th>Remark</th></tr><tr><td>Dead fold</td><td>Not Allowed</td><td>FPC coverlayer will turns white for the dead fold parts, to check by 10 time microscope if need.</td></tr></table></td></tr>
<tr><td>8)</td><td>EMI shielding Film (From IPC-A-600H 4.1.13)</td><td> 
图4113a
 
图4113b
 
图4113c
Acceptable - Class 1,2,3（可接受条件 - 1,2,3级）
• Voids, scratches or foreign material do not exceed 5 locations per side.
• Voids, scratches or foreign material do not expose metal underneath the silver film (exposed coverlay underneath is acceptable).
• Voids are less than 3.0 x 3.0 mm [0.118 x 0.118 in] in size.
• The width of scratches does not exceed 1.5 mm [0.060 in], and their length does not exceed 50% of the length of the flexible printed board.
• Touch up by black oil pen (see Figure 4113c).
• 每面的空洞、划痕或外来夹杂物不超过5处。
• 空洞、划痕或外来夹杂物未暴露银膜下的金属（暴露覆盖层可接受）。
• 空洞尺寸小于3.0x3.0mm[0.118x0.118in]。
• 划痕宽度未超过1.5mm[0.060in]，且长度未超过挠性印制板长度的50%。
• 用黑色油性笔进行修补（见图4113c）。
<table><tr><th>Item</th><th>Accept Qty</th><th>Remark</th></tr><tr><td>Exceed 5 locations per side</td><td>Not Allowed</td><td>Visual</td></tr><tr><td>Exposed metal underneath(Cu layer)</td><td>Not Allowed</td><td>Visual</td></tr><tr><td>The diameter of voids exceeds 3.0mm.</td><td>Not Allowed</td><td>Visual</td></tr><tr><td>The width of scratches exceeds 1.5mm, or length exceeds 50% of the length of FPC.</td><td>Not Allowed</td><td>Visual</td></tr></table></td></tr>
<tr><td>9)</td><td>Other Failures</td><td>To sign the golden sample if need</td></tr>
</table>

Electrical defect

<table>
<tr><th>Item #</th><th>Inspection Item</th><th>Acceptance Criteria</th></tr>
<tr><td>01</td><td>Dot(Pixel Defect)</td><td>
<table>
<tr><td>Defected item</td><td>Active area</td></tr>
<tr><td>Bright Dot</td><td>N≤3</td></tr>
<tr><td>Dark Dot</td><td>N≤4</td></tr>
<tr><td>Total Dot</td><td>N≤6</td></tr>
<tr><td>Two Adjacent Dot</td><td>N≤1</td></tr>
<tr><td>Three or More Adjacent Dot</td><td>Not Allowed</td></tr>
<tr><td>Line Defect</td><td>Not Allowed</td></tr>
<tr><td>Mura</td><td>Accept if it can not be visible by 5% ND filter in 50% gray pattern. Refer to limit sample if need.</td></tr>
</table>
Remark:
a. One pixel consists of 3 sub-pixels, including R,G and B dot(Sub-pixel=Dot)
b. The defective area of the dot is larger than 50% of one sub-pixel area as one defect; less than 50% of one sub-pixel area will be not made as one failure.
c. Bright dot is defined through 5% transmission ND filter as following:


d. Judge defect dot and adjacent dot as following. Allow below (as A, B, C and D status) adjacent defect dots, including bright and dark adjacent dot. And they will be counted as 2 defect dots in total quantity.


Defects on the Black Matrix, out of Display area, are not considered as a defect or counted.
</td></tr>
<tr><td>02</td><td>Electrical Defect</td><td>The below defects must be rejected.
1) Missing vertical / horizontal segment,
2) Abnormal Display.
3) No function or no display.
4) Current exceeds product specifications.
5) LCD viewing angle defect.
6) No Backlight.
7) Dark Backlight.</td></tr>
</table>

		8) Touch Panel no function.

10.10. Identification/marking criteria:

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

10.11. Packing:

There should be no damage of the outside carton box, each packaging box should have one identical label.

Modules inside package box should have compliant mark.

All direct package materials shall offer ESD protection.

11. Reliability Specification

Item	Condition	Cycle Time	Quantity	Remark
High Temp. Operation Test	+60 °C	96hrs	5pcs	
Low Temp. Operation Test	-20 °C	96hrs	5pcs	
High Temperature and High Humidity(operation)	Ta=+50 °C, 90%RH	96 hrs	5pcs	
Thermal Shock Test	-20°C (30min) → +60°C (30min)	10cycles	5pcs	
Laminative load test (for packaging)	First place the sample carton on a plane, then load the standard weight object on the top of the sample carton, finally observe the status of the sample carton	24 hrs	One inner carton	Refer to NVD WI Document, WI-Q4L-0196
Packing Drop test (for packaging)	1 drop on a corner, 1 drop on three arris, 1 drop on six sides	1time	One inner carton	
ESD(On Final Product)	150pF,330Ω, ±8KV contact test	10times	5pcs	*4
	150pF,330Ω, ±10KV air test			

Note: 1.For humidity test, DI water should be used.

Inspection Standard: Inspect after 1-2hrs storage at room temperature, the sample shall be free from the following defects:

- Air bubble in the LCD
- Seal Leakage
- Non-display
- Missing Segment
- Glass Crack
- IDD is greater than twice initial value.
- Others as per QA Inspection Criteria

2. No defect is allowed after testing.
3. ESD should be applied to LCD glass panel, not other areas (such as on IC and so on)
   IDD should be within twice initial value.
   In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
4. For the item in end product, the test should be implemented by customer.

12. Precautions and Warranty

12.1. Safety

12.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

12.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

12.2. Handling

12.2.1. Reverse and use within ratings in order to keep performance and prevent damage.

12.2.2. Do not wipe the polarizer or LENS with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

12.3. Storage

12.3.1. Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 10°C and 30°C, and keep the relative humidity between 40%RH and 70%RH.

12.3.2. Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant

12.3.3. Store them with no touch on surface by the anything else. It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

12.4. Metal Pin (Apply to Products with Metal Pins)

12.4.1. Pins of LCD and Backlight

12.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

12.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

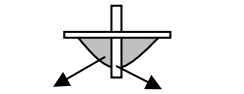
Maximum Solder Temperature: 370℃

Maximum Solder Time: 3s at the maximum temperature

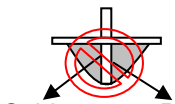
Recommended Soldering Temp: 350±20℃

Typical Soldering Time: ≤3s

12.4.1.3. Solder Wetting



Recommended



Not Recommended

12.4.2. Pins of EL

12.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.

12.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

12.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270～290℃

Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

12.4.2.4. No horizontal press on the EL leads during soldering.

12.4.2.5. 180° bend EL leads three times is not allowed.

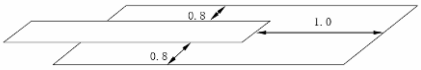
12.4.2.6. Solder Wetting

Recommended Not Recommended

12.4.2.7. The type of the solder iron:

Recommended Not Recommended

12.4.2.8. Solder Pad



12.5. Operation

12.5.1. Do not drive LCD with DC voltage

12.5.2. Response time will increase below lower temperature

12.5.3. Display may change color with different temperature

12.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.

12.6. Static Electricity

12.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.

12.6.2. The normal static prevention measures should be observed for work clothes and benches.

12.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

12.7. Limited Warranty

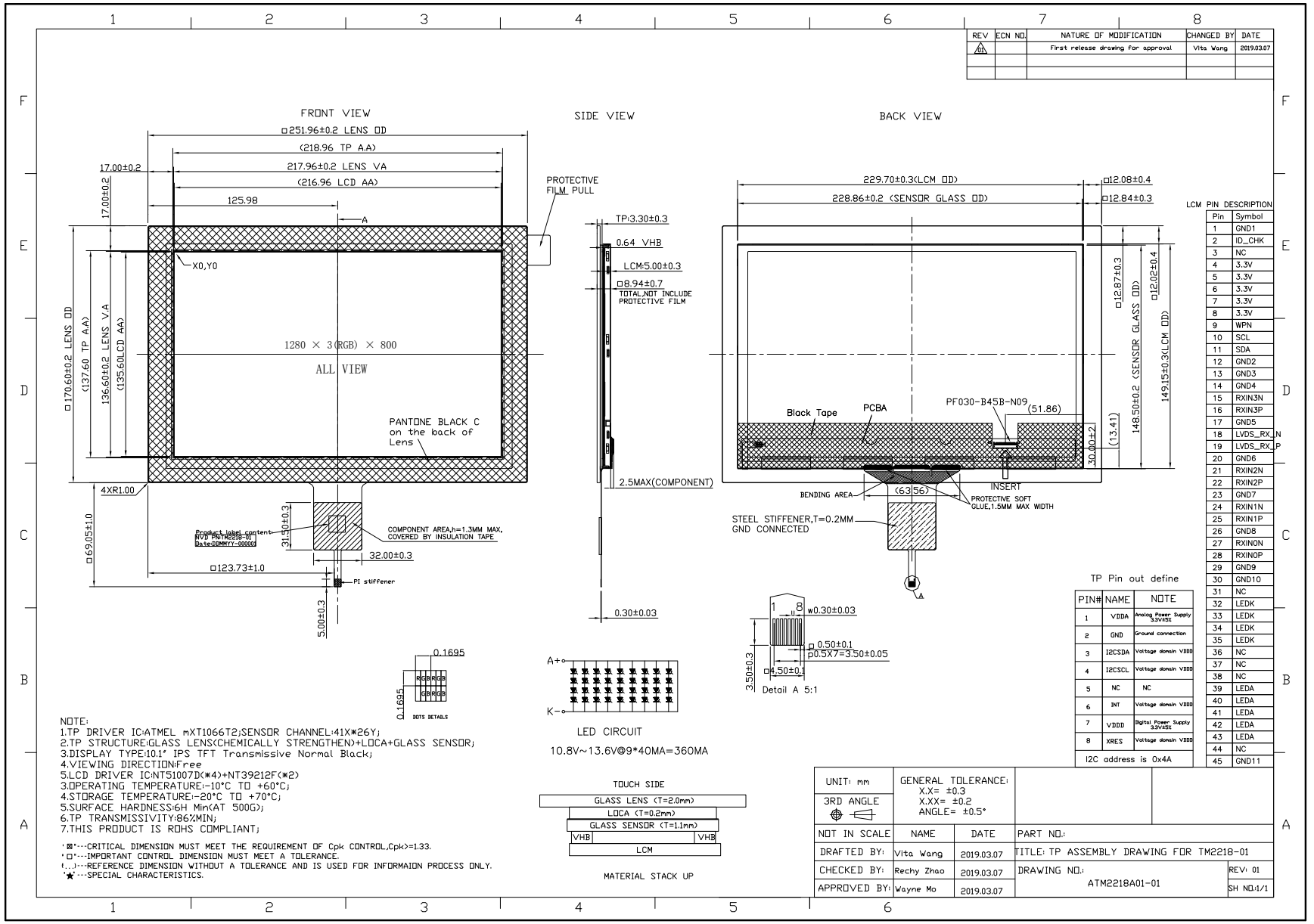
12.7.1. Unless agreed between OSD and the customer, OSD will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with OSD LCD acceptance for a period of one year from date of production. Cosmetic/ Visual defects must be returned to OSD within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of OSD limited to repair and/or replace on the terms set forth above. OSD will not be responsible for any subsequent or consequential events.

12.8. Scrap

For environment consideration, in case you scrape this product, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside, or consult a professional consultant for handling. Don’t attempt to disassemble the product, or throw it away with other wasted material.

13. Packaging

TBD



REV	ECN NO.	NATURE OF MODIFICATION	CHANGED BY	DATE
A0		First release drawing for approval	Vita Wang	2019.03.07

LCM PIN DESCRIPTION

Pin	Symbol
1	GND1
2	ID_CHK
3	NC
4	3.3V
5	3.3V
6	3.3V
7	3.3V
8	3.3V
9	WPN
10	SCL
11	SDA
12	GND2
13	GND3
14	GND4
15	RXIN3N
16	RXIN3P
17	GND5
18	LVDS_RX_N
19	LVDS_RX_P
20	GND6
21	RXIN2N
22	RXIN2P
23	GND7
24	RXIN1N
25	RXIN1P
26	GND8
27	RXIN0N
28	RXIN0P
29	GND9
30	GND10
31	NC
32	LEDK
33	LEDK
34	LEDK
35	LEDK
36	NC
37	NC
38	NC
39	LEDA
40	LEDA
41	LEDA
42	LEDA
43	LEDA
44	NC
45	GND11

TP Pin out define

PIN#	NAME	NOTE
1	VDDA	Analog Power Supply 3.3V±5%
2	GND	Ground connection
3	I2CSDA	Voltage domain VDDD
4	I2CSCL	Voltage domain VDDD
5	NC	NC
6	INT	Voltage domain VDDD
7	VDDD	Digital Power Supply 3.3V±5%
8	XRES	Voltage domain VDDD

I2C address is 0x4A

NOTE:
1.TP DRIVER IC:ATMEL mXT1066T2;SENSOR CHANNEL:41X*26Y;
2.TP STRUCTURE:GLASS LENS(CHEMICALLY STRENGTHEN)+LOCA+GLASS SENSOR;
3.DISPLAY TYPE:10.1" IPS TFT Transmissive Normal Black;
4.VIEWING DIRECTION:Free
5.LCD DRIVER IC:NT51007D(*4)+NT39212F(*2)
3.□PERATING TEMPERATURE:-10°C TO +60°C;
4.STORAGE TEMPERATURE:-20°C TO +70°C;
5.SURFACE HARDNESS:6H Min(AT 500G);
6.TP TRANSMISSIVITY:86%MIN;
7.THIS PRODUCT IS ROHS COMPLIANT;

'⊠'---CRITICAL DIMENSION MUST MEET THE REQUIREMENT OF Cpk CONTROL,Cpk>=1.33.
'□'---IMPORTANT CONTROL DIMENSION MUST MEET A TOLERANCE.
'(...)'---REFERENCE DIMENSION WITHOUT A TOLERANCE AND IS USED FOR INFORMAION PROCESS ONLY.
'★'---SPECIAL CHARACTERISTICS.

UNIT: mm	GENERAL TOLERANCE: X.X= ±0.3 X.XX= ±0.2 ANGLE= ±0.5°		
3RD ANGLE			
NOT IN SCALE	NAME	DATE	PART NO.:
DRAFTED BY:	Vita Wang	2019.03.07	TITLE: TP ASSEMBLY DRAWING FOR TM2218-01
CHECKED BY:	Rechy Zhao	2019.03.07	DRAWING NO.: ATM2218A01-01 REV: 01
APPROVED BY:	Wayne Mo	2019.03.07	SH NO:1/1