RI-INL-W007 24-mm LF Circular Inlay

1 Device Overview

1.1 Features

- Best-in-Class Performance Through Patented HDX+ (Half-Duplex) Technology
- Patented Inlay Tuning Provides Stable and High Read Performance
- 64-Bit Chip ID
- 64-Bit EN 14803 Numbering
- Insensitive to Almost All Nonmetallic Materials
- RoHS Compliant

1.2 Applications

- Waste Management

1.3 Description

Texas Instruments' 24-mm LF circular disk inlays provide superior performance and operate at a resonance frequency of 134.2 kHz. The products are compliant to the EN 14803 waste management numbering scheme and ISO/IEC global open standards. Texas Instruments LF inlays are manufactured with TI's patented tuning process to provide consistent read performance. Before delivery, the inlays undergo complete functional and parametric testing to provide the high quality customers have come to expect from TI. The inlays are well suited for various encapsulation processes.

Device Information(1)

PART NUMBER	PACKAGE	BODY SIZE
RI-INL-W007-40	TKD (0)	∅ 24 mm

(1) For more information, see Section 5, *Mechanical Packaging and Orderable Information*.

Figure 1-1 shows the RI-INL-W007 inlay.

Figure 1-1. RI-INL-W007 Inlay

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2 Revision History

DATE	REVISION	NOTES
June 2014	*	Initial Release

3 Specifications

3.1 Absolute Maximum Ratings (1)

over operating free-air temperature range (unless otherwise noted)

	RI-INL-W007
Operating temperature, T_{op}	–25°C to 70°C

(1) Stresses beyond those listed under *Absolute Maximum Ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Operating Characteristics* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

3.2 Handling Ratings

		MIN	MAX	UNIT
T_{stg}	Storage temperature	–40	85	°C

3.3 Operating Characteristics

over operating free-air temperature range (unless otherwise noted)

	RI-INL-W007
Functionality	Read only
Memory (Bits)	80 (64-bit data + 16-bit CRC)
Memory (Pages)	1
Resonance Frequency at 25°C	135.0 kHz (1) (Typical)
Modulation Frequencies at 25°C	Low bit: 135.0 ± 0.6 kHz High bit: 125.0 ± 2 kHz (2)
Transmission Principle	HDX (half duplex)
Power Source	Powered from the reader signal (battery-less)
Typical Reading Time	<70 ms (charge time of 50 ms + response time of <20 ms)
Reading Activation Field Strength at 25°C	121.5 dBµA/m (typical in gate applications, 3 x 50-ms charge cycles recommended)
EMC	Programmed code is not affected by natural electromagnetic interference or x-rays
Signal Penetration	Transponder can be read through virtually all nonmetallic material
Dimensions	∅ 24.1 (+0/-1.2) mm x 14.3 (+0.14/-0) mm x 3.15 (+0.35/-0.15) mm
Weight	2 g ± 0.5 g

(1) 800-Hz offset for encapsulation
(2) Modulation frequencies resulting from offset for encapsulation.

4 Device and Documentation Support

4.1 Documentation Support

The following documents describe the RI-INL-W007 device. Copies of these documents are available on the Internet at www.ti.com.

SPAT178 ***RFID Systems Product Specifications.*** Texas Instruments Radio Frequency Identification Systems is an industry leader in RFID technology, and the world's largest integrated manufacturer of TI-RFid™ tags, TI-RFid smart labels, and TI-RFid reader systems. With more than 1 billion RFID tags manufactured, TI-RFid technology is used in a broad range of RFID applications worldwide. TI is an active member of many standards bodies, including ISO, ISO/IEC, ECMA International, ETSI, and several national standardization bodies working to drive the adoption of global standards for RFID technology.

SLYT537 ***HDX Technology.*** White paper that describes the operation and benefits of TI's HDX Technology.

4.2 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

TI E2E™ Online Community ***TI's Engineer-to-Engineer (E2E) Community.*** Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

4.3 Trademarks

All trademarks are the property of their respective owners.

4.4 Electrostatic Discharge Caution

This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

4.5 Export Control Notice

Recipient agrees to not knowingly export or re-export, directly or indirectly, any product or technical data (as defined by the U.S., EU, and other Export Administration Regulations) including software, or any controlled product restricted by other applicable national regulations, received from disclosing party under nondisclosure obligations (if any), or any direct product of such technology, to any destination to which such export or re-export is restricted or prohibited by U.S. or other applicable laws, without obtaining prior authorization from U.S. Department of Commerce and other competent Government authorities to the extent required by those laws.

4.6 Glossary

TI Glossary This glossary lists and explains terms, acronyms, and definitions.

5 Mechanical Packaging and Orderable Information

5.1 Packaging Information

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
RI-INL-W007-40	LIFEBUY	RFIDN	TKD	0	1200	RoHS & Non-Green	Call TI	N / A for Pkg Type	0 to 0	37190 19	

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

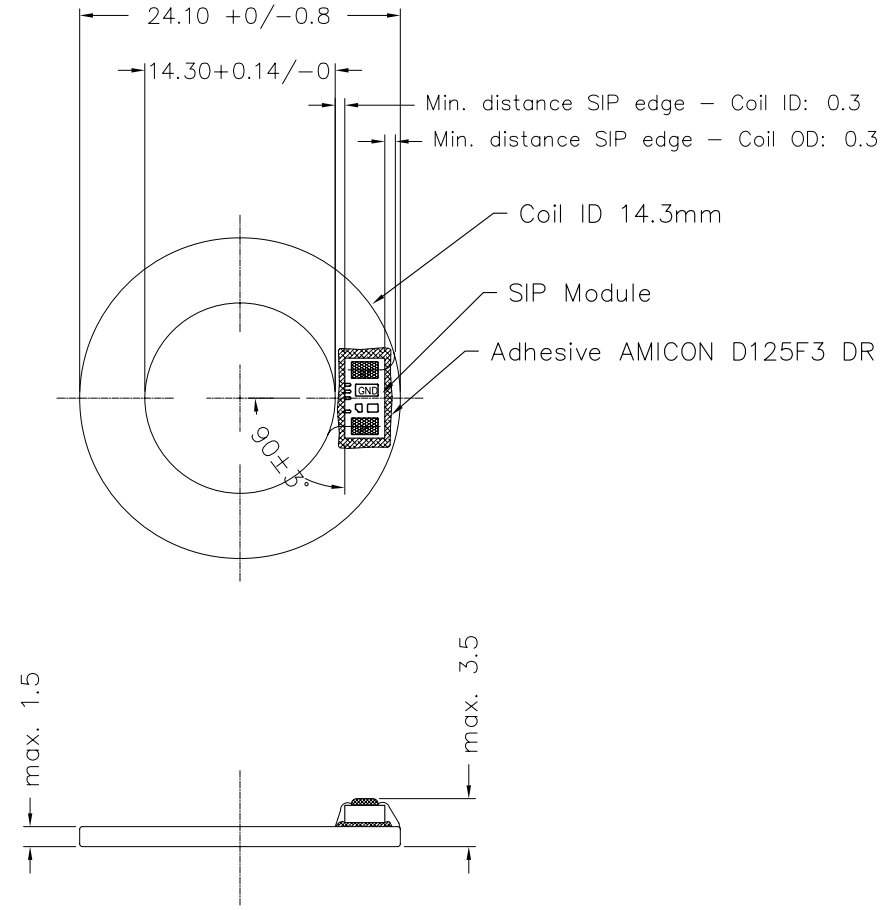
(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

TKD (R–RFID–N0) RFIDN



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NOTES:
A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5–1994.
B. This drawing is subject to change without notice.
C. See BOM 00166–1–99 for detailed component/material description.
D. Wire placement as shown.
E. Coil must meet dimensions according drawing 00140–C–07.
F. Q–Factor: min. 50
G. Lotus Notes number 00166–0–10 Rev A

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