

FEATURES:

- Enhanced N channel FET with no inherent diode to Vcc
- Low propagation delay and zero ground bounce
- 25Ω resistors for low noise
- Undershoot Clamp Diodes on all switch and control inputs
- Four enables control five bits each
- TTL-compatible input and output levels
- Available in 48-pin QVSOP package

APPLICATIONS:

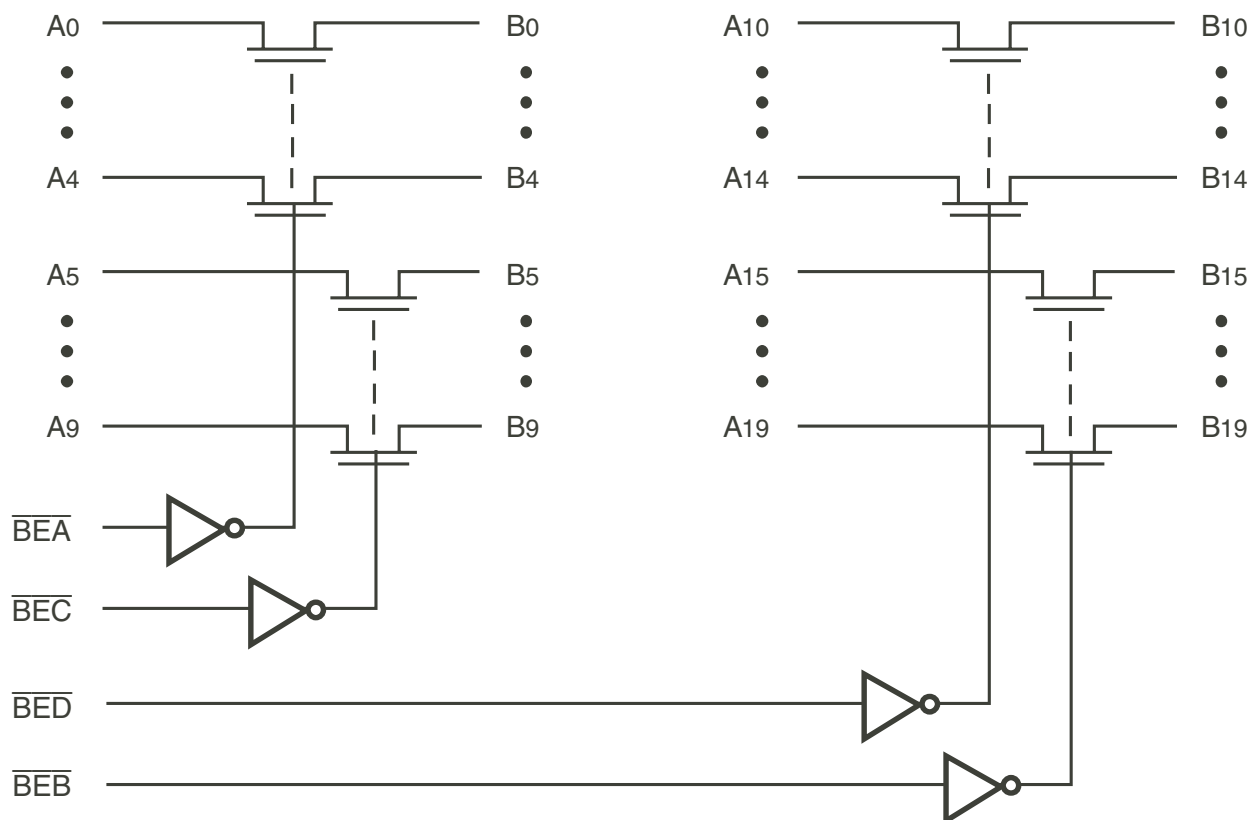
- Hot-swapping, hot-docking
- Voltage translation (5V to 3.3V)
- Power conservation
- Capacitance reduction and isolation
- Bus isolation
- Clock gating

DESCRIPTION:

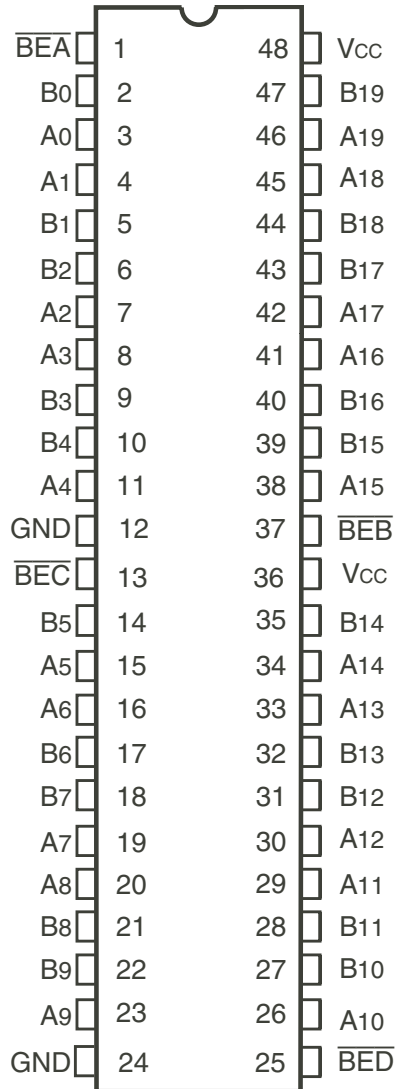
The QS32X2384 provides a set of twenty high-speed CMOS TTL-compatible bus switches. The QS32x2384 also includes internal 25Ω series termination resistors to reduce reflection noise in high-speed applications. The Bus Enable ($\overline{BE}$) signals turn the switches on. Four Bus Enable signals are provided, one for each of five bits of the 20-bit bus. The '384 family of QuickSwitch products is ideal for switching wide digital buses, as well as hotplug buffering, and 5V to 3V conversion.

The QS32X2384 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



QVSOP
TOP VIEW

PIN DESCRIPTION

Pin Names	I/O	Description
A0 - A19	I/O	Bus A
B0 - B19	I/O	Bus B
$\overline{\text{BEA}}$	I	Enable, 0 - 4
$\overline{\text{BEB}}$	I	Enable, 15 - 19
$\overline{\text{BEC}}$	I	Enable, 5 - 9
$\overline{\text{BED}}$	I	Enable, 10 - 14

ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max	Unit
$V_{\text{TERM}}^{(2)}$	Supply Voltage to Ground	-0.5 to +7	V
$V_{\text{TERM}}^{(3)}$	DC Switch Voltage V_{S}	-0.5 to +7	V
$V_{\text{TERM}}^{(3)}$	DC Input Voltage V_{IN}	-0.5 to +7	V
V_{AC}	AC Input Voltage (pulse width $\leq 20\text{ns}$)	-3	V
I_{OUT}	DC Output Current (max. sink current/pin)	120	mA
P_{MAX}	Maximum Power Dissipation ($T_{\text{A}} = 85^{\circ}\text{C}$)	0.5	W
T_{STG}	Storage Temperature	-65 to +150	$^{\circ}\text{C}$

NOTES:

- Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
- V_{CC} terminals.
- All terminals except V_{CC} .

CAPACITANCE ($T_{\text{A}} = +25^{\circ}\text{C}$, $F = 1\text{MHz}$, $V_{\text{IN}} = 0\text{V}$, $V_{\text{OUT}} = 0\text{V}$)

Pins	Typ.	Max. ⁽¹⁾	Unit
Control Inputs	3	5	pF
Quickswitch Channels (Switch OFF)	5	7	pF

NOTE:

- This parameter is guaranteed but not production tested.

FUNCTION TABLE⁽¹⁾

$\overline{\text{BEA}}$	$\overline{\text{BEB}}$	B0 - B4	B15 - B19	Function
H	H	Z	Z	Disconnect
L	H	A0 - A4	Z	Connect
H	L	Z	A15 - A19	Connect
L	L	A0 - A4	A15 - A19	Connect
$\overline{\text{BEC}}$	$\overline{\text{BED}}$	B5 - B9	B10 - B14	Function
H	H	Z	Z	Disconnect
L	H	A5 - A9	Z	Connect
H	L	Z	A10 - A14	Connect
L	L	A5 - A9	A10 - A14	Connect

NOTE:

- H = HIGH Voltage Level
L = LOW Voltage Level
X = Don't care
Z = High-Impedence

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:

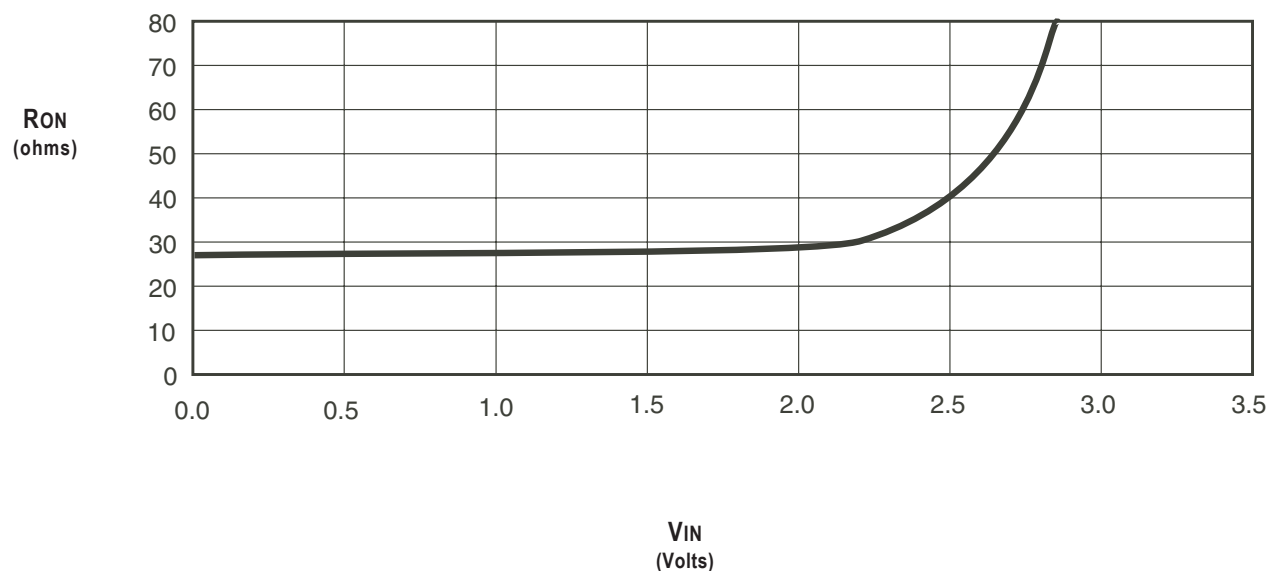
Industrial: $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 5.0\text{V} \pm 5\%$

Symbol	Parameter	Test Conditions	Min.	Typ. ⁽¹⁾	Max.	Unit
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH for Control Pins	2	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW for Control Pins	—	—	0.8	V
I_{IN}	Input Leakage Current (Control Inputs)	$0\text{V} \leq V_{IN} \leq V_{CC}$	—	± 0.01	± 1	μA
I_{OZ}	Off-State Current (Hi-Z)	$0\text{V} \leq V_{OUT} \leq V_{CC}$, Switches OFF	—	± 0.01	± 1	μA
R_{ON}	Switch ON Resistance ⁽²⁾	$V_{CC} = \text{Min.}$, $V_{IN} = 0\text{V}$, $I_{ON} = 30\text{mA}$	20	28	40	Ω
		$V_{CC} = \text{Min.}$, $V_{IN} = 2.4\text{V}$, $I_{ON} = 15\text{mA}$	20	35	48	
V_P	Pass Voltage ⁽³⁾	$V_{CC} = 5\text{V}$, $I_{OUT} = -5\mu\text{A}$	3.7	4	4.2	V

NOTES:

1. Typical values are at $V_{CC} = 5.0\text{V}$ and $T_A = 25^{\circ}\text{C}$.
2. Max value of R_{ON} is guaranteed but not production tested.
3. Pass voltage is guaranteed but not production tested.

TYPICAL ON RESISTANCE vs V_{IN} AT $V_{CC} = 5\text{V}$



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions ⁽¹⁾	Max.	Unit
I _{CCQ}	Quiescent Power Supply Current	V _{CC} = Max., V _{IN} = GND or V _{CC} , f = 0	3	mA
ΔI _{CC}	Power Supply Current per Input HIGH ⁽²⁾	V _{CC} = Max., V _{IN} = 3.4V, f = 0	2.5	mA
I _{CCD}	Dynamic Power Supply Current per MHz ⁽³⁾	V _{CC} = Max., A and B Pins Open Control Input Toggling @ 50% Duty Cycle	0.25	mA/MHz

NOTES:

- For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
- Per TTL driven input (V_{IN} = 3.4V, control inputs only). A and B pins do not contribute to ΔI_{CC}.
- This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

T_A = -40°C to +85°C

Symbol	Parameter	Min. ⁽¹⁾	Typ.	Max.	Unit
t _{PLH} t _{PHL}	Data Propagation Delay ^(2,4) A _x to B _x , B _x to A _x	—	—	1.25 ⁽³⁾	ns
t _{PZL} t _{PZH}	Switch Turn-On Delay $\overline{\text{BEx}}$ to A _x , B _x	1.5	—	7.5	ns
t _{PLZ} t _{PHZ}	Switch Turn-Off Delay ⁽²⁾ $\overline{\text{BEx}}$ to A _x , B _x	1.5	—	5.5	ns

NOTES:

- Minimums are guaranteed but not production tested.
- This parameter is guaranteed but not production tested.
- The time constant for the switch alone is of the order of 1.25ns at C_L = 50pF.
- The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

ORDERING INFORMATION

QS	XXXXX	XX	X	X		
	Device Type	Package	Process			
				Blank	8	Tube or Tray Tape and Reel
				Blank		Industrial (-40°C to +85°C)
				Q1G		48-Pin QVSOP - Green
				32X2384		High Speed CMOS 20-Bit Bus Switch

IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES ("RENESAS") PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD-PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers who are designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only to develop an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third-party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising from your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use of any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Disclaimer Rev.1.01 Jan 2024)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit www.renesas.com/contact-us/.