

MCIMX31WPDK :Part Detail

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General Information

Part Number	MCIMX31WPDK
Description	I.MX31 WINCE PDK
Material Type	Hardware
Life Cycle Description (code)	REMOVED FROM ACTIVE PORTFOLIO
Status	No Longer Manufactured

Package Information

Package Type and Termination Count	Hardware Only
Package Description and Mechanical Drawing	Hardware Only
Tape & Reel	No

Environmental and Compliance Information

Pb-Free	Not Available
Material Composition Declaration (MCD)	Download MCD Report Download MCD Report
RoHS Certificate of Analysis (CoA)	Contact Us
Peak Package Body Temperature (PPT)(°C)	Not Available
REACH SVHC	NXP REACH Statement

Ordering Information

Exempt from Minimum Delivery Value	Yes
Export Control Classification Number (US)	5A002
Harmonized Tariff (	
CCATS Document	-
ENC Status	-
Other Trade Compliance Document	-

Order

Distributor

Operating Characteristics

Additional Features	ATA Controller Watchdog OSC/Timer
Bus Frequency (Max) (MHz)	133
Cache (KB)	16
Core Type	ARM11
Core: Number of cores (Spec)	1
Core: Operating Voltage (Spec) (V)	1.65
External Bus Interface	PCMCIA
External Bus Width (bit)	0
External Memory Supported	DDR SDRAM PSRAM
I2C	3
I2S	2
Internal RAM (KB)	16
L2 Cache (Max) (KB)	128
Operating Frequency (Max) (MHz)	532
PWM (bit)	16
PWM (ch)	1
Sample Exception Availability	N
SPI	3
USB features	USB OTG

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