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April 2012



FJD3305H1 NPN Silicon Transistor

### Features

- High Voltage Switch Mode Application
- Fast Speed Switching
- Wide Safe Operating Area
- Suitable for Electronic Ballast Application
- Wave Soldering



1. Base 2. Collector 3. Emitter

#### **Absolute Maximum Ratings\*** T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	700	V
$V_{CEO}$	Collector-Emitter Voltage	400	V
V <sub>EBO</sub>	Emitter-Base Voltage	9	V
I <sub>C</sub>	Collector Current (DC)	4	Α
I <sub>CP</sub>	Collector Current (Pulse)	8	Α
I <sub>B</sub>	Base Current	2	Α
$P_{C}$	Collector Dissipation, T <sub>a</sub> = 25°C	1.1	W
	$T_c = 25^{\circ}C$	50	W
$T_J$	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-65 to 150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

### **Thermal Characteristics** $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	110	°C/W
$R_{ heta Jc}$	Thermal Resistance, Junction to Case	2.0	°C/W

<sup>\*</sup> Device mounted on minimum pad size

#### **Ordering Information**

Part Number	Marking	Package	Packing Method	Remarks
FJD3305H1TM	J3305H1	D-PAK	Tape & Reel	

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© 2012 Fairchild Semiconductor Corporation FJD3305H1 Rev. A1

# **Electrical Characteristics\*** T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdwon Voltage	$I_C = 500 \mu A, I_E = 0$	700			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 5mA, I_B = 0$	400			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 500 \mu A, I_C = 0$	9			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 700V, I_{E} = 0$			1	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = 9V, I_{C} = 0$			1	μΑ
h <sub>FE1</sub>	DC Current Gain *	$V_{CE} = 5V, I_{C} = 1A$	19		28	
h <sub>FE2</sub>		$V_{CE} = 5V, I_{C} = 2A$	8		40	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 1A, I_B = 0.2A$			0.5	V
		$I_C = 2A, I_B = 0.5A$			0.6	V
		$I_{C} = 4A, I_{B} = 1A$			1.0	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = 1A, I_B = 0.2A$			1.2	V
` ,		$I_C = 2A, I_B = 0.5A$			1.6	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 0.5A$	4			MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = 10V, f = 1MHz$		65		pF
t <sub>ON</sub>	Turn On Time	$V_{CC} = 125V, I_{C} = 2A$			0.8	μS
t <sub>STG</sub>	Storage Time	$I_{B1} = -I_{B2} = 0.4A$			4.0	μS
$t_{F}$	Fall Time	$R_L = 62.5\Omega$			0.9	μS

<sup>\*</sup> Pulse Test: Pulse Width $\leq$ 300 $\mu$ s, Duty Cycle $\leq$ 2%

## **Typical Performance Characteristics** V<sub>CE</sub> = 5V 4.0 I<sub>B</sub> = 300mA Ic [A], COLLECTOR CURRENT 3.5 I<sub>B</sub> = 250mA h<sub>Fe</sub>, DC CURRENT GAIN $I_{B} = 200 \text{m/s}$ 2.5 2.0 $I_B = 50 \text{mA}$ 0.01 $V_{CE}[V]$ , COLLECTOR-EMITTER VOLTAGE I<sub>c</sub> [A], COLLECTOR CUTRRENT Figure 1. Static Characteristic Figure 2. DC Current Gain $I_c = 4 I_B$ Voe(sat) [V], SATURATION VOLTAGE V<sub>BE</sub>(sat) [V], SATURATION VOLTAGE Ta = 125 °C $I_c$ [A], COLLECTOR CURRENT I<sub>c</sub> [A], COLLECTOR CURRENT Figure 4. Base - EmitterSaturation Voltage Figure 3. Collector- Emitter Saturation Voltage 10000 C t<sub>F</sub> & t<sub>srg</sub> [μs], SWITCHING TIME 1000 CAPACITANCE[pF] 1000 Ta = 125 °C 100 Cob $I_c$ [A], COLLECTOR CURRENT REVERSE VOLTAGE[V] Figure 5. Switching Time Figure 6. Capacitance

#### **Typical Performance Characteristics** (Continued)

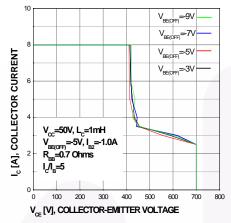


Figure 7. Reverse Biased Safe Operating Area

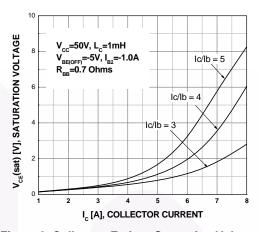


Figure 8. Collector- Emitter Saturation Voltage at RBSOA

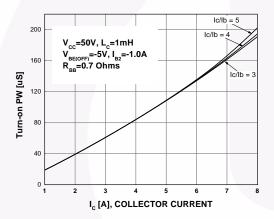


Figure 9. Input Pulse width vs Correct current at RBSOA

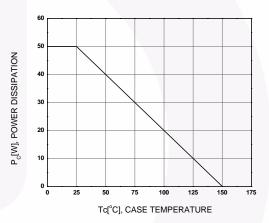


Figure 10. Power Derating

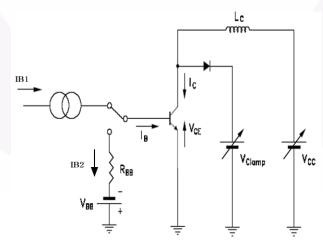
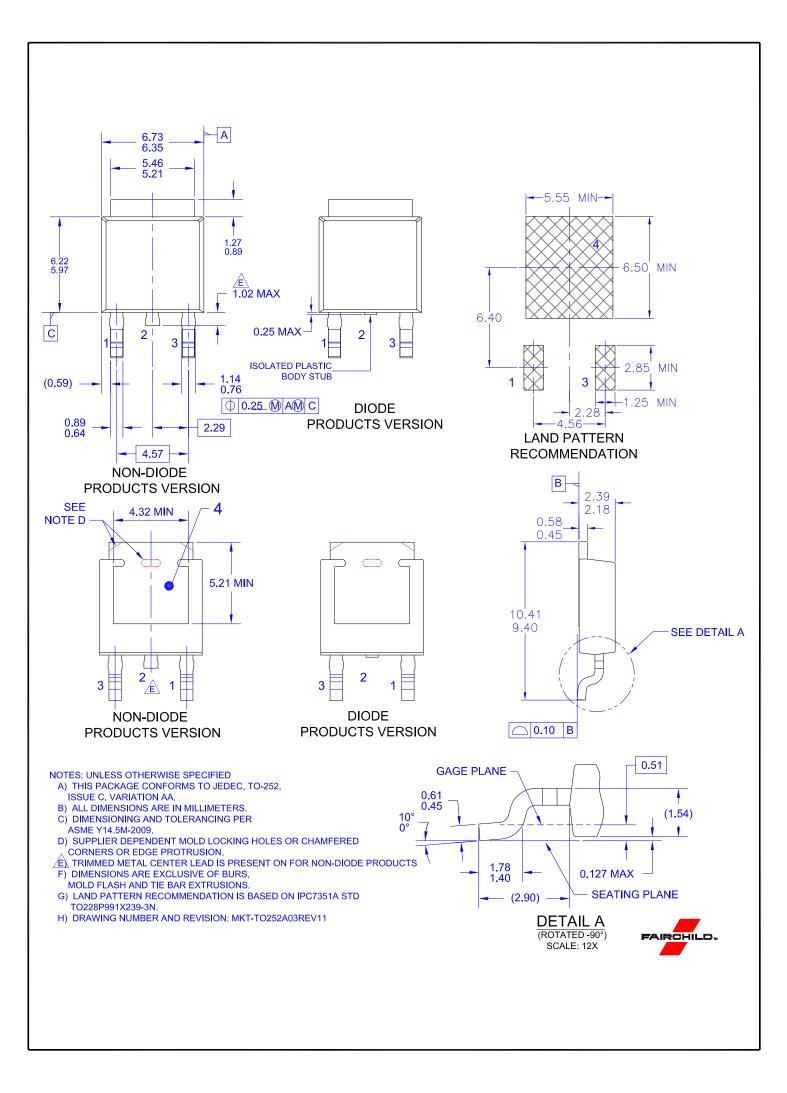


Figure 11. RBSOA Test Circuit



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