Engineering/Process Change Notice

ECN/PCN No.: 4139

For Manufacturer			
Product Description: PLASTIC SMD MEMS OSCILLATOR	**Abracon Part Number / Part Series:** EMRA36	☐ Documentation only ☐ ECN ☒ EOL	☒ Series ☐ Part Number
Affected Revision: A	**New Revision:** EOL	**Application:**	☐ Safety ☒ Non-Safety
Prior to Change: Active https://abracon.com/datasheets/Ecliptek/EMRA36.pdf			
After Change: EOL			
Cause/Reason for Change: Discontinuation of manufacturing capability.			

Change Plan		
Effective Date: 2/7/2022	**Additional Remarks:** N/A	
Change Declaration: N/A		
Issued Date: 2/7/2022	**Issued By:** *Brooke Cushman* *Product Engineer*	**Issued Department:** Engineering
Approval: *Thomas Culhane* Engineering Director	**Approval:** *Reuben Quintanilla* Quality Director	**Approval:** *Ying Huang* Purchasing Director

For Abracon EOL only		
Last Time Buy (if applicable): 5/7/2022	**Alternate Part Number / Part Series:** ASEDV (3.2x2.5mm)	
Additional Approval:	**Additional Approval:**	**Additional Approval:**

Customer Approval (If Applicable)		
Qualification Status: ☐ Approved ☐ Not accepted *Note: It is considered approved if there is no feedback from the customer 1 month after ECN/PCN is released.*		
Customer Part Number:	**Customer Project:**	
Company Name:	**Company Representative:**	**Representative Signature:**
Customer Remarks:		

ABRACON Ecliptek FOX ILSI MMD OSCILENT

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REGULATORY COMPLIANCE

Lead Free COMPLIANT | EU RoHS 2011/65 + 2015/863 COMPLIANT | China RoHS COMPLIANT | REACH SVHC COMPLIANT | DRC CONFLICT FREE

ITEM DESCRIPTION

MEMS Clock Oscillators LVCMOS (CMOS) 2.25Vdc to 3.63Vdc 4 Pad 2.5mm x 3.2mm Plastic Surface Mount (SMD)

ELECTRICAL SPECIFICATIONS

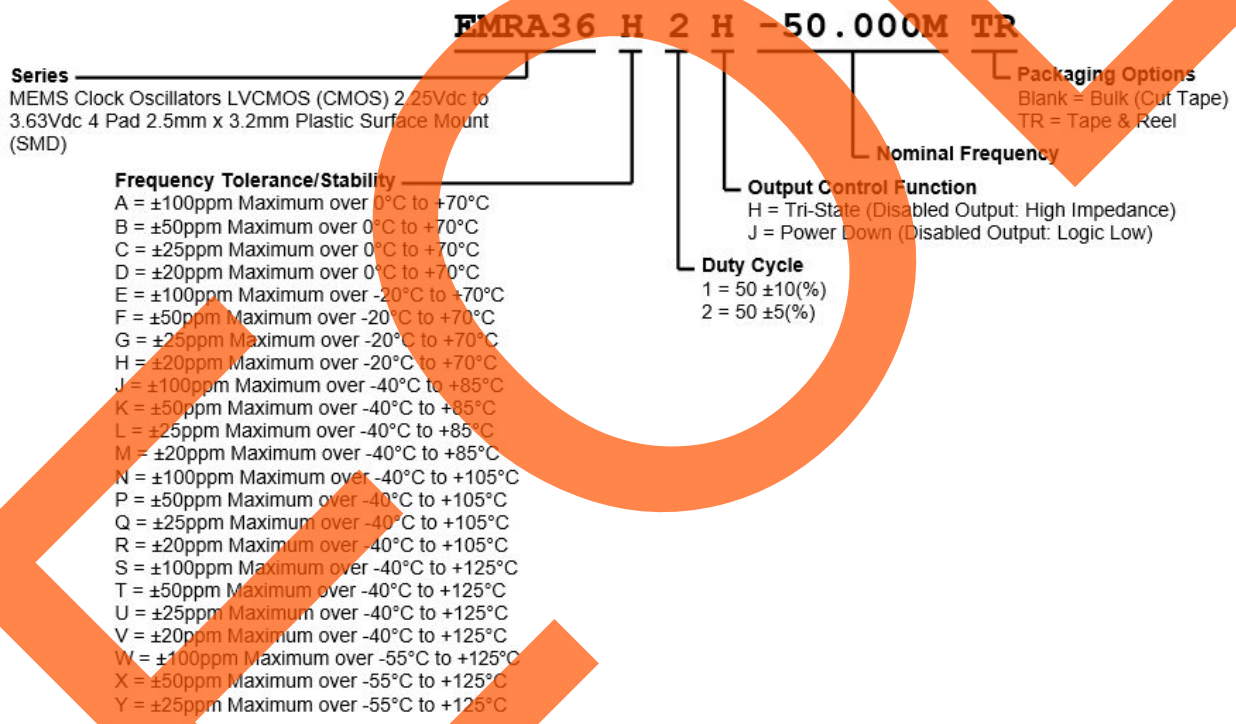
Nominal Frequency	1MHz to 137MHz
Frequency Tolerance/Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, and First Year Aging at 25°C ±100ppm Maximum over 0°C to +70°C ±50ppm Maximum over 0°C to +70°C ±25ppm Maximum over 0°C to +70°C ±20ppm Maximum over 0°C to +70°C ±100ppm Maximum over -20°C to +70°C ±50ppm Maximum over -20°C to +70°C ±25ppm Maximum over -20°C to +70°C ±20ppm Maximum over -20°C to +70°C ±100ppm Maximum over -40°C to +85°C ±50ppm Maximum over -40°C to +85°C ±25ppm Maximum over -40°C to +85°C ±20ppm Maximum over -40°C to +85°C ±100ppm Maximum over -40°C to +105°C ±50ppm Maximum over -40°C to +105°C ±25ppm Maximum over -40°C to +105°C ±20ppm Maximum over -40°C to +105°C ±100ppm Maximum over -40°C to +125°C ±50ppm Maximum over -40°C to +125°C ±25ppm Maximum over -40°C to +125°C ±20ppm Maximum over -40°C to +125°C ±100ppm Maximum over -55°C to +125°C ±50ppm Maximum over -55°C to +125°C ±25ppm Maximum over -55°C to +125°C
Aging at 25°C	±1.5ppm Maximum First Year
Supply Voltage	2.25Vdc to 3.63Vdc
Input Current	No Load 5mA Maximum over Nominal Frequency of 1MHz to 20MHz 6mA Maximum over Nominal Frequency of 20.000001MHz to 50MHz 7mA Maximum over Nominal Frequency of 50.000001MHz to 80MHz 9mA Maximum over Nominal Frequency of 80.000001MHz to 137MHz
Output Voltage Logic High (V_{OH})	IOH = -4mA 90% of Vdd Minimum
Output Voltage Logic Low (V_{OL})	IOL = +4mA 10% of Vdd Maximum
Rise/Fall Time	Measured from 20% to 80% of waveform 1.2nSec Typical, 3nSec Maximum
Duty Cycle	Measured at 50% of waveform 50 ±10(%) 50 ±5(%)
Load Drive Capability	15pF Maximum
Output Logic Type	CMOS
Output Control Function	Tri-State (Disabled Output: High Impedance) Power Down (Disabled Output: Logic Low)
Output Control Input Voltage Logic High (Vih)	70% of Vdd Minimum or No Connect to Enable Output

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Output Control Input Voltage Logic Low (Vil)	30% of Vdd Maximum to Disable Output
Power Down Output Enable Time	5mSec Maximum (Disabled Output: Logic Low)
Tri-State Output Enable Time	150nSec Maximum (Disabled Output: High Impedance)
Power Down Output Disable Time	150nSec Maximum (Disabled Output: Logic Low)
Tri-State Output Disable Time	150nSec Maximum (Disabled Output: High Impedance)
Standby Current	5µA Maximum (Disabled Output: Logic Low)
Period Jitter (RMS)	2pSec Typical, 4pSec Maximum
RMS Phase Jitter (Fj = 900kHz to 7.5MHz; Random)	0.5pSec Typical, 1pSec Maximum
RMS Phase Jitter (Fj = 12kHz to 20MHz; Random)	1.5pSec Typical, 3pSec Maximum
Start Up Time	5mSec Maximum
Storage Temperature Range	-65°C to +150°C

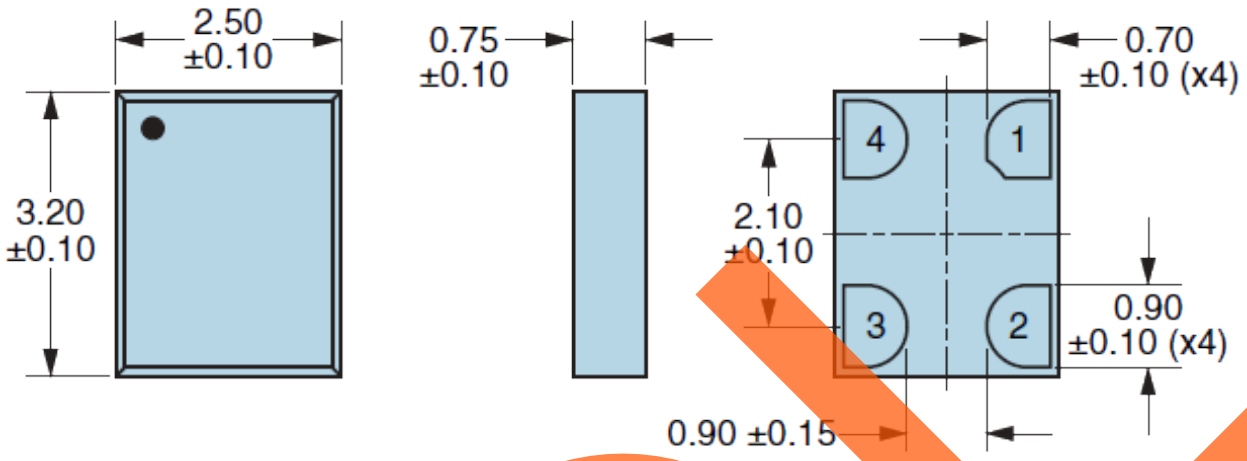
PART NUMBERING GUIDE



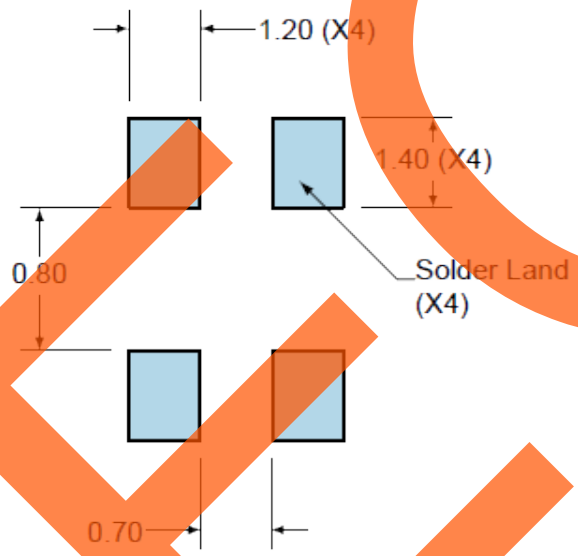
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MECHANICAL DIMENSIONS



SUGGESTED SOLDER PAD LAYOUT



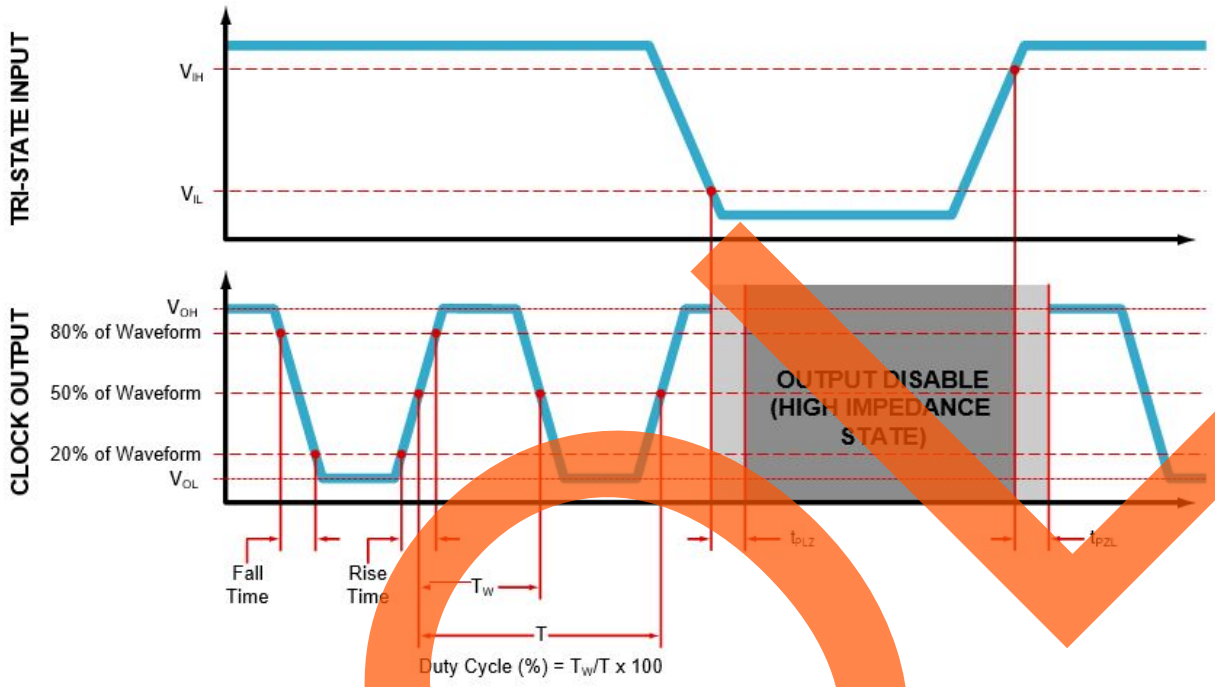
PIN	CONNECTION
1	Power Down Or Tri-State
2	Ground
3	Output
4	Supply Voltage

All Tolerances are ±0.1

All Dimensions in Millimeters

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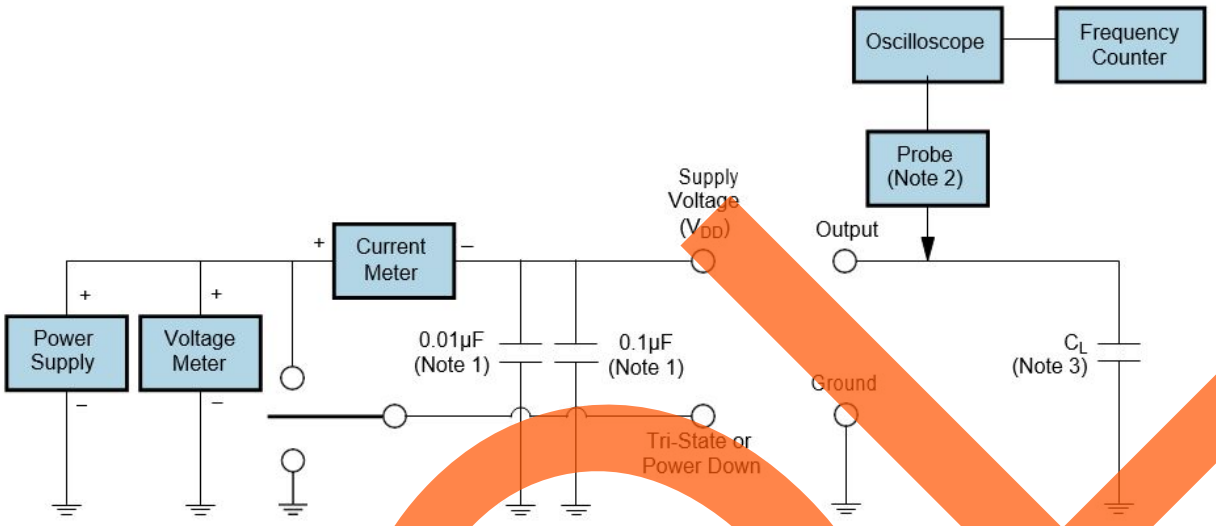
OUTPUT WAVEFORM & TIMING DIAGRAM



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TEST CIRCUIT FOR CMOS OUTPUT



Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less Than 2mm) to the package ground and supply voltage pin is required.
Note 2: A low input capacitance (<12pF), 10X Attenuation Factor, High Impedance (>10Mohms), and High bandwidth (>300MHz) Passive probe is recommended.
Note 3: Capacitance value CL includes sum of all probe and fixture capacitance. See applicable specification sheet for 'Load Drive Capability'.

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TAPE & REEL DIMENSIONS

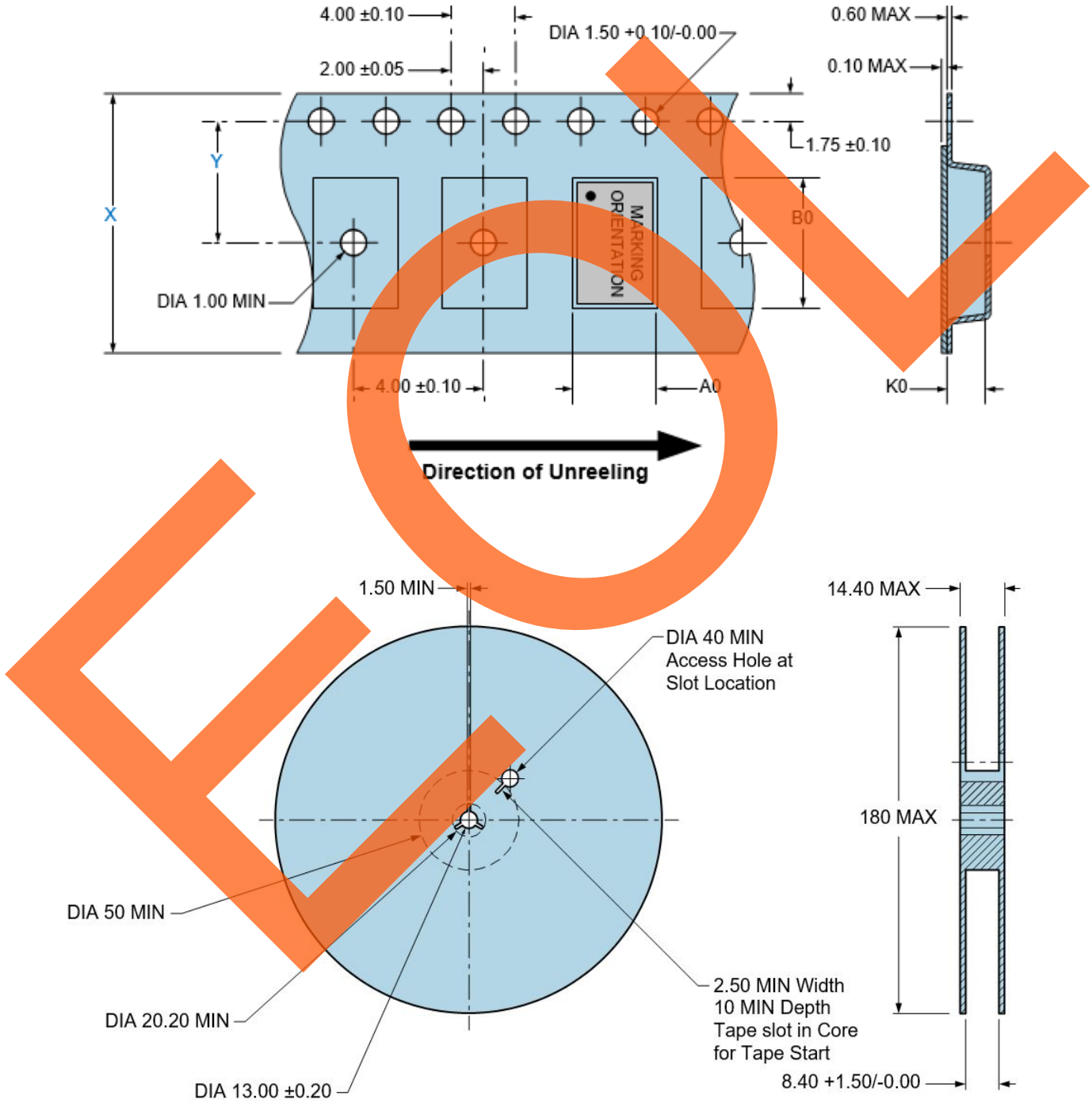
Quantity per Reel: 1,000 Units

All Dimensions in Millimeters

Compliant to EIA-481

X = 8.00 ±0.30 or 12 ±0.30

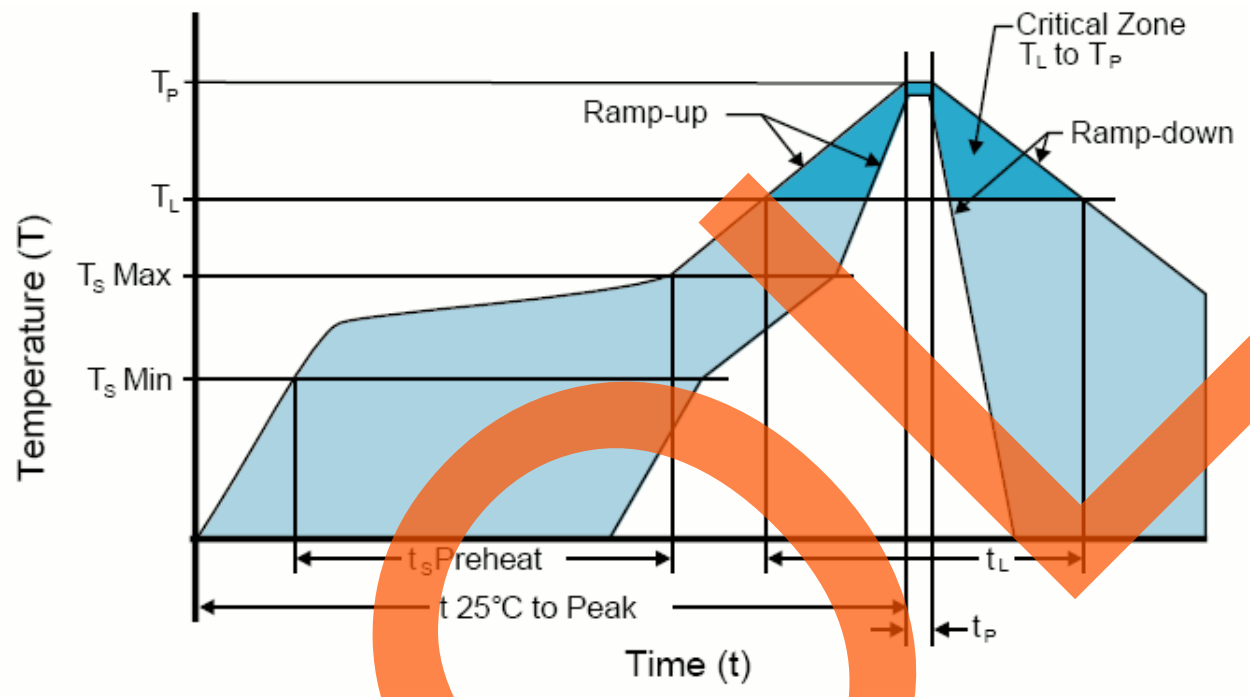
Y = 3.50 ±0.05 or 5.5 ±0.05



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RECOMMENDED SOLDER REFLOW METHOD



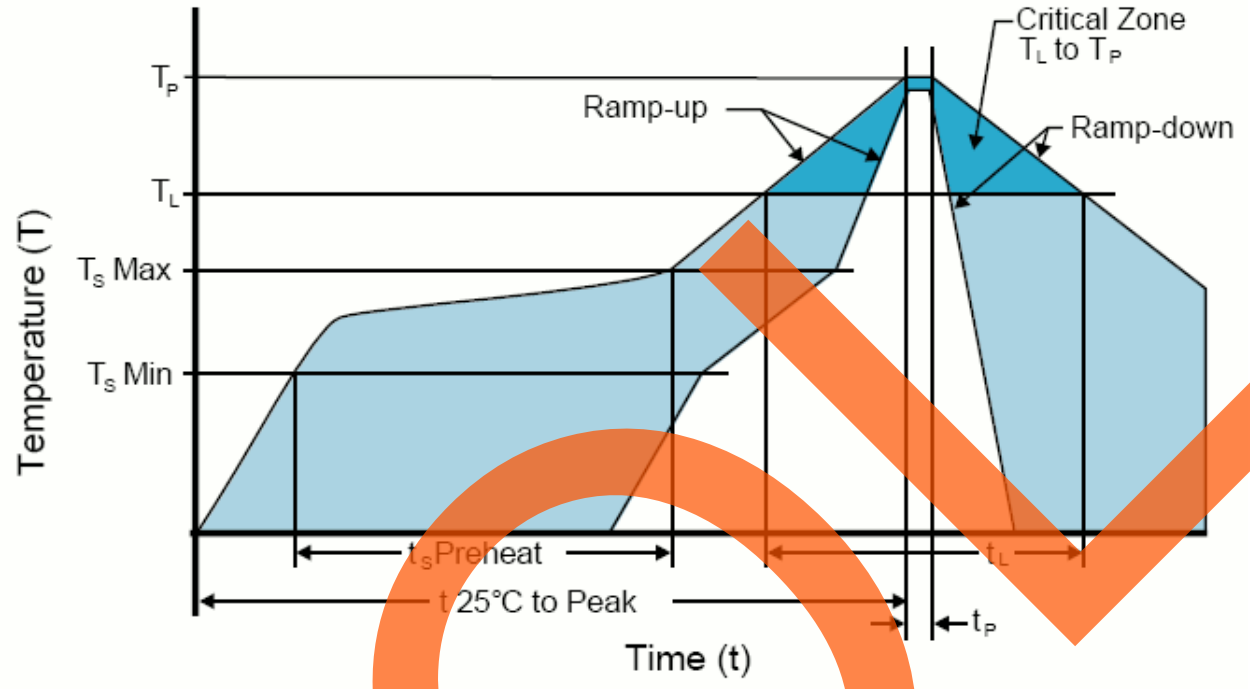
HIGH TEMPERATURE INFRARED/CONVECTION	
T_S MAX to T_L (Ramp-up Rate)	3°C/Second Maximum
Preheat - Temperature Minimum (T_S MIN) - Temperature Typical (T_S TYP) - Temperature Maximum(T_S MAX) - Time (t_S MIN)	 150°C 175°C 200°C 60 - 180 Seconds
Ramp-up Rate (T_L to T_P)	3°C/Second Maximum
Time Maintained Above: - Temperature (T_L) - Time (t_L)	 217°C 60 - 150 Seconds
Peak Temperature (T_P)	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature(T_P Target)	250°C +0/-5°C
Time within 5°C of actual peak (t_p)	20 - 40 Seconds
Ramp-down Rate	6°C/Second Maximum
Time 25°C to Peak Temperature (t)	8 Minutes Maximum
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

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RECOMMENDED SOLDER REFLOW METHOD



LOW TEMPERATURE INFRARED/CONVECTION	
T_S MAX to T_L (Ramp-up Rate)	5°C/Second Maximum
Preheat **- Temperature Minimum (T_S MIN)** **- Temperature Typical (T_S TYP)** **- Temperature Maximum(T_S MAX)** **- Time (t_S MIN)**	 N/A 150°C N/A 60 - 120 Seconds
Ramp-up Rate (T_L to T_P)	5°C/Second Maximum
Time Maintained Above: **- Temperature (T_L)** **- Time (t_L)**	 150°C 200 Seconds Maximum
Peak Temperature (T_P)	240°C Maximum
Target Peak Temperature(T_P Target)	240°C Maximum 2 Times/230˚C Maximum 1Time
Time within 5°C of actual peak (t_p)	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time
Ramp-down Rate	5°C/Second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1
Additional Notes	Temperatures shown are applied to body of device.

Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

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