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CUSTOMER . MIE

SAMPLE CODE · SC1602LRU-HWA-B-Q

MASS PRODUCTION CODE . PC1602LRU-HWA-B-Q

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 001

DRAWING NO. (Ver.) · JLMD-PC1602LRU-HWA-B-Q_001

PACKAGING NO. (Ver.) : JPKG-PC1602LRU-HWA-B-Q_001

Customer Approved

Date:

POWERTIP 2013.10.11 JS RD APPROVED

Approved	Checked	Designer
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- ☐ Preliminary specification for design input
- Specification for sample approval

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NO.PT-A-005-8



RECORDS OF REVISION

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
03/30/2005	0		PC1602LRU-HWA-B-Qis the ROHS compliant part number based on Powertip's standard PC1602LRU-HWA-B		
10/07/2007	A		Update Timing Characteristics and Display Command	12,14	
10/09/2013	01	001	Update Sample Specification	-	周志仙
					<i></i>

Total: 30 pages



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Note: For detailed information please refer to IC data sheet: SitronixST7066U, ST7065B



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN, YG, Positive ,Transflective, Normal Temp
Driver Condition	LCD Module: 1/16 Duty, 1/4 Bias
Viewing Direction	6 O'clock
Backlight	YG LED B/L
Weight	36g
Interface	-
Other(controller /	ST7044H ST7045D
driver IC)	ST7066U,ST7065B
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web site:
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	85.0 (L) *36.0(W) * 14.5max(H)	mm
Viewing Area	66.0(L) * 16.2(W)	mm
Active Area	56.21(L) * 11.5 (W)	mm
Dot Size	0.56(L) * 0.66(W)	
Dot Pitch	0.6 (L) * 0.7(W)	mm

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	-	-0.3	7.0	V
LCD Driver Supply Voltage	V_{LCD}	-	V _{DD} -10.0	V _{DD} +0.3	V
Input Voltage	$V_{\rm IN}$	-	-0.3	V _{DD} +0.3	V
Operating Temperature	T_{OP}	Excluded B/L	0	+50	$^{\circ}\!\mathbb{C}$
Storage Temperature	T_{ST}	Excluded B/L	-20	+70	$^{\circ}\!\mathbb{C}$
Storage Humidity	H_D	Ta < 60 °C	-	90	%RH



1.4 DC Electrical Characteristics

 $V_{DD} = 5.0 \text{ V} \pm 10\%$, $V_{SS} = 0\text{V}$, $Ta = 25^{\circ}\text{C}$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	$V_{ m DD}$	-	4.5	5.0	5.5	V
"H" Input Voltage	V_{IH}	-	$0.7 V_{DD}$	-	$V_{ m DD}$	V
"L" Input Voltage	$V_{ m IL}$	-	-0.3	-	0.6	V
"H" Output Voltage	V_{OH}	IOH=-0.1mA	3.9	•	V_{DD}	V
"L" Output Voltage	$V_{ m OL}$	IOL=0.1mA	-	-	0.4	V
Supply Current	I_{DD}	V _{DD} =5.0V		2.0	3.0	mA
		0℃	-		-	
LCM Driver Voltage	$ m V_{OP}$	25℃ *1	4.2	4.4	4.6	V
		50°C	-	-	-	

NOTE: *1 The VOP test point is the V_{DD} - V_{O}





1.5 Optical Characteristics

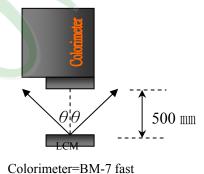
LCD Panel: 1/16Duty · 1/4Bias · $V_{LCD} = 4.2$ V · Ta = 25°C

			ЕСВТ		Duty 17 1.	Dias Vic		14 23 0
Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Response Time	Rise	tr		-	150		me	Note2
Response Time	Fall	tf	-	-	300	-	ms	Note2
	Тор	θ+		40	-			
Viewing angle	Bottom	θ-	C≧2.0	40	-	-	Dog	Note1
range	Left	θL	U <u>≦</u> 2.0	45	-	-	Deg.	Note1
	Right	θR		45	-	-		
Contrast Rat	io	С	-	5	7	1	-	Note3
Uniformity	I	△B	If=120mA	70	-	-	%	Note4

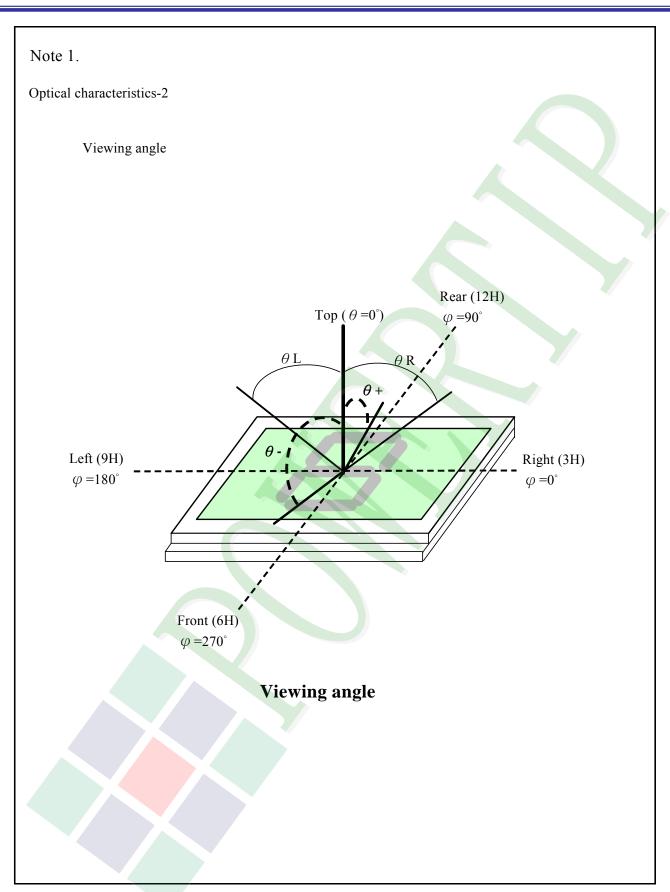
Note 4:

- 1 : △B=B(min) / B(max)* 100 %
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^{\circ})$
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness \pm 4%
- 3: This value will be changed while mass production.

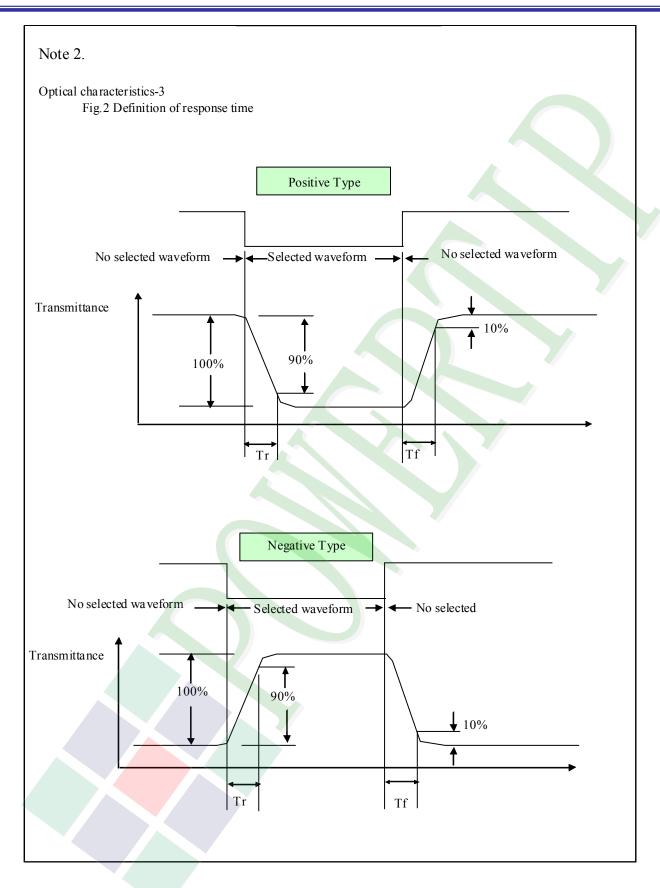














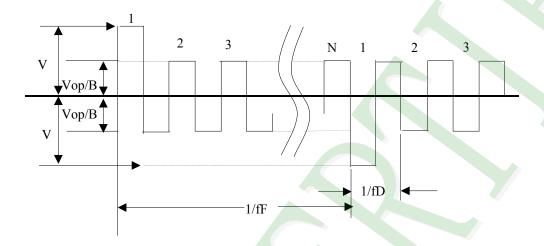
Electrical characteristics-2

※2 Drive waveform

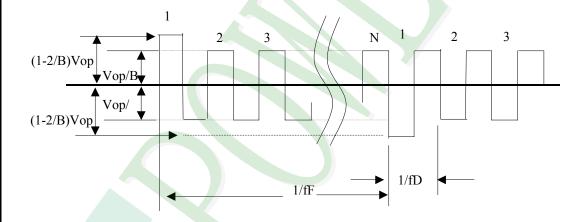
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



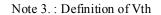
(2) Non- Selected wave form



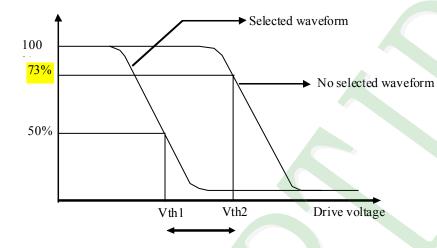
Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period





Transmittance



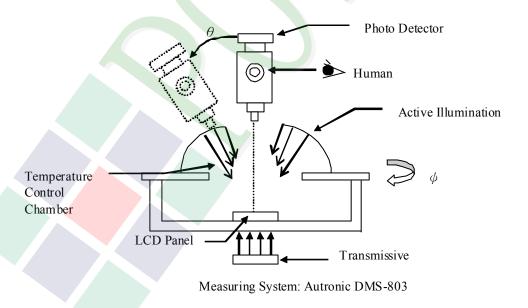
Active voltage range

	Vth1	Vth2
	VIII	VtIIZ
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio

= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System





1.6 Backlight Characteristics

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°℃	-	300	mA
Reverse Voltage	VR	Ta =25°℃	-	8	V
Power Dissipation	PD	Ta =25°C	- 🔨	1.38	mW
Operating temperature	Тор	-	0	50	$^{\circ}$ C
Storage temperature	Tst	-	-20	70	$^{\circ}$ C

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF=120mA	-	4.2	4.6	V
Wavelength	λр	IF=120mA	569	>	576	nm
Reverse Current	IR	VR=5V	-	-	0.2	mA
Luminous Intensity (without LCD)	Iv	IF=120mA	220	290	-	cd/m ²
Color	Yellow-green					





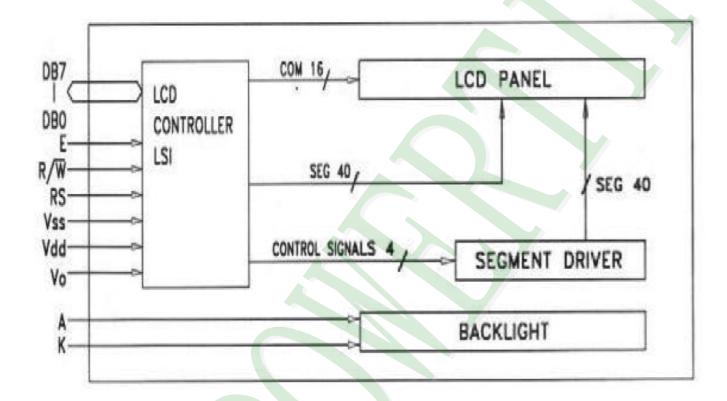
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





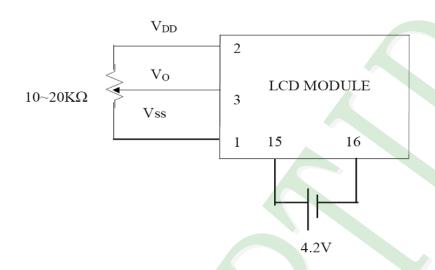
2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	V_{SS}	Power Supply (Vss=0)
2	V_{DD}	Power Supply (V _{DD} >V _{SS})
3	Vo	Operating voltage for LCD
		Register Selection input
4	RS	High = Data register
4	KS	Low = Instruction register (for write)
		Busy flag address counter (for read)
		Read/Write signal input is used to select the read/write
5	5 R/W	mode
		High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
		Four low order bi-directional three-state data bus lines. Use
7~10	$DB0 \sim DB3$	for data transfer between the MPU and the LCD module.
		These four are not used during 4-bit operation.
		Four high order bi-directional three-state data bus lines.
		Used for data transfer between the MPU and the LCD
11~14	DB4~DB7	module.
		DB7 can be used as a busy flag.
15	A	Power supply for LED B/L (+)
16	K	Power supply for LED B/L (-)



2.2.1 Application Notes

Contrast Adjust



2.2.2 Refer Initial code

```
delay(40);
    write_com(0x01);
    delay(5);
    write_com(0x38);
    delay(5);
    write_com(0x0c);
    delay(5);
    write_com(0x06)
    delay(5);
```

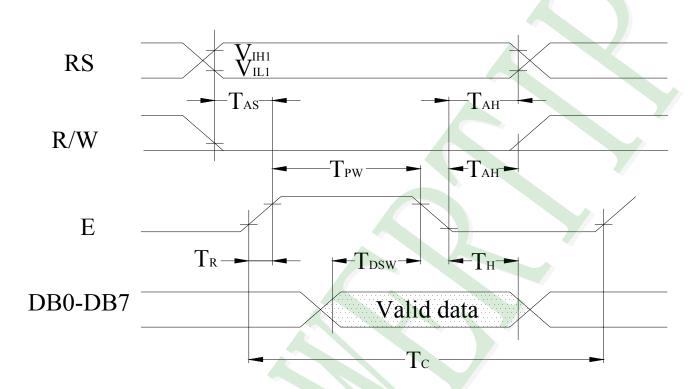
void initial()

}

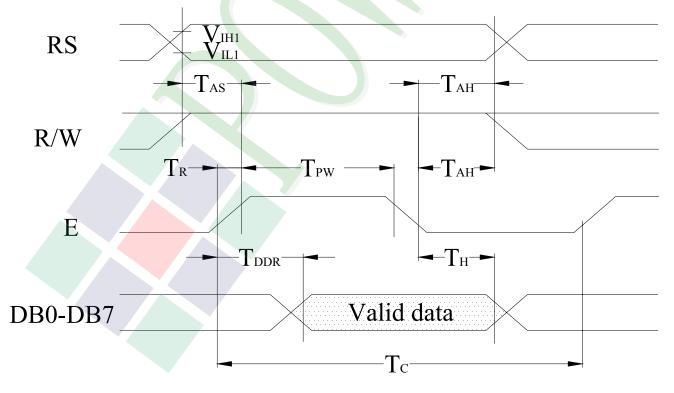


2.3 Timing Characteristics

• Writing data from MPU to ST7066U



Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

 $(Vdd = +5V,Ta=25^{\circ}C)$

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T_{C}	Enable Cycle Time	Pin E	1200	1	-	ns
T_{PW}	Enable Pulse Width	Pin E	140	1		ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-		25	ns
T_{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DSW}	Data Setup Time	Pins:DB0~DB7	40		-	ns
T_{H}	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

• Read Mode (Reading data from ST7066U to MPU)

 $(V_{dd} = +5V, Ta = 25^{\circ}C)$

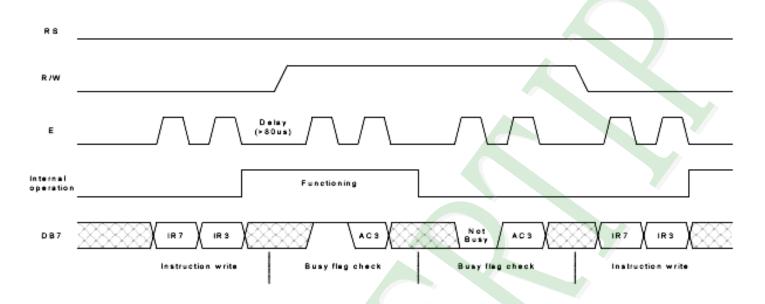
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T_{C}	Enable Cycle Time	Pin E	1200	-	-	ns
T_{PW}	Enable Pulse Width	Pin E	140		-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	1	25	ns
T_{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T_{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_{DDR}	Data Setup Time	Pins:DB0~DB7	-	<i>-</i>	100	ns
T_{H}	Data Hold Time	Pins:DB0~DB7	10		-	ns





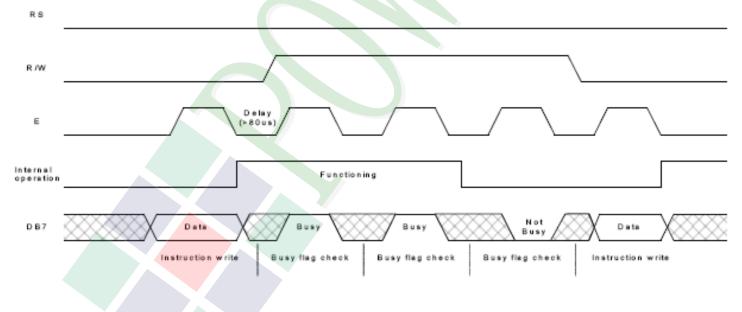
For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .

Example of busy flag check timing sequence





2.4 Display Command

					Instru	ection	Code	:				Description
Instructions			DB	DB	DB	DB	DB	DB	DB	DB	Description	Time
	RS	R/W	7	6	5	4	3	2	1	0		(270KHz)
C1											Write "20H" to DDRAM. and set	
Clear	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms
Display											AC.	
											Set DDRAM address to "00H"	
Return											from AC and return cursor to it's	- >>
Home	0	0	0	0	0	0	0	0	1	×	original position if shifted.	1.52ms
Tionic											The contents of DDRAM are not	
											changed.	
											Sets cursor move direction and	
Entry Mode	0	0	0	0	0	0	0	1	I/D	S	specifies display shift. These	37µs
Set			U					1		3	operations are performed during	57μ8
											data write and read.	
Display											D=1 : entire display on	
ON/OFF	0	0	0	0	0	0	1	D	C	В	C=1 : cursor on	37µs
							4				B=1 : cursor position on	
Cursor or											Set cursor moving and display	
Display	0	0	0	0	0	1	S/C	R/L	×	×	shift control bit, and the direction,	37µs
Shift			U			1	3/0	N.L		^	without changing of DDRAM	37μ8
Silit											data.	
Function											DL: interface data is 8/4 bits	
Set	0	0	0	0	1	DL	N	F	X	×	NL: number of line is 2/1	37µs
Set											F: font size is $5 \times 11/5 \times 8$	
Set					AC	AC	AC	AC	AC	۸С	Set CGRAM address in address	
CGRAM	0	0	0	1	5	4	3	2	1	0	counter.	37µs
Address					3	4	3	Z	1	U	Counter.	
Set				AC	AC	AC	AC	AC	AC	ΔC	Set DDRAM address in address	
DDRAM	0	0	1	6	5	4	3	2	1	0	counter.	37µs
Address				0	3	7	3		1		Counter.	



Read Busy Flag and Address	0	1	BF	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0µs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF.

Refer to Instruction Table for the list of each instruction execution time.





2.5 Character pattern

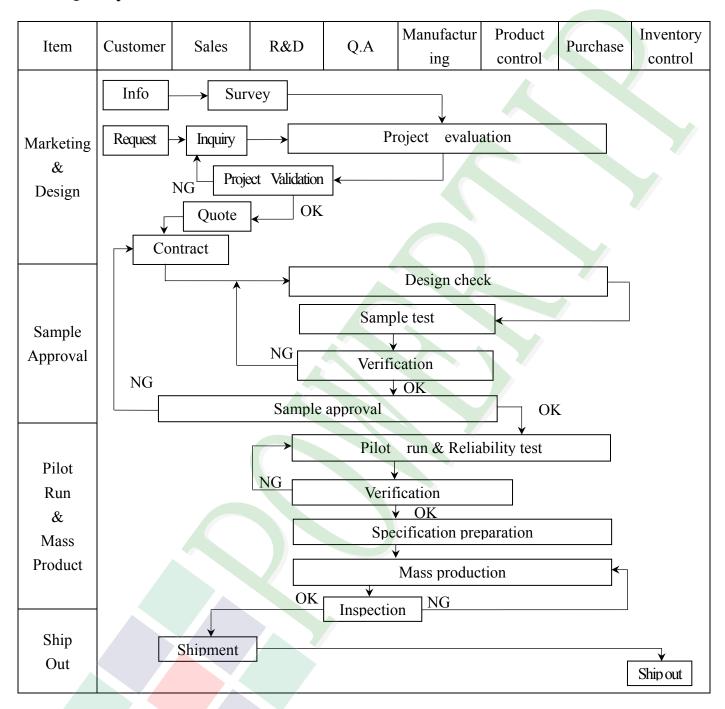
NO.7066-0A

NO.7	000	0/1														
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)															
0110	3															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110	7)															
1111	(8)															

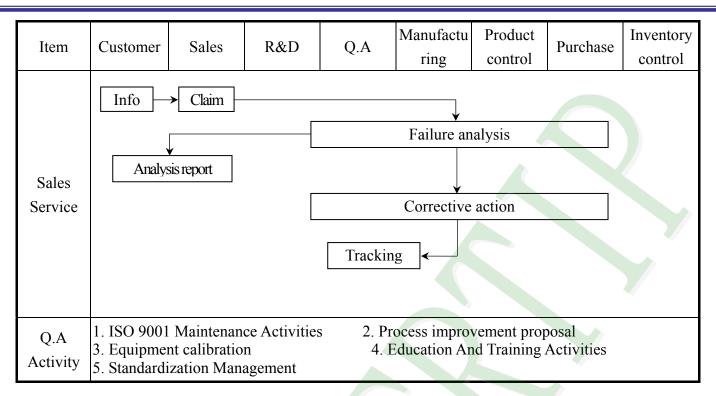


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

- ◆Scope: The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).
- ♦Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge \ MIL-STD \ Powertip Tester \ Sample
- ◆Defect Level: Major Defect AQL: 0.4 ; Minor Defect: AQL: 1.5.
- **♦**OUT Going Defect Level : Sampling .
- **♦**Manner of appearance test :
 - (1). The test be under 20W×2 fluorescent light 'and distance of view must be at 30 cm.
 - (2). Standard of inspection: (Unit: mm)
 - (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (4). Definition of area . (Fig. 2)

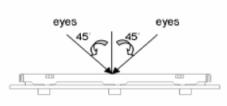


Fig.1

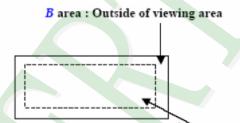


Fig. 2 A area: viewing area

♦ Specification:

NO	Item	Criterion	Level
		1. 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1. 3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3. 1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
04	Electrical Testing	4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4. 5 Current consumption exceeds product specifications.	Major



◆Specification For Monotype and Color STN:

NO	Item	type and Color STN :	riteri	on				Level
	Black or white dot \ scratch \ contamination	 5. 1 Round type: 5. 1. 1 display only: • White and black spots on display ≤ 0. 30 mm, no more than 4 white or black spots present. • Densely spaced: NO more than two spots or lines within 3 mm. 						
	Round type	5, 1, 2 Non-display : Dimension (diameter : Φ)		Acceptance (Q'ty) A area B area				
	+ <u>×</u> + ₊	$\Phi \leq 0.10$		ept no dense		Ignore		
05	<u> </u>	$0.10 < \Phi \le 0.20$ $0.20 < \Phi \le 0.30$		3	J			Minor
	$\Phi = (x+y)/2$	Total quantity		4				
	Line type	5. 1. 3 Line type: Dimension	0. 03	Accept A area		e (Q'ty) B area		
	→ L	$L \le 3.0 \qquad 0.03 < W \le 0$ $L \le 2.5 \qquad 0.05 < W \le 0.$	075	4		Ignore	•	
		W > 0.	. 075	As	roun	d type		
		Dimension (diameter : Φ)		Acceptance (O'ty) Barea	a	
		$\Phi \leq 0.20$		cept no dense				
06	Polarizer	$0.20 < \Phi \leq 0.50$		3				Minor
	Bubble	$0.50 < \Phi \le 1.00$ $\Phi > 1.00$		0		Ignore		
		Total quantity		4				



◆Specification For Monotype and Color STN:

NO	Item	Criterion	Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length	
		7. 1 General glass chip: 7. 1. 1 Chip on panel surface and crack between panels:	
		Z Z Y	
07	The crack of glass	SP SP [NG]	Minor
		Seal width	
		Z. I. I.	
		X Y Z $\leq a$ Crack can't enter viewing area $\leq 1/2 t$	
		≤ a Crack can't exceed the half of SP width. 1/2 t < Z ≤2 t	



♦Specification For Monotype and Color STN:

NO	Item	Criterion	Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length	
		7.1.2 Corner crack:	
		X Y Z	
		≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t	
	The crack of	$\leq 1/5$ a Crack can't exceed the half of SP width. $1/2$ t $<$ Z ≤ 2 t	
07	glass	7.2 Protrucion over terminal:	Minor
		7.2 Protrusion over terminal: 7.2.1 Chip on electrode pad:	
		W Y X X Y Z	
		X	
		X Y Z	
		Front \leq a \leq 1/2 W \leq t	
		Back Neglect	



◆Specification For Monotype and Color STN:

NO	Item	Criterion	Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length	
		7. 2. 2 Non-conductive portion:	
07	The crack of glass	X Y Z ≤1/3 a ≤W ≤t O If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode	Minor
		the 110 must remain and be inspected according to electrode terminal specifications. 7, 2, 3 Glass remain:	
		$\begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \text{ W} & \leq t \end{array}$	



◆Specification For Monotype and Color STN:

NO	Item	Criterion	Level
		8. 1 Backlight can't work normally.	Major
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
		9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤1. 5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO	TECT ITEM	TECT OF	ANDITION (VCI.DOI)					
NO.	TEST ITEM		ONDITION					
1	High Temperature	Keep in +70°C±2°C 96 hrs						
	Storage Test	Surrounding temperature, then sto	orage at normal condition 4hrs.					
2	Low Temperature	Keep in -20°C ±2°C 96 hrs	1 12 1					
	Storage Test	Surrounding temperature, then sto						
0	High Temperature /	Keep in +60°C /90% R.H duration						
3	High Humidity Storage Test	Surrounding temperature, then sto	orage at normal condition 4hrs.					
	Divinge Test	(Excluding the polarizer)	→+70°C→ +25°C					
4	Temperature Cycling	(30mins) (5mins)						
	Storage Test	10 Cycle						
		Surrounding temperature, then sto	orage at normal condition 4hrs.					
		Air Discharge:	Contact Discharge:					
		Apply 6 KV with 5 times	Apply 250 V with 5 times					
		Discharge for each polarity +/-	discharge for each polarity +/-					
		1. Temperature ambiance : 15℃						
5	ESD Test	2. Humidity relative: 30%~60%						
		3. Energy Storage Capacitance(Cs+Cd): 150pF±10%						
		4. Discharge Resistance(Rd): 330 Ω±10%						
		5. Discharge, mode of operation: Single Discharge (time between guessesive discharges at least 1 see)						
		Single Discharge (time between successive discharges at least 1 sec)						
		(Tolerance if the output voltage indication: ±5%)						
_	Vibration Test	1. Sine wave 10~55 Hz frequence	· · · · · · · · · · · · · · · · · · ·					
6	(Packaged)	2. The amplitude of vibration :1.						
		3. Each direction (X \ Y \ Z) dur	ration for 2 Hrs					
		Packing Weight (Kg)	Drop Height (cm)					
		0 ~ 45.4	122					
_	Drop Test	45.4 ~ 90.8	76					
7	(Packaged)	90.8 ~ 454	61					
		Over 454	46					
		Drop Direction: **1 corner / 3 edg	es / 6 sides each 1time					



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

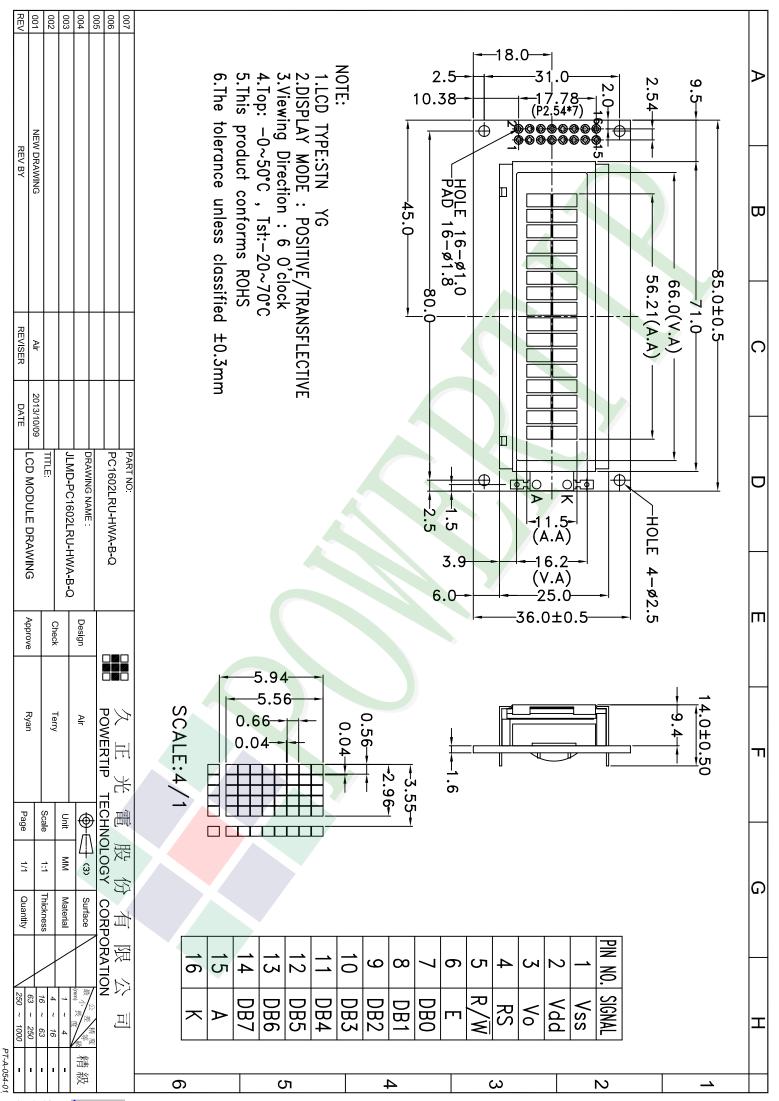
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Ver.001

Documents NO. JPKG-PC1602LRU-HWA-B-Q

LCM包裝規格書 LCM Packaging Specifications

Approve	Check	Contact	
Ryan	Terry	Air	

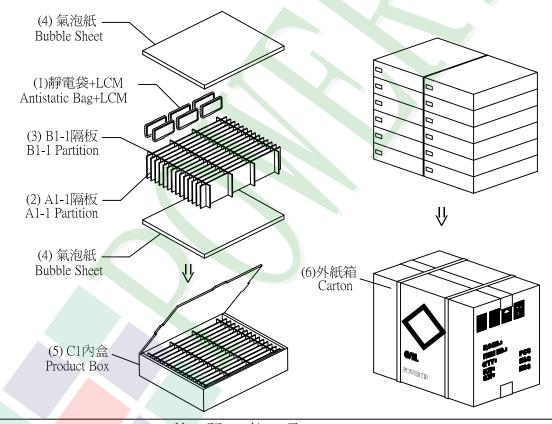
1.包裝材料規格表 (Packaging Material): (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	PC1602LRU-HWA-B-Q	85.0 X 36.0 X 14.0	0.036	432	15.552
2	靜電袋(1)Antistatic Bag	BAG100100ARABA	100 X 100	0.0011	432	0.4752
3	A1-1隔板(2)A1-1 Partition	BX29500047BZBA	295 X 47 X 3	0.0078	168	1.3104
4	B1-1隔板(3)B1-1 Partition	BX24500047BZBA	245 X 47 X 3	0.0065	48	0.312
5	氣泡紙(4)Bubble Sheet	BAG280240BWABA	280 X 240	0.006	24	0.144
6	C1內盒(5)Product Box	BX31025555AABA	310 X 255 X 55	0.13	12	1.56
7	外紙箱(6)Carton	BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
8						
9						

- 2.一整箱總重量 (Total LCD Weight in carton): 20.18 Kg±10%
- 3.單箱數量規格表 (Packaging Specifications and Quantity):

(1)Quantity Of Spacer: A1-1隔板 X 14, B1-1隔板 X

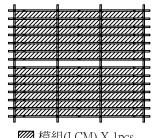
(2)Total LCM quantity in carton: quantity per box x no of boxes 432 12



特 記 事 項 (REMARK)

4. Label Specifications: 標籤依廠內標準作業

- 5. LCM排放示意圖(前,中,后後間隔不放置)
- 5. LCM placed as figure showing:
- (First middle and last slot should be empty)



類模組(LCM) X 1pcs.