# GD50MPS12H
## 1200V 50A SiC Schottky MPS™ Diode

**GeneSiC™**
SEMICONDUCTOR

### Silicon Carbide Schottky Diode

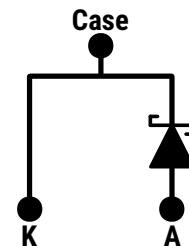| | | |
|---|---|---|
| $V_{RRM}$ | = | 1200 V |
| $I_{F\,(T_C = 144°C)}$ = | | 50 A |
| $Q_C$ | = | 162 nC |

## Features

- Gen4 Thin Chip Technology for Low $V_F$
- Superior Figure of Merit $Q_C * V_F$
- 100% Avalanche (UIL) Tested
- Enhanced Surge Current Withstand Capability
- Temperature Independent Fast Switching
- Low Thermal Resistance
- Positive Temperature Coefficient of $V_F$
- High dV/dt Ruggedness

## Package

Case(K)

**Case**

**TO-247-2**

K    A

RoHS ✔

REACH ✔

## Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

## Applications

- Electric Vehicles and Fast Chargers
- Solar Inverters
- Train Auxiliary Power Supplies
- High frequency Converters
- Motor Drives
- Induction Heating and Welding
- Uninterruptible Power Supplies
- Pulsed Power

## Absolute Maximum Ratings (At $T_C$ = 25°C Unless Otherwise Stated)

| Parameter | Symbol | Conditions | Values | Unit | Note |
|---|---|---|---|---|---|
| Repetitive Peak Reverse Voltage | $V_{RRM}$ | | 1200 | V | |
| Continuous Forward Current | $I_F$ | $T_C$ = 100°C, D = 1 | 86 | A | Fig. 4 |
| | | $T_C$ = 135°C, D = 1 | 59 | | |
| | | $T_C$ = 144°C, D = 1 | 50 | | |
| Non-Repetitive Peak Forward Surge Current, Half Sine Wave | $I_{F,SM}$ | $T_C$ = 25°C, $t_P$ = 10 ms | 400 | A | |
| | | $T_C$ = 150°C, $t_P$ = 10 ms | 320 | | |
| Repetitive Peak Forward Surge Current, Half Sine Wave | $I_{F,RM}$ | $T_C$ = 25°C, $t_P$ = 10 ms | 240 | A | |
| | | $T_C$ = 150°C, $t_P$ = 10 ms | 168 | | |
| Non-Repetitive Peak Forward Surge Current | $I_{F,MAX}$ | $T_C$ = 25°C, $t_P$ = 10 µs | 2000 | A | |
| $i^2t$ Value | $\int i^2 dt$ | $T_C$ = 25°C, $t_P$ = 10 ms | 800 | $A^2s$ | |
| Non-Repetitive Avalanche Energy | $E_{AS}$ | L = 0.4 mH, $I_{AS}$ = 50 A | 452 | mJ | |
| Diode Ruggedness | dV/dt | $V_R$ = 0 ~ 960 V | 200 | V/ns | |
| Power Dissipation | $P_{TOT}$ | $T_C$ = 25°C | 463 | W | Fig. 3 |
| Operating and Storage Temperature | $T_j$ , $T_{stg}$ | | -55 to 175 | °C | |

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## Electrical Characteristics

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit | Note |
|---|---|---|---|---|---|---|---|
| Diode Forward Voltage | $V_F$ | $I_F$ = 50 A, $T_j$ = 25°C | | 1.5 | 1.8 | V | Fig. 1 |
| | | $I_F$ = 50 A, $T_j$ = 175°C | | 1.9 | | | |
| Reverse Current | $I_R$ | $V_R$ = 1200 V, $T_j$ = 25°C | | 3 | 30 | µA | Fig. 2 |
| | | $V_R$ = 1200 V, $T_j$ = 175°C | | 33 | | | |
| Total Capacitive Charge | $Q_C$ | $I_F \leq I_{F,MAX}$ dl$_F$/dt = 200 A/µs $V_R$ = 400 V | | 111 | | nC | Fig. 7 |
| | | $V_R$ = 800 V | | 162 | | | |
| Switching Time | $t_S$ | $V_R$ = 400 V | | < 10 | | ns | |
| | | $V_R$ = 800 V | | | | | |
| Total Capacitance | C | $V_R$ = 1 V, f = 1MHz | | 1842 | | pF | Fig. 6 |
| | | $V_R$ = 800 V, f = 1MHz | | 108 | | | |

## Thermal/Package Characteristics

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit | Note |
|---|---|---|---|---|---|---|---|
| Thermal Resistance, Junction - Case | $R_{thJC}$ | | | 0.32 | | °C/W | Fig. 9 |
| Weight | $W_T$ | | | 6.0 | | g | |
| Mounting Torque | $T_M$ | Screws to Heatsink | | | 1.1 | Nm | |

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## Figure 1: Typical Forward Characteristics



175°C
150°C
125°C
75°C
25°C

Forward Current, $I_F$ (A) vs Forward Voltage, $V_F$ (V)
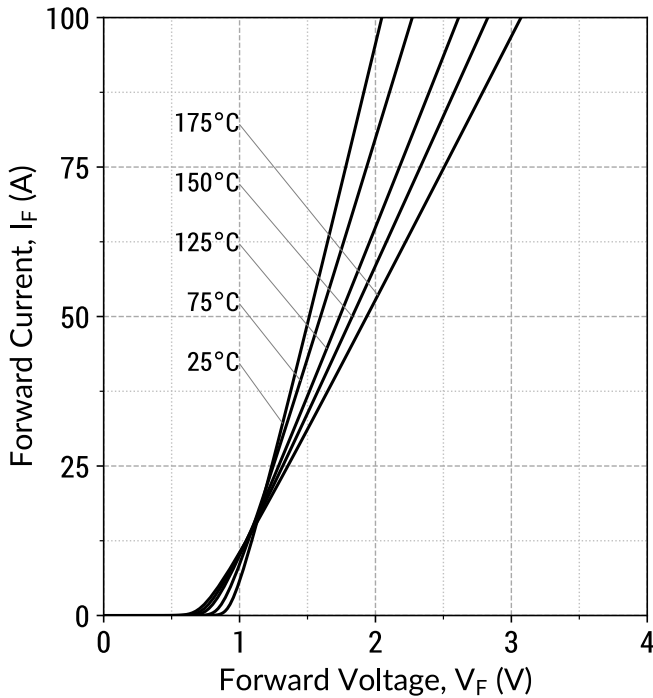
$I_F = f(V_F, T_j)$; $t_P$ = 250 μs

## Figure 2: Typical Reverse Characteristics



175°C
150°C
125°C
75°C
25°C

Reverse Current, $I_R$ (A) vs Reverse Voltage, $V_R$ (V)
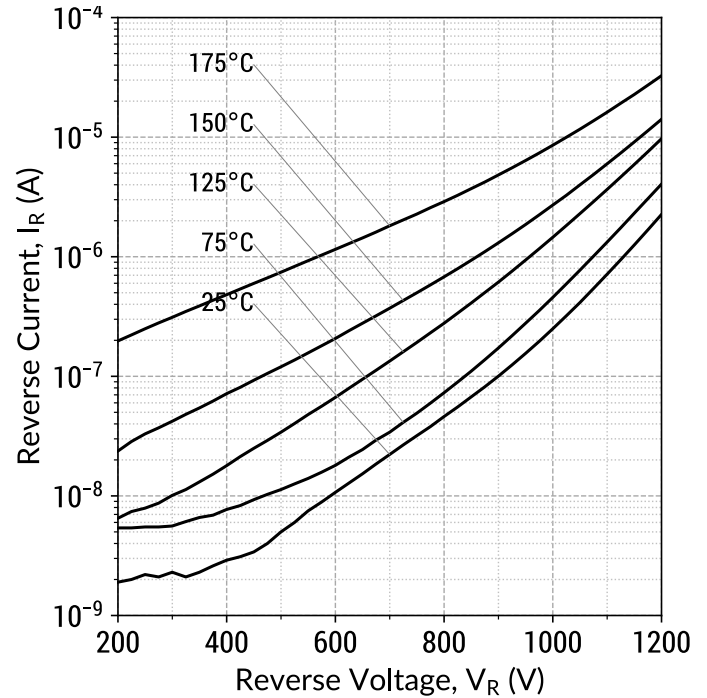
$I_R = f(V_R, T_j)$

## Figure 3: Power Derating Curves



Power Dissipated (W) vs Case Temperature, $T_C$ (°C)
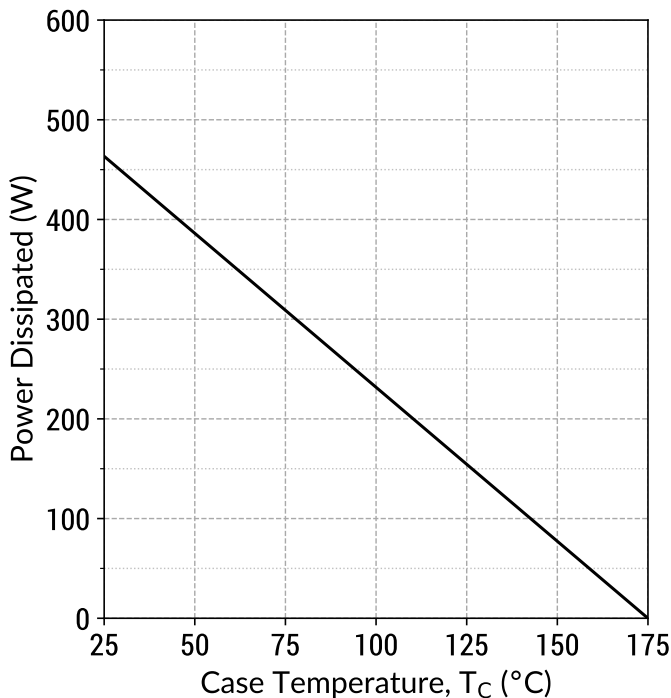
$P_{TOT} = f(T_C)$; $T_j$ = 175°C

## Figure 4: Current Derating Curves (Typical $V_F$)



D = 0.1
D = 0.2
D = 0.3
D = 0.5
D = 0.7
D = 1

Current Rating (A) vs Case Temperature, $T_C$ (°C)

$I_F = f(T_C)$; D = $t_P/T$; $T_j$ ≤ 175°C; $f_{SW}$ > 10kHz

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## Figure 5: Current Derating Curves (Maximum $V_F$)
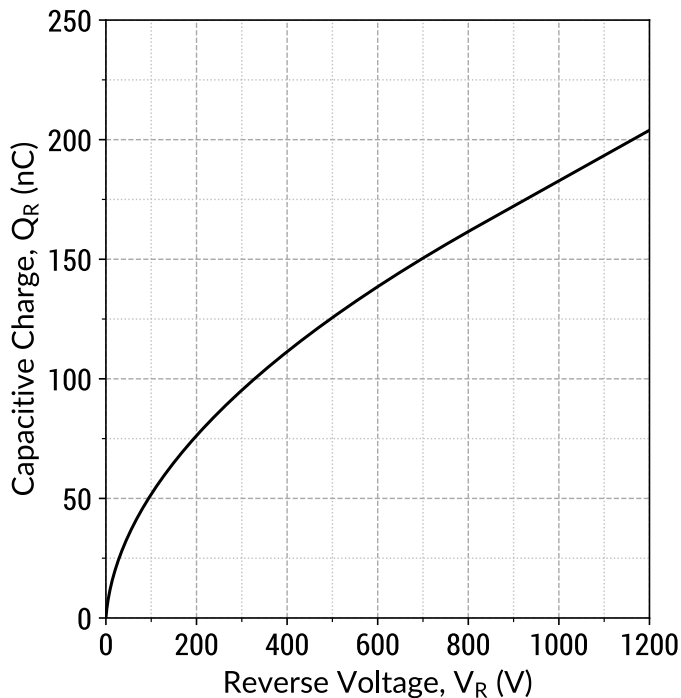


D = 0.1
D = 0.2
D = 0.3
D = 0.5
D = 0.7
D = 1
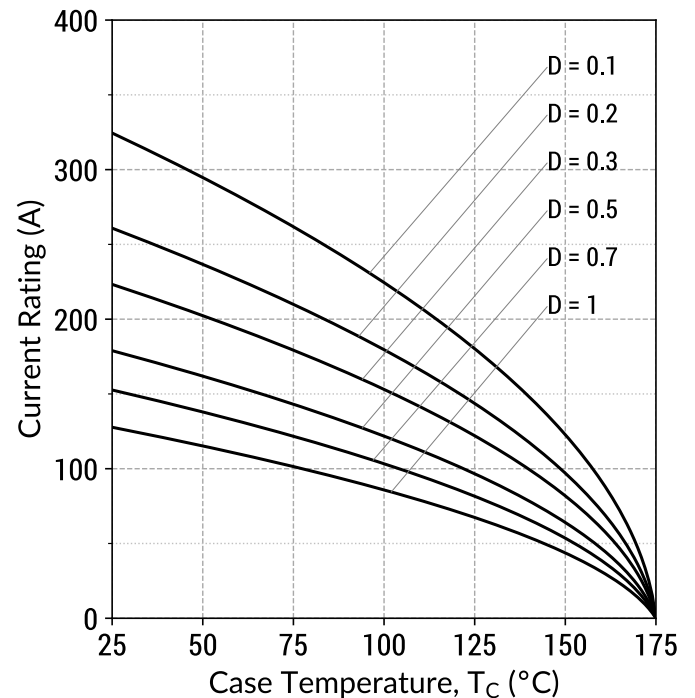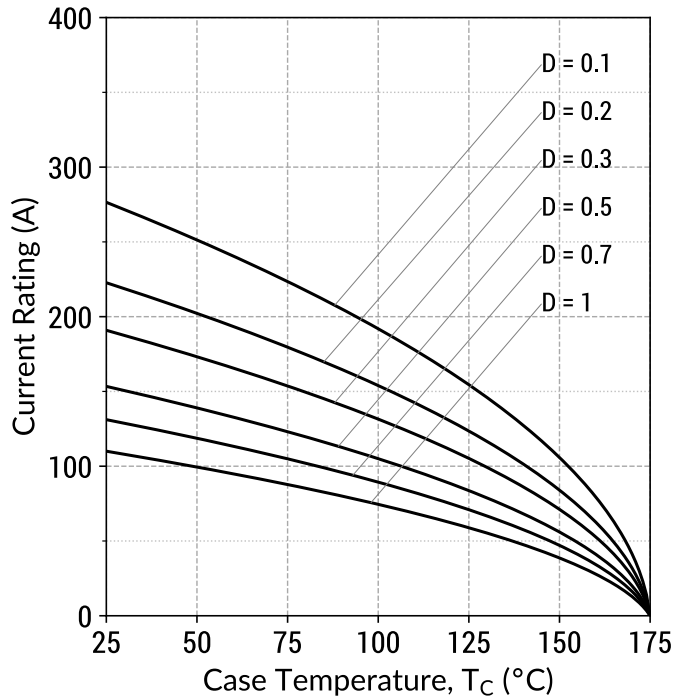
$I_F = f(T_C); D = t_P/T; T_j \leq 175°C; f_{SW} > 10kHz$

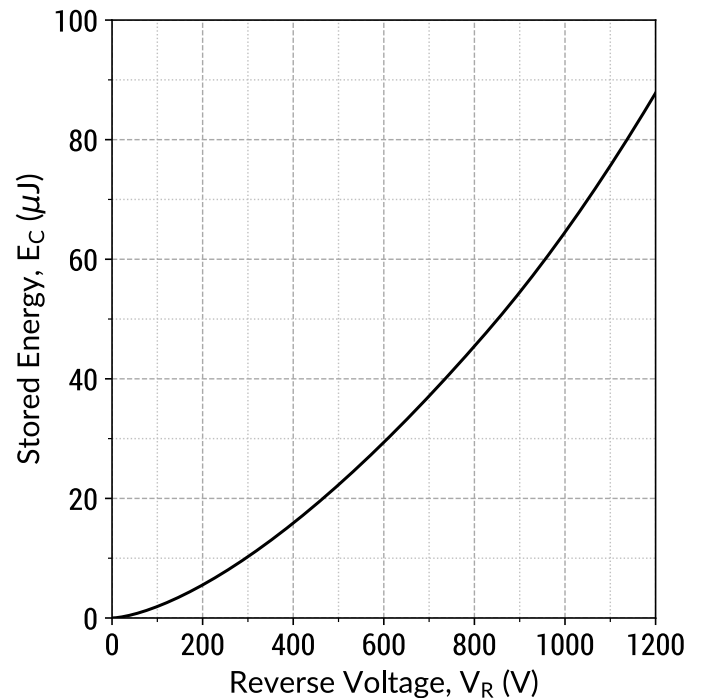## Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics
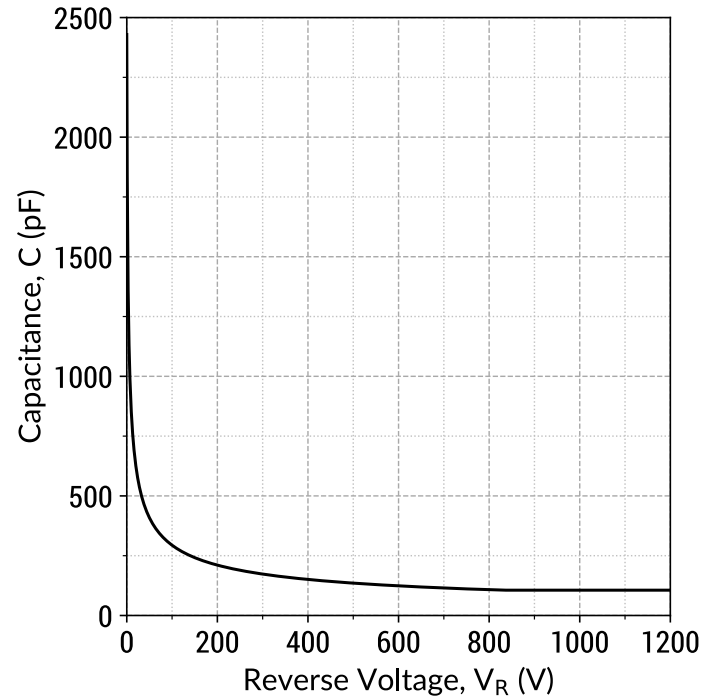


$C = f(V_R); f = 1MHz$

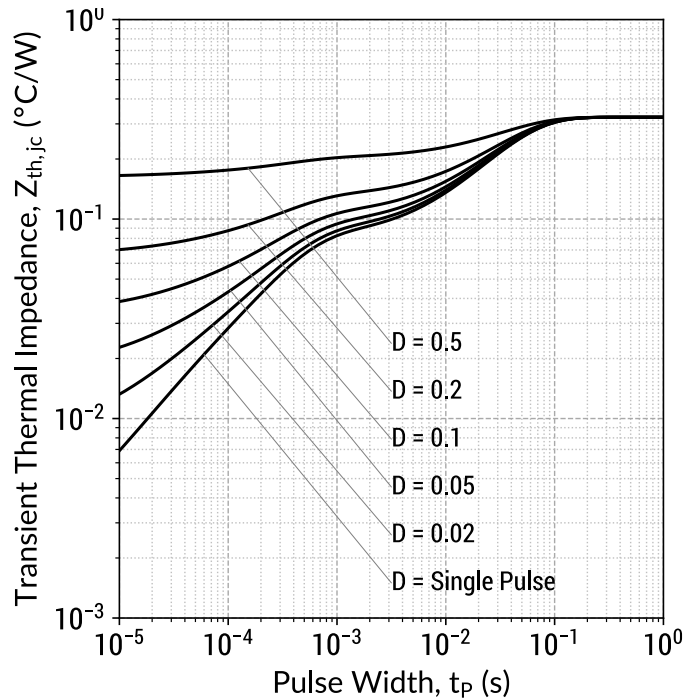## Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics



$Q_C = f(V_R); f = 1MHz$

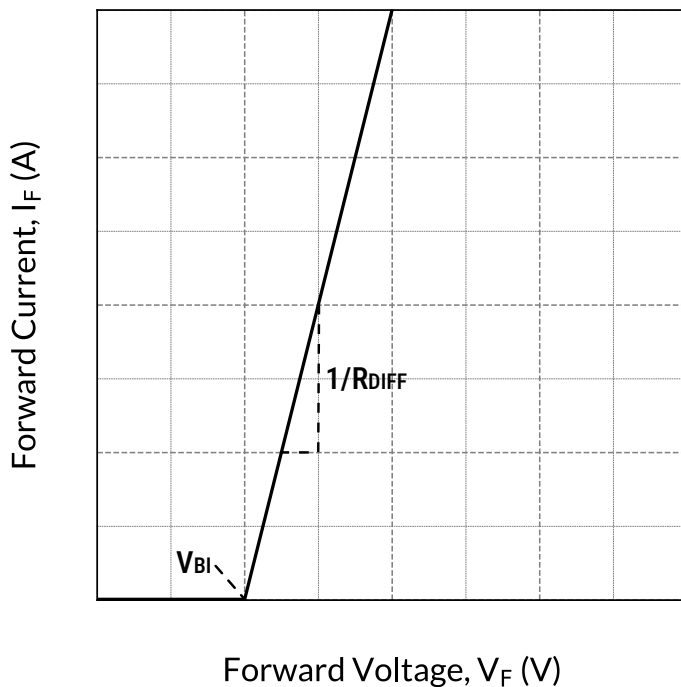## Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics



$E_C = f(V_R); f = 1MHz$

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## Figure 9: Transient Thermal Impedance



D = 0.5
D = 0.2
D = 0.1
D = 0.05
D = 0.02
D = Single Pulse

$Z_{th,jc} = f(t_P, D); D = t_P/T$

## Figure 10: Forward Curve Model



$1/R_{DIFF}$

$V_{BI}$

$I_F = f(V_F, T_j)$

### Forward Curve Model Equation:

$I_F = (V_F - V_{BI})/R_{DIFF}$ (A)

#### Built-In Voltage ($V_{BI}$):

$V_{BI}(T_j) = m \times T_j + n$ (V)

$m = -0.00119$ (V/°C)

$n = 1.01$ (V)

#### Differential Resistance ($R_{DIFF}$):

$R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c$ (Ω)

$a = 2.37e\text{-}07$ (Ω/°C²)

$b = 3.29e\text{-}05$ (Ω/°C)

$c = 0.00976$ (Ω)

### Forward Power Loss Equation:

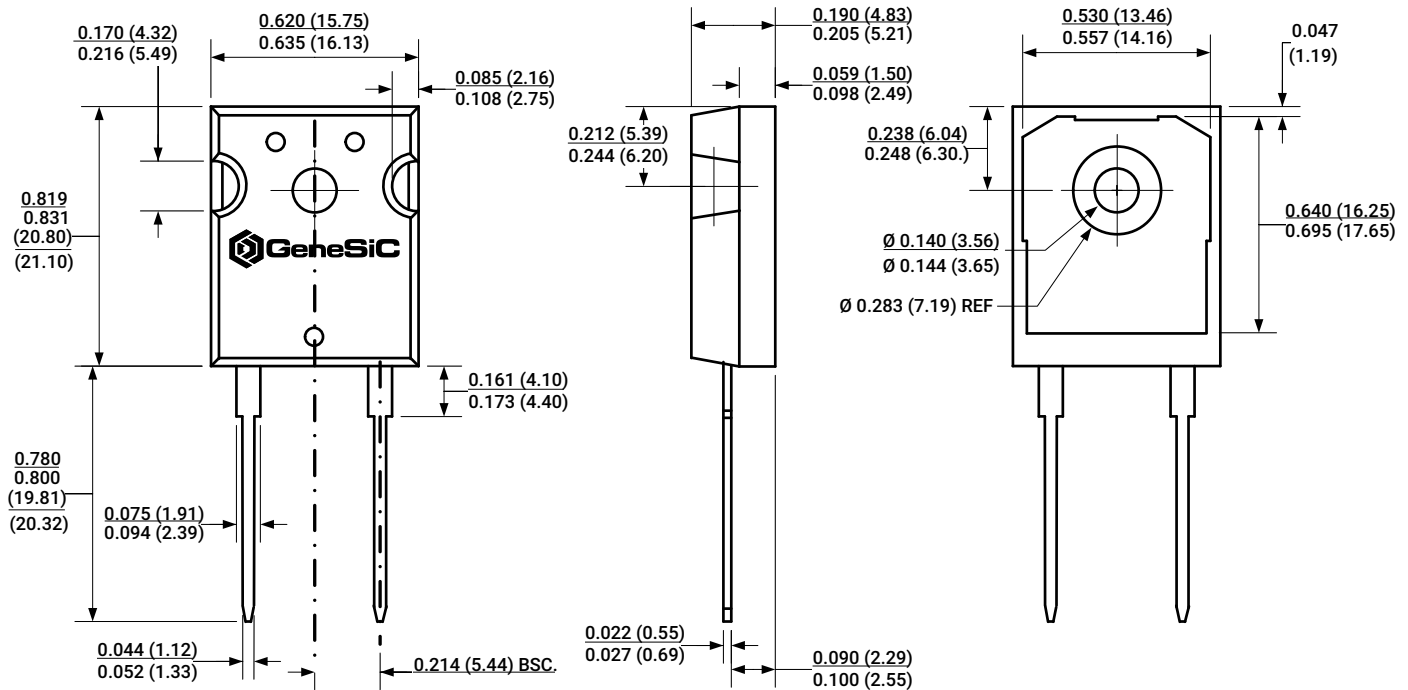$P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$

## Package Dimensions

### TO-247-2 Package Outline



0.170 (4.32) / 0.216 (5.49)

0.620 (15.75) / 0.635 (16.13)

0.085 (2.16) / 0.108 (2.75)

0.819 0.831 (20.80) (21.10)

0.161 (4.10) / 0.173 (4.40)

0.780 0.800 (19.81) (20.32)

0.075 (1.91) / 0.094 (2.39)

0.044 (1.12) / 0.052 (1.33)

0.214 (5.44) BSC.

0.190 (4.83) / 0.205 (5.21)

0.059 (1.50) / 0.098 (2.49)

0.212 (5.39) / 0.244 (6.20)

0.022 (0.55) / 0.027 (0.69)

0.090 (2.29) / 0.100 (2.55)

0.530 (13.46) / 0.557 (14.16)

0.047 (1.19)

0.238 (6.04) / 0.248 (6.30.)

Ø 0.140 (3.56) / Ø 0.144 (3.65)

Ø 0.283 (7.19) REF

0.640 (16.25) / 0.695 (17.65)

### Recommended Solder Pad Layout



0.120 (3.05)

0.428 (10.88)

0.08 (2.03)

### Package View



Case (K)

K    A
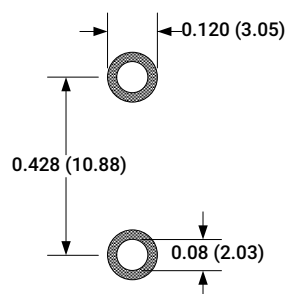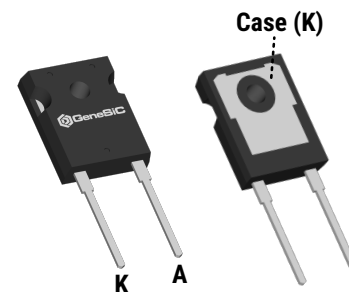
## NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.

## Compliance

**RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

**REACH Compliance**

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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## Related Links

- SPICE Models: https://www.genesicsemi.com/sic-schottky-mps/GD50MPS12H/GD50MPS12H_SPICE.zip
- PLECS Models: https://www.genesicsemi.com/sic-schottky-mps/GD50MPS12H/GD50MPS12H_PLECS.zip
- CAD Models: https://www.genesicsemi.com/sic-schottky-mps/GD50MPS12H/GD50MPS12H_3D.zip
- Evaluation Boards: https://www.genesicsemi.com/technical-support
- Reliability: https://www.genesicsemi.com/reliability
- Compliance: https://www.genesicsemi.com/compliance
- Quality Manual: https://www.genesicsemi.com/quality

## Revision History

- Rev 21/Jul: Updated with most recent test data
- Supersedes: Rev 20/Jul

**www.genesicsemi.com/sic-schottky-mps/**

**Rev 21/Jul**
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