

PRODUCT SUMMARY

SKY13581-676LF: 0.4 to 3.8 GHz SPDT Switch

Applications

- Cellular systems
- 3G/4G LTE systems
- Pre- and post-PA switching

Features

- Broadband frequency range: 0.4 to 3.8 GHz
- Very low insertion loss
- High isolation: 25 dB at 2.7 GHz
- Integrated GPIO interface
- No DC blocking capacitors required
- Lead (Pb)-free and RoHS-compliant
- Small QFN (9-pin, 1.1 × 1.1 × 0.5 mm) package (MSL1, 260 °C per JEDEC J-STD-020)



Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.

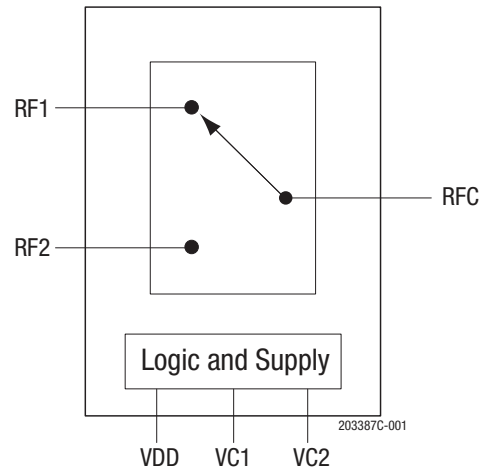


Figure 1. SKY13581-676LF Block Diagram

Description

The SKY13581-676LF is a single-pole, double-throw (SPDT) switch. No external DC blocking capacitors are required on the RF paths as long as there is no DC voltage on the RF line. The switch can operate over the temperature range of -40 °C to +85 °C.

Switching is controlled by two CMOS/TTL compatible control voltage inputs (VC1 and VC2). Depending on the logic voltage level applied to the control pin, the RFC pin is connected to one of two switched RF outputs (RF1 or RF2) by using a low insertion loss path, while the path between the RFC pin and the other RF pin is in isolation.

The SKY13581-676LF is provided in a small 9-pin, 1.1 × 1.1 × 0.5 mm QFN package.

A functional block diagram is shown in Figure 1.

Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number
SKY13581-676LF: SPDT Switch	SKY13581-676LF	EN37-D665-001

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