

Press Pack IEGT Silicon N-Channel IEGT

# ST1500GXH24

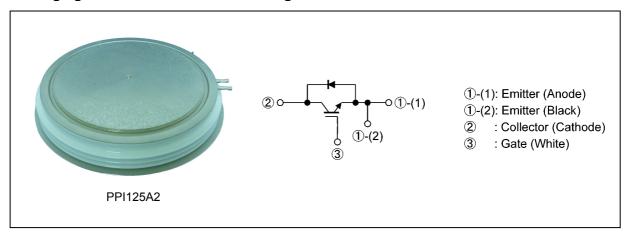
# 1. Applications

- · High-Power Switching
- · Motor Controllers

### 2. Features

- (1) Enhancement mode.
- (2) Double side cooling type.

# 3. Packaging and Internal Circuit Pin Assignment



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# 4. Absolute Maximum Ratings (Note) (T<sub>c</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Collector-emitter voltage	V <sub>CES</sub>			4500	V
Gate-emitter voltage	V <sub>GES</sub>			±20	V
Collector current (DC)	Ic	(Note 2)		1500	Α
Collector current (pulsed)	I <sub>CP</sub>	(Note 3)		3000	Α
Forward current (DC)	I <sub>F</sub>			1500	Α
Forward current (pulsed)	I <sub>FP</sub>	(Note 3)		3000	Α
Non-repetitive peak forward surge current	I <sub>FSM</sub>		10 ms half-sine wave	10	kA
Collector power dissipation	Pc	(Note 1)	T <sub>f</sub> = 25°C	14285	W
Power dissipation	$P_{D}$	(Note 1)	Diode part, T <sub>f</sub> = 25°C 5555		W
Junction temperature	Tj			-40 to 125	°C
Storage temperature	T <sub>stg</sub>			-40 to 125	°C
Mounting force				50 to 70	kN

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: In addition to the above remarks, refer to the application notes.

Note 2:  $T_f = 80^{\circ}C$ 

Note 3: Pulse width and repetition rate should be such that junction temperature  $(T_j)$  does not exceed maximum  $T_j$  rating.

#### 5. Thermal Characteristics

Characteristics		Symbol	Note	Test Condition	Max	Unit
Thermal resistance (junction-to-fin)	Transistor part	R <sub>th(j-f)</sub> (Note 1) Emitter side		Emitter side	19	K/kW
				Collector side	12	K/kW
				Double side	7	K/kW
	Diode part			Anode side(Emitter side)	49	K/kW
				Cathode side(Collector side)	29	K/kW
				Double side	18	K/kW

Note 1: Conductive thermal compound is added.

Customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this doucument, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for.



# 6. Electrical Characteristics (T<sub>C</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate-emitter leakage current	I <sub>GES</sub>	V <sub>GE</sub> = ±20 V, V <sub>CE</sub> = 0 V		_	±20	nA
Collector-emitter cut-off current	I <sub>CES</sub>	$V_{CE} = 4500 \text{ V}, V_{GE} = 0 \text{ V},$ $T_j = 125^{\circ}\text{C}$	_	_	100	mA
Gate-emitter cut-off voltage	V <sub>GE(off)</sub>	I <sub>C</sub> = 1.5 A, V <sub>CE</sub> = 10 V	6.5	7.5	8.5	V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 1500 A, V <sub>GE</sub> = 15 V, T <sub>j</sub> = 125°C		3.0	4.0	V
Input capacitance	C <sub>ies</sub>	$V_{CE} = 10 \text{ V}, V_{GE} = 0 \text{ V},$ f = 100 kHz		320	_	nF
Switching time (turn-on delay time)	t <sub>d(on)</sub>	V <sub>CC</sub> = 2700 V, I <sub>C</sub> = 1500 A,		0.6	_	μS
Switching time (rise time)	t <sub>r</sub>	$V_{GE} = \pm 15 \text{ V}, R_{G(on)} = 5.1\Omega,$ $R_{G(off)} = 10\Omega, T_i = 125^{\circ}\text{C}$		0.4	_	μS
Switching time (turn-on time)	t <sub>on</sub>	(Inductive load, $L_s \approx 330 \text{ nH}$ )		1.0	_	μS
Switching time (turn-off delay time)	t <sub>d(off)</sub>	See Fig. 6.1 and Fig. 6.2.		7.5	_	μS
Switching time (fall time)	t <sub>f</sub>			2.5	_	μS
Switching time (turn-off time)	t <sub>off</sub>		_	10	_	μS
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 1500 A, T <sub>j</sub> = 125°C	_	3.2	4.2	V
Peak reverse recovery current	Irr	V <sub>CC</sub> = 2700 V, I <sub>F</sub> = 1500 A, V <sub>GE</sub> = -15 V, T <sub>j</sub> = 125°C,	_	1500	_	Α
Reverse recovery time	t <sub>rr</sub>	Drive side: $V_{GE}$ = ±15V, $R_{G(on)}$ = 5.1 $\Omega$ (Inductive load, $L_s$ $\approx$ 330 nH) See Fig. 6.1 and Fig. 6.3.	_	1.3	_	μS
Turn-on switching loss	E <sub>on</sub>	$V_{CC}$ = 2700 V, $I_{C}$ = 1500 A, $V_{GE}$ = ±15 V, $R_{G(on)}$ = 5.1 $\Omega$ ,	_	8.0	_	J
Turn-off switching loss	E <sub>off</sub>	$R_{G(off)} = 10\Omega$ , $T_j = 125^{\circ}C$ (Inductive load, $L_s \approx 330$ nH) See Fig. 6.1 and Fig. 6.2.	_	7.0	_	J
Reverse recovery loss	E <sub>rr</sub>	$\begin{split} &V_{CC} = 2700 \text{ V, I}_F = 1500 \text{ A,} \\ &V_{GE} = \text{-}15 \text{ V, T}_j = 125^{\circ}\text{C,} \\ &\text{Drive side:} V_{GE} = \pm 15\text{V, R}_{G(on)} = 5.1\Omega \\ &\text{(Inductive load, L}_s \approx 330 \text{ nH)} \\ &\text{See Fig. 6.1 and Fig. 6.3.} \end{split}$	_	3.5	_	J
Short-circuit pulse width	t <sub>psc</sub>	$V_{CC}$ = 3000 V, $V_{GE}$ = ±15 V, $R_{G(on)}$ = 5.1 $\Omega$ , $R_{G(off)}$ = 10 $\Omega$ , $T_{j}$ = 125°C	_	_	10	μѕ

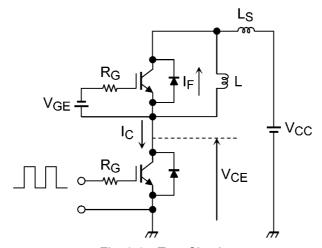


Fig. 6.1 Test Circuit

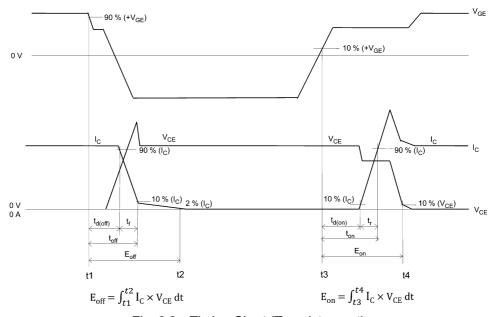


Fig. 6.2 Timing Chart (Transistor part)

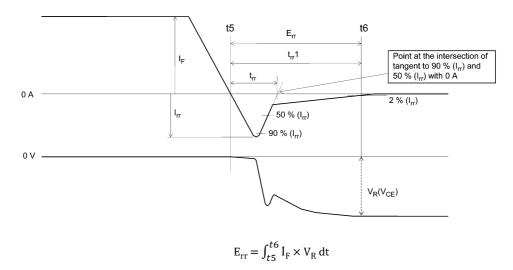


Fig. 6.3 Timing Chart (Diode part)



# 7. Characteristics Curves (Note)

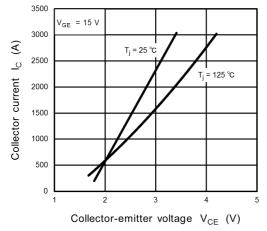


Fig. 7.1 Ic - VCE

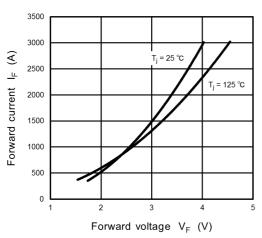


Fig. 7.3 I<sub>F</sub> - V<sub>F</sub>

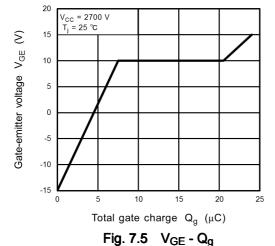


Fig. 7.2 I<sub>C</sub> - V<sub>CE</sub>

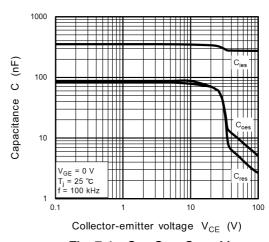


Fig. 7.4 Cies, Coes, Cres - VCE

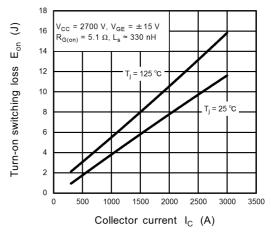


Fig. 7.6 E<sub>on</sub> - I<sub>C</sub>

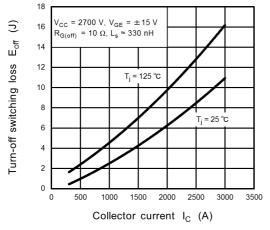


Fig. 7.7 Eoff - IC

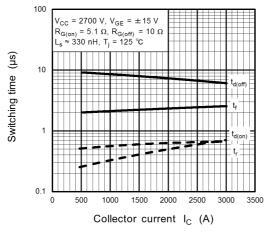


Fig. 7.9  $t_{d(off)}$ , $t_f$ , $t_{d(on)}$ , $t_r$  -  $I_C$ 

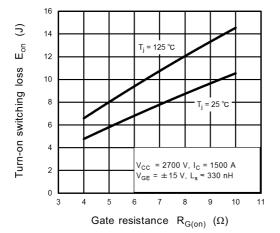


Fig. 7.11 Eon - R<sub>G(on)</sub>

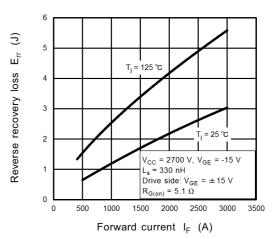


Fig. 7.8 E<sub>rr</sub> - I<sub>F</sub>

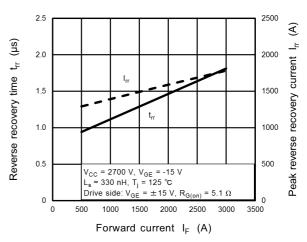


Fig. 7.10 t<sub>rr</sub>, l<sub>rr</sub> - l<sub>F</sub>

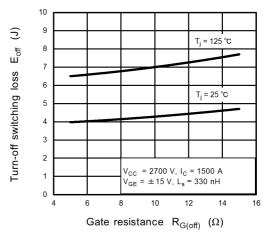


Fig. 7.12 E<sub>off</sub> - R<sub>G(off)</sub>

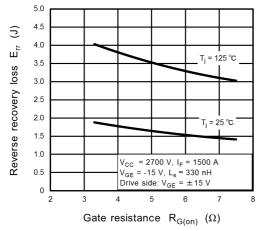


Fig. 7.13 E<sub>rr</sub> - R<sub>G(on)</sub>

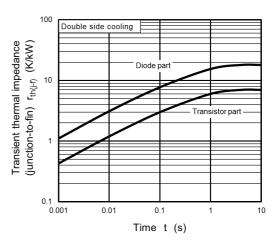


Fig. 7.14  $r_{th(j-f)} - t$  (Guaranteed value)

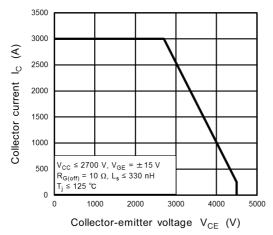


Fig. 7.15 RBSOA (Guaranteed value)

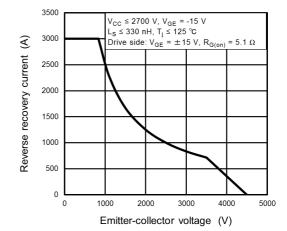


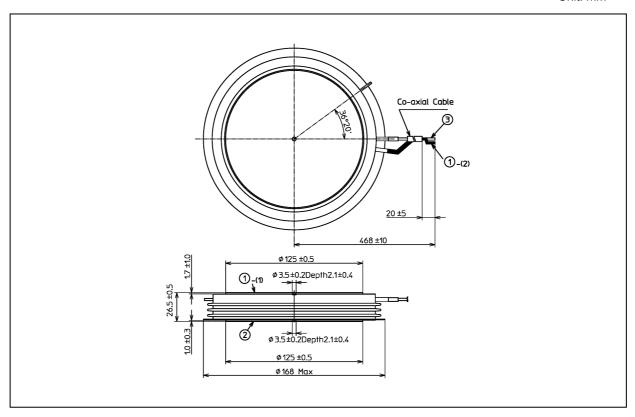
Fig. 7.16 RRSOA (Guaranteed value)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



# **Package Dimensions**

Unit: mm



Weight: 3500 g (typ.)

	Package Name(s)
TOSHIBA: 2-168A2S	
Nickname: PPI125A2	



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