R5F10DMEJFB

User's Manual: Hardware

Product Info | Design Support | Documentation | Sample Code | Software & Tools | Ordering

Product Spec

Basic Information Memory CPU (Bit, Clock, DMA, External Bus etc) Timers Analog Interfaces Display Functions Operating Conditions Remarks Package

Basic Information	
Product Status	Mass Production
Family	RL78
Series	RL78/D1x
Group	RL78/D1A

Memory	
Program memory	64 KB Flash memory
RAM	4 KB
Data Flash	8 KB

CPU (Bit, Clock, DMA, External Bus etc)	
CPU	RL78 (16-bit)
Max. Frequency	32 MHz
Sub-Clock (32.768 kHz)	YES
On-chip Oscillator Freq.	32M, 24M, 16M, 12M, 8M, 4M Low-speed Oscillator 15kHz
PLL	YES
Real-Time Clock (RTC)	YES
Power-On Reset	YES
Low Voltage Detection	YES
DMA Remarks	DMAC x 2 ch
External Interrupt Pins	8
I/O Ports	68

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16-bit Timers	24 ch
Watchdog Timers	1 ch
Other Timers	Interval Timer x 1 ch, Wakeup Timer x 1ch
PWM Output	21

Analog

A/D Converters	10-bit x 8 ch

Interfaces

CAN	1 ch
CSIs	2 ch
UARTs	2 ch
I2Cs	1 ch
LIN	2 ch

Display Functions

Segment LCD controller/driver	48

Operating Conditions

Operating Voltage	2.7 to 5.5 V
Operating Temperature	-40 to 85 ℃ (Ambient Temperature)

Remarks

Remarks	Automotive

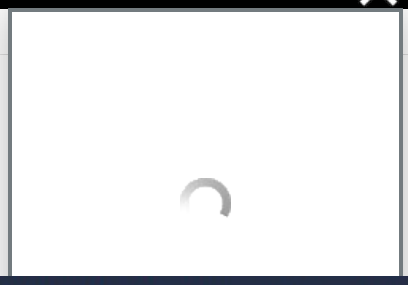
Package

Pins	80
Terminal pitch (mm)	0.5 mm
Package Type	LFQFP
Dimensions (mm)	12x12 mm^2
Drawing Link	plqp0080ke-a(RL78)(including packing specifications)
Mount pad	fig0009e
Mounting height (mm)[MAX]	1.6 mm
Mass (g) [TYP,]	0.53 g
Renesas code	PLQP0080KE-A (old: P80GK-50-8EU-2(RL78))
Remarks	LQFP(matrix line)
Packing	Tray/Embosed tape
Terminal material - Base	Cu alloy
Terminal material - Surface	Sn-Bi or Sn

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