

DFLS140L

#### 1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

### **Product Summary**

V <sub>R</sub> (V)	I <sub>F</sub> (A)	V <sub>F MAX</sub> (V) @1A (+25°C)	I <sub>R MAX</sub> (mA) @40V (+25°C)
40	1	0.55	0.1

## **Description and Applications**

This Schottky barrier rectifier has been designed to meet the stringent requirements of automotive applications. It is ideally suited to use as:

- Polarity Protection Diode
- · Re-Circulating Diode
- Switching Diode

#### **Features and Benefits**

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability and Low Forward Voltage Drop
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Qsuffix) part. A listing can be found at <a href="https://www.diodes.com/products/automotive/automotive-products/">https://www.diodes.com/products/automotive/automotive-products/</a>.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (DFLS140LQ)

### **Mechanical Data**

- Case: PowerDI<sup>®</sup>123
- Surface Mount Package
- Max Soldering Temperature +260°C for 30 secs as per JEDEC J-STD-020
- Case Material Molded Plastic, UL Flammability Rating 94V-0
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.01 grams (Approximately)



Top View

## Ordering Information (Note 4)

Ī	Part Number	Case	Packaging
	DFLS140L-7	PowerDI123	3000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

## **Marking Information**



F06 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)



Date Code Key

Year	2004		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	R		Н		J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sen	Oct	Nov	Dec
Month Code	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

PowerDI is a registered trademark of Diodes Incorporated.



## **Maximum Ratings** (@TA = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	40	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Forward Current	I <sub>F(AV)</sub>	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	50	А

# **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	550	mW
Power Dissipation (Note 6)	PD	820	mW
Thermal Resistance Junction to Soldering Point (Note 7)	Rejs	10	°C/W
Thermal Resistance Junction to Ambient (Note 5)	RθJA	180	°C/W
Thermal Resistance Junction to Ambient (Note 6)	$R_{ heta JA}$	120	°C/W
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

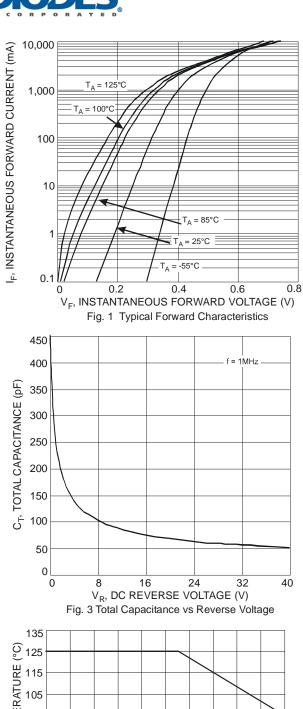
## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

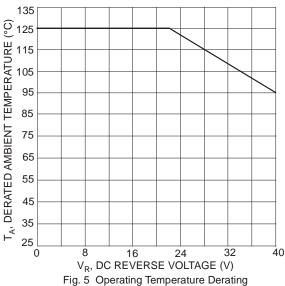
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	$V_{(BR)R}$	40		_	V	$I_R = 500 \mu A$
Forward Voltage	VF			0.36 0.30 0.55 0.515 0.85 0.88	٧	$\begin{split} &  _F = 0.1\text{A}, \ T_J = +25^{\circ}\text{C} \\ &  _F = 0.1\text{A}, \ T_J = +85^{\circ}\text{C} \\ &  _F = 1.0\text{A}, \ T_J = +25^{\circ}\text{C} \\ &  _F = 1.0\text{A}, \ T_J = +85^{\circ}\text{C} \\ &  _F = 3.0\text{A}, \ T_J = +25^{\circ}\text{C} \\ &  _F = 3.0\text{A}, \ T_J = +85^{\circ}\text{C} \\ \end{split}$
Leakage Current (Note 8)	lR	_ _ _	_ _ _	0.1 10 0.05 5	mA	VR = 40V, TJ = +25°C VR = 40V, TJ = +85°C VR = 20V, TJ = +25°C VR = 20V, TJ = +85°C
Total Capacitance	Ст	_	90	_	pF	V <sub>R</sub> = 10V, f = 1.0MHz

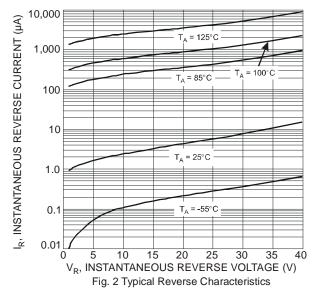
Notes:

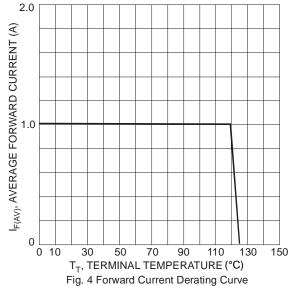
- 5. 1\*MRP FR-4 PC board,2oz.copper PCB board.
- 6. 1inch sq. copper pad, 2oz. PCB board.
- 7. Theoretical  $R_{\theta}$ Js calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- 8. Short duration pulse test to minimize self-heating effect.









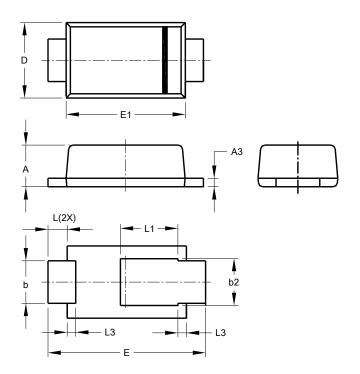




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI123

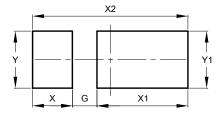


PowerDI123						
Dim	Min	Max	Тур			
Α	0.93	1.00	0.98			
А3	0.15	0.25	0.20			
b	0.85	1.25	1.00			
b2	1.025	1.125	1.10			
D	1.63	1.93	1.78			
Е	3.50	3.90	3.70			
E1	2.60	3.00	2.80			
L	0.40	0.50	0.45			
L1	1.25	1.40	1.35			
L3	0.125	0.275	0.20			
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### PowerDI123



Dimensions	Value		
2	(in mm)		
G	0.65		
X	1.05		
X1	2.40		
X2	4.10		
Y	1.50		
Y1	1.50		



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